AN1339 (AN6912N), AN1339S

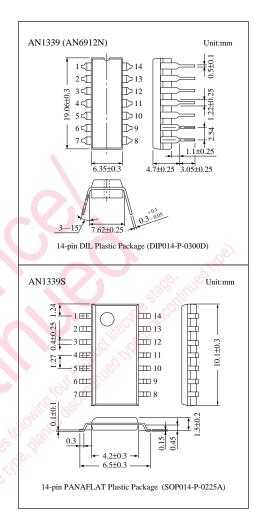
Quadruple Comparators

Overview

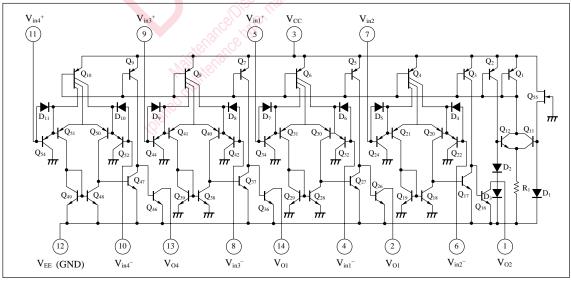
The AN1339 (AN6912N) and the AN1339S are quadruple (voltage) comparators with wide range of operating supply voltages.

■ Features

- Wide range of supply voltage Single supply:2 to 36V Dual supply:±1 to ±18V
- Low circuit current:0.8mA typ.
- Wide range of common-mode input voltages :0V to Vcc-1.5V (single supply)
- Open collector output



■ Block Diagram



■ Pin Descriptions

Pin No.	Pin name	Pin No.	Pin name
1	Ch.2 output	8	Ch.3 inverting input
2	Ch.1 output	9	Ch.3 non inverting input
3	V _{CC}	10	Ch.4 inverting input
4	Ch.1 inverting input	11	Ch.4 non inverting input
5	Ch.1 non inverting input	12	V _{EE} (GND)
6	Ch.2 inverting input	13	Ch.4 output
7	Ch.2 non inverting input	14	Ch.3 output

■ Absolute Maximum Ratings (Ta=25°C)

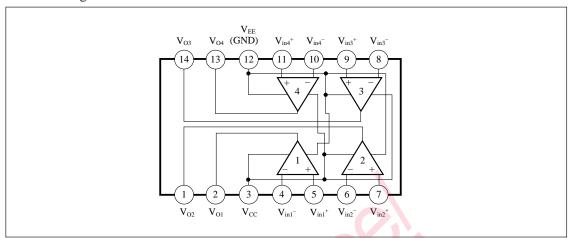
Pa	rameter	Symbol	Rating	Unit	
	Supply voltage	V_{CC}	36	V	
Voltage	Common-mode input voltage	$V_{ICM} *_1$	-0.3 to +36	V	
	Differential input voltage	V _{ID} *2	36	V	
Down dissination	AN1339 (AN6912N)	P_{D}	570	777	
Power dissipation	AN1339S	1 D	380	mW	
Operating ambient	temperature	$T_{ m opr}$	-30 to +85	· · · · · · · · · · · · · · · · · · ·	
Storage	AN1339 (AN6912N)		-55 to +150	illu C	
temperature	AN1339S	T_{stg}	-55 to +125	C	

^{*1} The common mode input voltage is a voltage applied to the non-inverting input pin and inverting input pin simultaneously.
*2 Differential input is equivalent to the potential difference between the non-inverting input pin and inverting input pin.

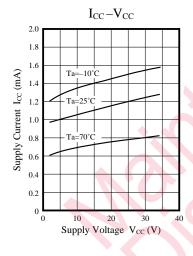
■ Electrical Characteristics (V_{CC}=5V, Ta=25±2°C)

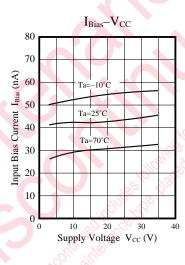
Parameter	Symbol	Condition	min	typ	max	Unit
Input offset voltage	V _{I (offset)}	"Chilly of dis		2	5	mV
Input offset current	I _{IO}	following the same of the same			50	nA
Input bias current	I_{Bias}	11/962 61		_	250	nA
Voltage gain	Gv	$R_L=15k\Omega$		200		V/mV
Common-mode input voltage range	V_{CM}	169, 400	0		V _{CC} -1.5	V
Supply current	I_{CC}	$R_{L}=\infty$		1.1	2	mA
Response time	t _r	$R_L=5.1k\Omega$, $V_{RL}=5V$		1.3		μs
Output sink current	I _{SINK}	$V_{REF}=0V, V_{IN}=1V, V_{O} \le 1.5V$	10			mA
Low-level output voltage	V _{OL}	V_{REF} =0V, V_{IN} =1V, I_{SINK} =3mA		0.2	0.4	V
Output terminal leakage current	I _{O (Leak)}	$V_{IN}=0V, V_{REF}=1V, V_{O}=5V$		0.1		nA

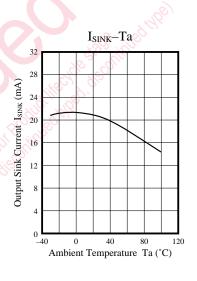
■ Block Diagram

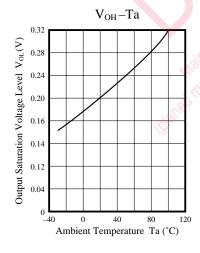


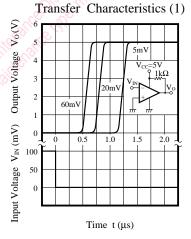
■ Characteristics Curve

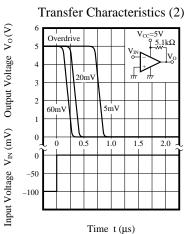












Request for your special attention and precautions in using the technical information and semiconductors described in this book

- (1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products, and no license is granted under any intellectual property right or other right owned by our company or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information described in this book.
- (3) The products described in this book are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).

 Consult our sales staff in advance for information on the following applications:
 - Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
 - Any applications other than the standard applications intended.
- (4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
- Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of Matsushita Electric Industrial Co., Ltd.