# RENESAS

# RJK0304DPB

Silicon N Channel Power MOS FET Power Switching

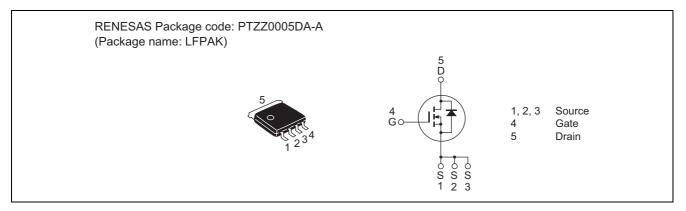
REJ03G1352-0600 Rev.6.00 Apr 19, 2006

# Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)} = 4.0 \text{ m}\Omega \text{ typ.} (at \text{ V}_{GS} = 10 \text{ V})$ 

## Outline



# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	+16/-12	V
Drain current	I <sub>D</sub>	35	A
Drain peak current	Note1 I <sub>D(pulse)</sub>	140	A
Body-drain diode reverse drain current	I <sub>DR</sub>	35	A
Avalanche current	I <sub>AP</sub> Note 2	14	A
Avalanche energy	E <sub>AR</sub> Note 2	19	mJ
Channel dissipation	Pch Note3	50	W
Channel to Case Thermal Resistance	θch-C	2.5	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10 µs, duty cycle  $\leq$  1%

2. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50 \Omega$ 

3. Tc = 25°C



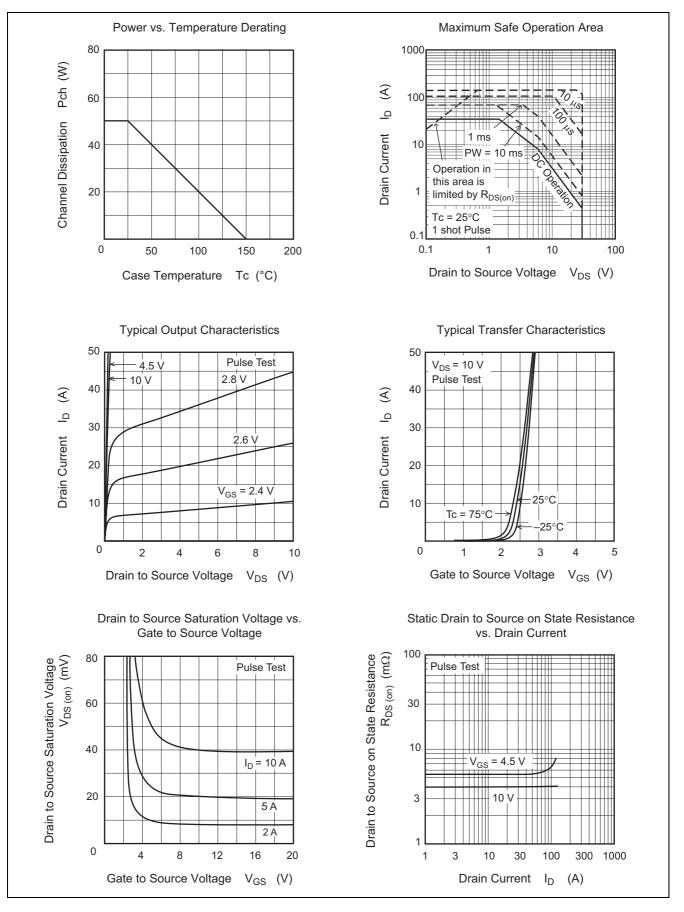
# **Electrical Characteristics**

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	—	_	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>		—	± 0.1	μΑ	$V_{GS} = +16/-12 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	—	1	μΑ	$V_{DS} = 30 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS(on)</sub>	_	4.0	4.8	mΩ	$I_D = 17.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
	R <sub>DS(on)</sub>	_	5.5	7.2	mΩ	$I_D = 17.5 \text{ A}, V_{GS} = 4.5 \text{ V}$ Note4
Forward transfer admittance	y <sub>fs</sub>	—	60	_	S	I <sub>D</sub> = 17.5 A, V <sub>DS</sub> = 10 V <sup>Note4</sup>
Input capacitance	Ciss	_	2500	_	pF	$V_{DS} = 10 V, V_{GS} = 0,$
Output capacitance	Coss	_	850	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss		130		pF	
Gate Resistance	Rg		0.5	_	Ω	
Total gate charge	Qg		17	_	nC	$V_{DD} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$ $I_D = 35 \text{ A}$
Gate to source charge	Qgs		6.7	—	nC	
Gate to drain charge	Qgd	_	3.7	—	nC	
Turn-on delay time	t <sub>d(on)</sub>		8.5	—	ns	
Rise time	tr		3.2	—	ns	
Turn-off delay time	t <sub>d(off)</sub>	_	41	—	ns	
Fall time	t <sub>f</sub>	—	4.0	—	ns	
Body-drain diode forward voltage	V <sub>DF</sub>		0.84	1.10	V	$IF = 35 A, V_{GS} = 0^{Note4}$
Body–drain diode reverse recovery time	t <sub>rr</sub>		35	_	ns	IF = 35 A, V <sub>GS</sub> = 0 di <sub>F</sub> / dt = 100 A/ μs

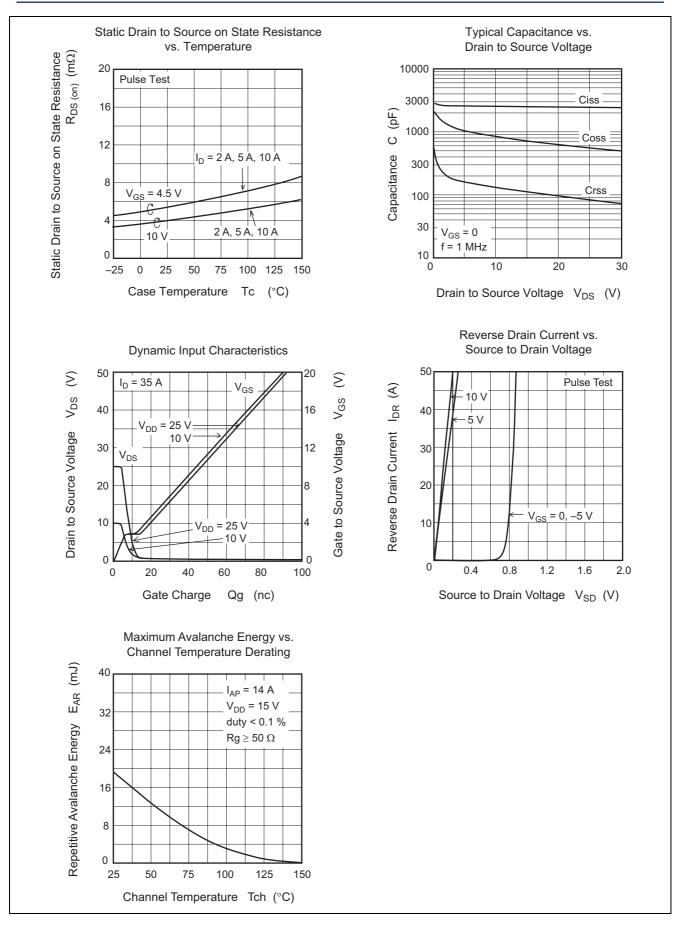
Notes: 4. Pulse test



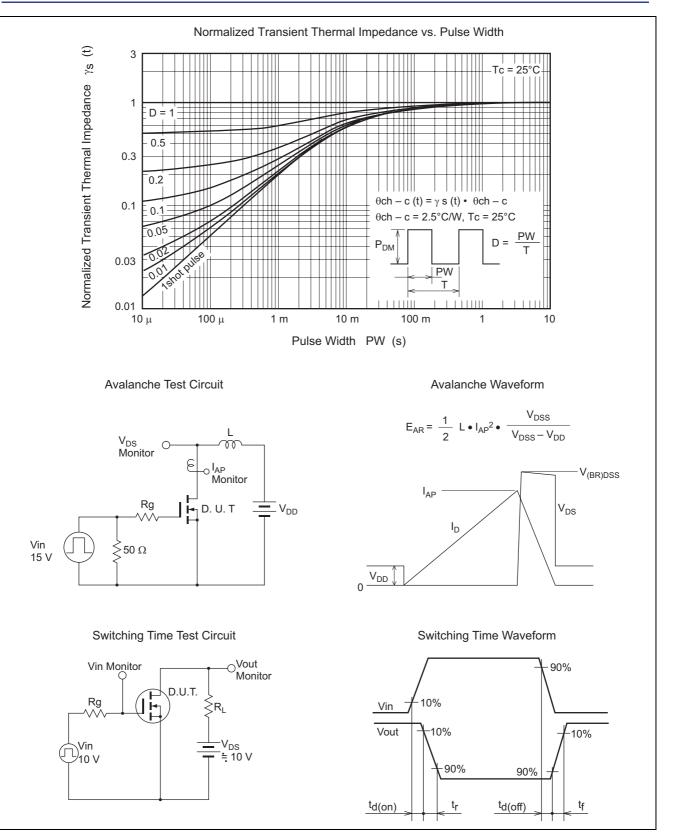
## **Main Characteristics**





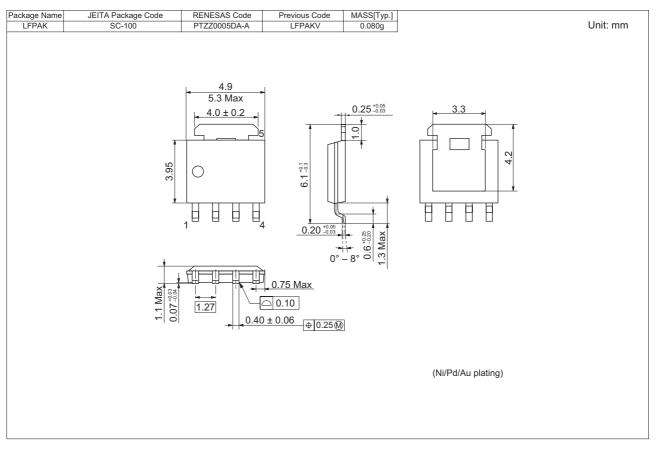








# **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
RJK0304DPB-00-J0	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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