

FEATURES

- With page mode function
- Switchable configuration
 - 4M x 8(byte mode)
 - 2M x 16(word mode)
- Single +5V power supply
- Fast access time: 100/120/150ns (max)
- Fast page mode access time: 45/50/70ns

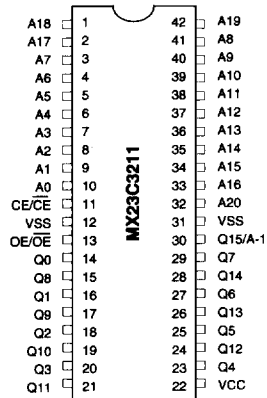
- Totally static operation
- Completely TTL compatible
- Operating current: 60mA
- Standby current: 100 μ A
- Package
 - 42 pin DIP (600 mil)/(word mode only)
 - 44 pin SOP (500 mil)

GENERAL DESCRIPTION

The MX23C3211 is a 5V only, 32M-bit, Read Only Memory with page mode. It is organized as 4M x 8 bits (byte mode) or as 2M x 16 bit (word mode) depending on BYTE (pin 32/44 SOP) voltage level. MX23C3211 has a static standby mode, and has an access time of 100/120/150ns. It is designed to be compatible with all microprocessors and similar applications in which high performance, large bit storage and simple interfacing are important design considerations.

MX23C1611 offers automatic power-down, with power-down controlled by the chip enable(CE/ \overline{CE}) Input. When CE/ \overline{CE} is not selected, the device automatically powers down and remains in a low-power standby mode as long as CE/ \overline{CE} stays in the unselected mode.

The OE/ \overline{OE} inputs as well as $\overline{CE}/\overline{CE}$ input may be programmed either active High or Low.

PIN CONFIGURATIONS
42 PDIP

44 SOP
