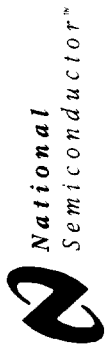


# PNP Transistors

Discrete **POWER & Signal**  
Technologies



## PNP General Purpose Amplifiers and Switches

Device No.	Case Style	V <sub>CE0</sub> (V) Min	V <sub>CE0</sub> (V) Min	V <sub>EBO</sub> (V) Min	I <sub>CBO</sub> (nA) Max	V <sub>CB</sub> (V) Max	h <sub>FE</sub> Min	h <sub>FE</sub> Max	I <sub>C</sub> (mA)	V <sub>CE</sub> & V <sub>CE</sub> (V)	V <sub>CE(SAT)</sub> (V) Max	V <sub>BE(SAT)</sub> (V) Min	V <sub>BE(SAT)</sub> (V) Max	I <sub>C</sub> (mA) Min	I <sub>C</sub> (mA) Max	C <sub>ob</sub> (pF) Max	f <sub>T</sub> (MHz) Min	f <sub>T</sub> (MHz) Max	t <sub>off</sub> (ns) Max	Test Conditions	Process No.
2N4402	TO-92 (92)	40	40	5			20	500	2	2	0.4	0.7	0.95	150	150	10	150	20	255	(Note 4)	63
							50	150	2		.75										
							30	1	1	1											
2N4403	TO-92 (92)	40	40	5			20	500	2	2	0.4	0.75	0.95	150	150	10	200	20	255	(Note 4)	63
							100	300	2												
							100	10	1	1											
							60	1	1	1	0.75		1.3	500							
							30	0.1	1	1											
MPS3702	TO-92 (92)	40	25	5	100	20	60	300	5	5	0.25			50	50	12	100	50			63
MPS3703	TO-92 (92)	50	30	5	100	20	30	150	5	5	0.25			50	50	12	100	50			63
MPS6534	TO-92 (92)	40	40	4	50	30	50	500	10	10	0.3		1	100		6					63
							90	270	1	1											
							60	10	1	1											
PN2907	TO-92 (92)	60	40	5	20	50	30	500	10	10	0.4		1.3	150	150	8	200	50	100	(Note 2)	63
							100	300	10	10			2.6	500							
							60	1	10	10	1.6										
							35	0.1	10	10											
PN2907A	TO-92 (92)	60	60	5	20	50	50	500	10	10	0.4		1.3	150	150	8	200	50	100	(Note 2)	63 (5-32)
							100	300	10	10			2.6	500							
							100	1	10	10	1.6										
							75	0.1	10	10											

NOTE: National preferred device for each process in **bold**. Number shown in parentheses indicates location (section-page) of device datasheet.

### PNP General Purpose Amplifiers and Switches (continued)

Device No.	Case Style	V <sub>CB0</sub> (V) Min	V <sub>CEO</sub> (V) Min	V <sub>EB0</sub> (V) Min	I <sub>CB0</sub> * (nA) Max	V <sub>CB</sub> (V)	h <sub>FE</sub> Min	I <sub>C</sub> @ Max (mA)	V <sub>CE</sub> & V <sub>CE</sub> (V)	V <sub>CE(SAT)</sub> (V) Max	V <sub>BE(SAT)</sub> (V) Min	I <sub>C</sub> @ Max (mA)	C <sub>ob</sub> (pF) Max	f <sub>T</sub> (MHz) Min	I <sub>C</sub> (mA) Max	t <sub>on</sub> (ns) Max	Test Conditions	Process No.
PN3638	TO-92 (92)	25	25	4	35*	15	20	300	2	0.25	1.1	50	20	100	50	170	(Note 1)	63
							20	50	1	1	0.8	2	300					
PN3638A	TO-92 (92)	25	25	4	35*	15	20	300	2	0.25	1.1	50	10	150	50	170	(Note 1)	63
							100	10	10	1	0.8	2	300					
PN3644	TO-92 (92)	45	45	5	35*	30	20	300	2	0.25	1	50	8	200	20	100	(Note 4)	63
							100	150	10	0.4	1.3	150						
PN3645	TO-92 (92)	60	60	5	35*	50	20	300	2	0.25	1	50	8	200	20	100	(Note 4)	63
							80	240	10	0.4	1.3	150						
PN4143	TO-92 (92)	60	40	5			30	500	10	0.4	1.3	150	8	200	50	100	(Note 6)	63
							50	150	1	1.6	2.6	500						
T1593	TO-92 (97)	40	40	5	100	20	100	300	2	0.25		50						63
TN2905A	TO-226 (99)	60	60	5	10	50	50	500	10	0.4	1.3	150	8	200	50	100	(Note 2)	63
							100	150	10	1.6	2.6	500						

# PNP Transistors

**PNP General Purpose Amplifiers and Switches** (continued)

Device No.	Case Style	V <sub>CB0</sub> (V) Min	V <sub>CE0</sub> (V) Min	V <sub>EBO</sub> (V) Min	I <sub>CE0</sub> <sup>*</sup> I <sub>CB0</sub> @ (nA) Max	V <sub>CE(SAT)</sub> & V <sub>BE(SAT)</sub> (V) Max Min	I <sub>C</sub> & V <sub>CE</sub> (mA) (V)	h <sub>FE</sub> @ (mA) Min Max	V <sub>CE(SAT)</sub> & V <sub>BE(SAT)</sub> (V) Max Min	I <sub>C</sub> (mA) Max	C <sub>ob</sub> (pF) Max	f <sub>T</sub> (MHz) Min	I <sub>C</sub> (mA) Max	t <sub>(on)</sub> (ns) Max	NF (dB) Max	Test Conditions	Process No.
2N3905	TO-92 (92)	40	40	5			100 1 50 1 10 1 1 1 0.1 1	15 30 50 40 30 30 150 40 30	0.25 0.65 0.85 0.4 0.65 0.95	10 50	4.5	200	10	260	5	(Note 5) (Note 8)	66
2N3906	TO-92 (92)	40	40	5			100 1 50 1 10 1 1 1 0.1 1	30 60 100 80 60 300 10 1 1	0.25 0.65 0.85 0.4 0.65 0.95	10 50	4.5	250	10	300	4	(Note 5) (Note 8)	66 (5-72)
2N4125	TO-92 (92)	30	30	4	50 20		50 1 2 1	25 60 150 2	0.4 0.95	50	4.5	200	10		5	(Note 8)	66
2N4126	TO-92 (92)	25	25	4	50 20		50 1 2 1	60 120 360 2	0.4 0.95	50	4.5	250	10		4	(Note 8)	66
MPS6518	TO-92 (92)		40	4	500 30		100 10 2 10	90 150 300 2	0.5	50	4						66
PN4121	TO-92 (92)	40	40	5	25* 30		50 1 10 1 1 1 0.1 1	15 70 60 40 200 1 1 1	0.13 0.14 0.3 0.7 0.9 1.1	1 10 50	4.5	400	10	150	4	(Note 7) (Note 8)	66
PN4122	TO-92 (92)	40	40	5	25* 30		50 1 10 1 1 1 0.1 1	30 150 150 100 300 1 1 1	0.13 0.14 0.3 0.7 0.9 1.1	1 10 50	4.5	450	10	150	4	(Note 7) (Note 8)	66
PN4917	TO-92 (92)	30	30	5	25* 15		50 1 10 1 1 1 0.1 1	150 150 100 300 1 1 1	0.13 0.14 0.3 0.75 0.9 1.1	1 10 50	4.5	450	10	150	4	(Note 3) (Note 8)	66

NOTE: National preferred device for each process in **bold**. Number shown in parentheses indicates location (section-page) of device datasheet.

## PNP General Purpose Amplifiers and Switches (continued)

Device No.	Case Style	V <sub>CB0</sub> (V) Min	V <sub>CEO</sub> (V) Min	V <sub>EBO</sub> (V) Min	I <sub>CES</sub> * I <sub>CB0</sub> @ (nA) Max	V <sub>CB</sub> (V) @	h <sub>FE</sub> @ I <sub>C</sub> & V <sub>CE</sub> Min Max (mA) (V)	V <sub>CE(SAT)</sub> (V) Max	V <sub>BE(SAT)</sub> (V) Max	I <sub>C</sub> (mA) Max	C <sub>ob</sub> (pF) Max	f <sub>T</sub> (MHz) Min Max	I <sub>C</sub> (mA) @ Max	t <sub>off</sub> (ns) Max	NF (dB) Max	Test Conditions	Process No.
2N3702	TO-92 (94)	40	25	5	100	20	60 300 50	0.25	5	5	12	100	50				68
2N3703	TO-92 (94)	50	30	5	100	20	30 150 50	0.25	5	5	12	100	50				68
PN200	TO-92 (92)	60	45	6	50	50	80 450 100 100 350	0.2 0.4	1 1	10 200	6	250	20		5	(Note 8)	68 (5-5)
PN200A	TO-92 (92)	60	45	6	50	50	300 600 100 100 200	0.2 0.4	1 1	10 200	6	250	20		4	(Note 8)	68 (5-5)
PN5138	TO-92 (92)	30	30	5	50	20	50 50 800	0.3	1 10 10	10 10	7	30	0.5				68
2N5400	TO-92 (92)	130	120	5	100	100	40 180 30	0.2 0.5	1 1 5	10 50	6	100 400	10		8	(Note 9)	74
2N5401	TO-92 (92)	160	150	5	50	120	50 60 50	0.2 0.5	1 1 5	10 50	6	100 300	10		8	(Note 9)	74 (5-114)
MPSL51	TO-92 (92)	100	100	4	1 μA	50	40 250 50	0.25 0.3	1.2 1.2	10 50	8	60	10				74

### TEST CONDITIONS

Note 1: I<sub>C</sub> = 300 mA, V<sub>CC</sub> = 10V, I<sub>B</sub><sup>1</sup> = I<sub>B</sub><sup>2</sup> = 30 mA.  
 Note 2: I<sub>C</sub> = 150 mA, V<sub>CC</sub> = 6V, I<sub>B</sub><sup>1</sup> = I<sub>B</sub><sup>2</sup> = 15 mA.  
 Note 3: I<sub>C</sub> = 50 mA, V<sub>CE</sub> = 10V, I<sub>B</sub><sup>1</sup> = I<sub>B</sub><sup>2</sup> = 5 mA.

Note 4: I<sub>C</sub> = 300 mA, V<sub>CC</sub> = 30V, I<sub>B</sub><sup>1</sup> = I<sub>B</sub><sup>2</sup> = 30 mA.  
 Note 5: I<sub>C</sub> = 10 mA, V<sub>CC</sub> = 3V, I<sub>B</sub><sup>1</sup> = I<sub>B</sub><sup>2</sup> = 1 mA.  
 Note 6: I<sub>C</sub> = 150 mA, V<sub>CC</sub> = 30V, I<sub>B</sub><sup>1</sup> = I<sub>B</sub><sup>2</sup> = 15 mA.

Note 7: I<sub>C</sub> = 50 mA, V<sub>CC</sub> = 30V, I<sub>B</sub><sup>1</sup> = I<sub>B</sub><sup>2</sup> = 5 mA.  
 Note 8: I<sub>C</sub> = 100 μA, V<sub>CE</sub> = 5V, f = 1 kHz.  
 Note 9: I<sub>C</sub> = 250 μA, V<sub>CE</sub> = 5V, f = 1 kHz.

NOTE: National preferred device for each process in bold. Number shown in parentheses indicates location (section-page) of device datasheet.