

**Triacs
sensitive gate**

BT136S series E
BT136M series E

GENERAL DESCRIPTION

Glass passivated, sensitive gate triacs in a plastic envelope, suitable for surface mounting, intended for use in general purpose bidirectional switching and phase control applications, where high sensitivity is required in all four quadrants.

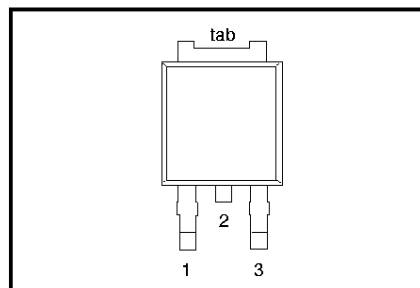
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V_{DRM}	BT136S (or BT136M)- Repetitive peak off-state voltages	500E 500	600E 600	800E 800	V
$I_{T(RMS)}$	RMS on-state current	4	4	4	A
I_{TSM}	Non-repetitive peak on-state current	25	25	25	A

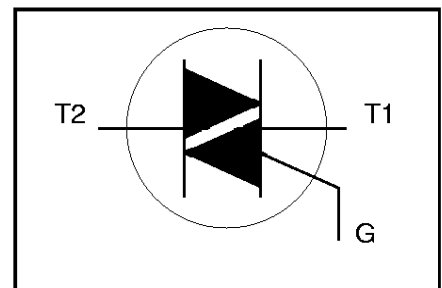
PINNING - SOT428

PIN NUMBER	Standard S	Alternative M
1	MT1	gate
2	MT2	MT2
3	gate	MT1
tab	MT2	MT2

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
				-500 500 ¹	-600 600 ¹	-800 800	
V_{DRM}	Repetitive peak off-state voltages		-				V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 107^\circ\text{C}$	-	4			A
I_{TSM}	Non-repetitive peak on-state current	full sine wave; $T_j = 25^\circ\text{C}$ prior to surge $t = 20\text{ ms}$	-	25			A
		$t = 16.7\text{ ms}$	-	27			A
		$t = 10\text{ ms}$	-	3.1			A ² s
I^2t	I^2t for fusing	$I_{TM} = 6\text{ A}$; $I_G = 0.2\text{ A}$; $di_G/dt = 0.2\text{ A}/\mu\text{s}$	-	50			A/μs
di_T/dt	Repetitive rate of rise of on-state current after triggering		-	50			A/μs
		T2+ G+	-	50			A/μs
		T2+ G-	-	10			A/μs
		T2- G-	-	2			A
		T2- G+	-	5			V
I_{GM}	Peak gate current		-	5			W
V_{GM}	Peak gate voltage		-	0.5			W
P_{GM}	Peak gate power		-	150			°C
$P_{G(AV)}$	Average gate power	over any 20 ms period	-40	125			°C
T_{sig}	Storage temperature		-				
T_j	Operating junction temperature		-				

1 Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 3 A/μs.

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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j-mb}$	Thermal resistance junction to mounting base	full cycle	-	-	3.0	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient	half cycle pcb (FR4) mounted; footprint as in Fig.14	-	-	3.7	K/W
			-	75	-	K/W

STATIC CHARACTERISTICS

 $T_j = 25\text{ }^\circ\text{C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{GT}	Gate trigger current	$V_D = 12\text{ V}; I_T = 0.1\text{ A}$				
		T2+ G+	-	2.5	10	mA
		T2+ G-	-	4.0	10	mA
		T2- G-	-	5.0	10	mA
		T2- G+	-	11	25	mA
I_L	Latching current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$				
		T2+ G+	-	3.0	15	mA
		T2+ G-	-	10	20	mA
		T2- G-	-	2.5	15	mA
		T2- G+	-	4.0	20	mA
I_H	Holding current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$	-	2.2	15	mA
V_T	On-state voltage	$I_T = 5\text{ A}$	-	1.4	1.70	V
V_{GT}	Gate trigger voltage	$V_D = 12\text{ V}; I_T = 0.1\text{ A}$	-	0.7	1.5	V
		$V_D = 400\text{ V}; I_T = 0.1\text{ A}; T_j = 125\text{ }^\circ\text{C}$	0.25	0.4	-	V
I_D	Off-state leakage current	$V_D = V_{DRM(max)}; T_j = 125\text{ }^\circ\text{C}$	-	0.1	0.5	mA

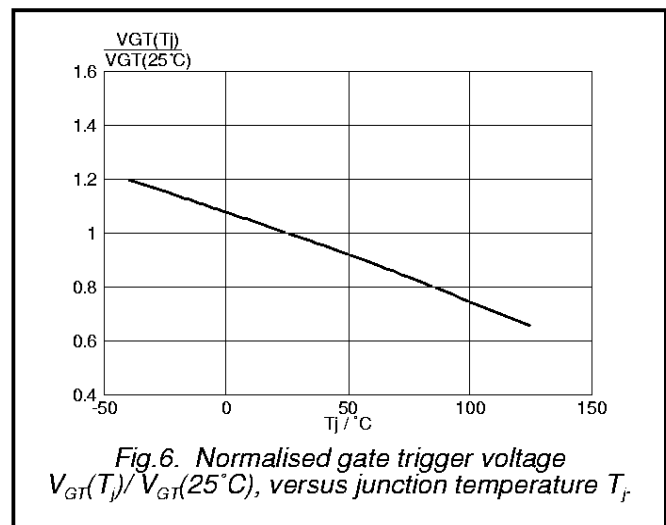
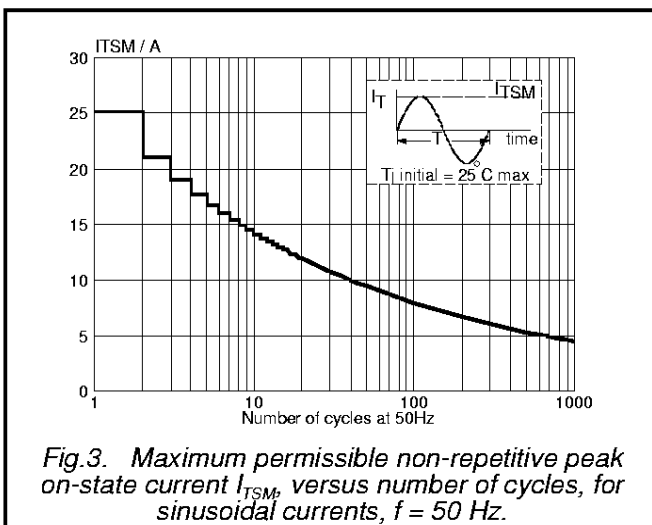
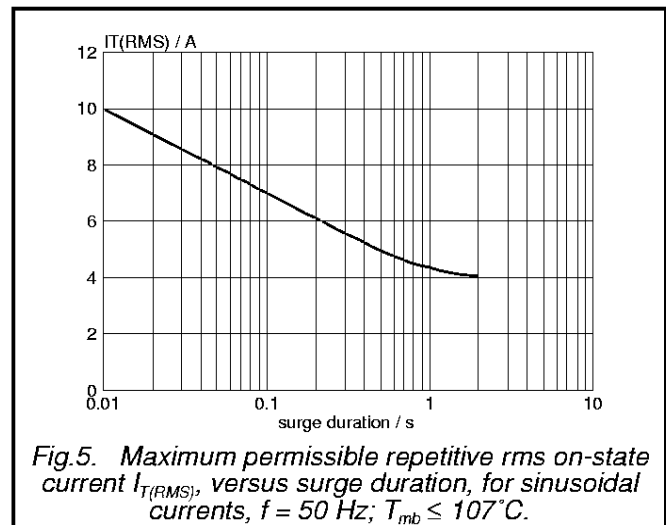
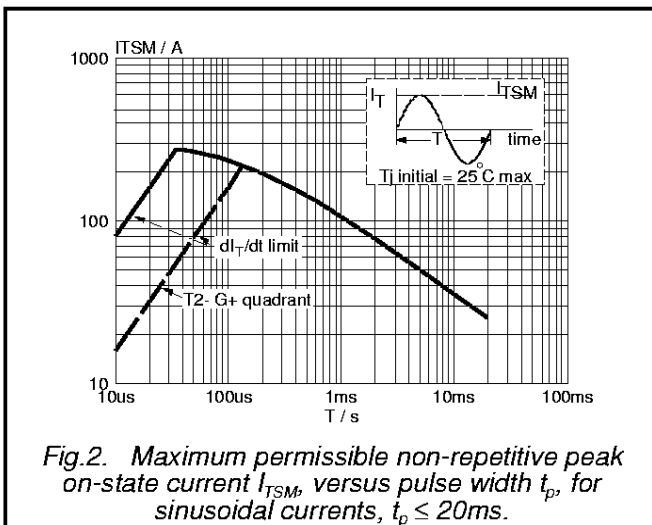
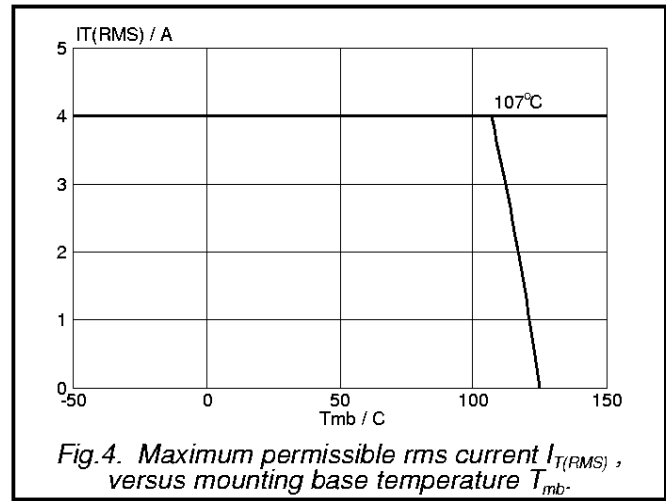
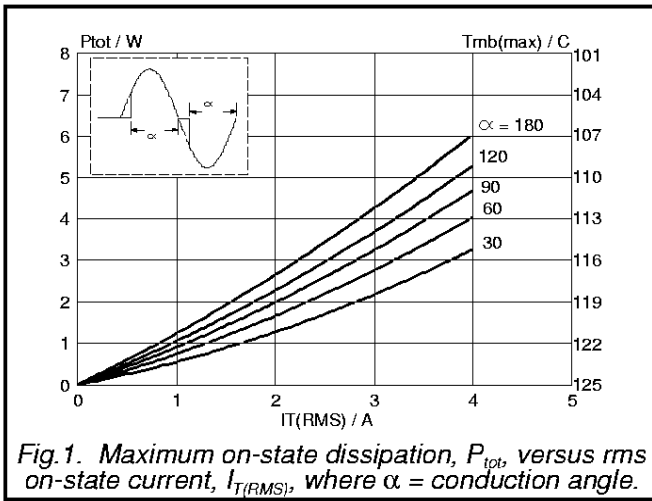
DYNAMIC CHARACTERISTICS

 $T_j = 25\text{ }^\circ\text{C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV_D/dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125\text{ }^\circ\text{C};$ exponential waveform; gate open circuit	-	50	-	V/ μs
t_{gt}	Gate controlled turn-on time	$I_{TM} = 6\text{ A}; V_D = V_{DRM(max)}; I_G = 0.1\text{ A};$ $dI_G/dt = 5\text{ A}/\mu\text{s}$	-	2	-	μs

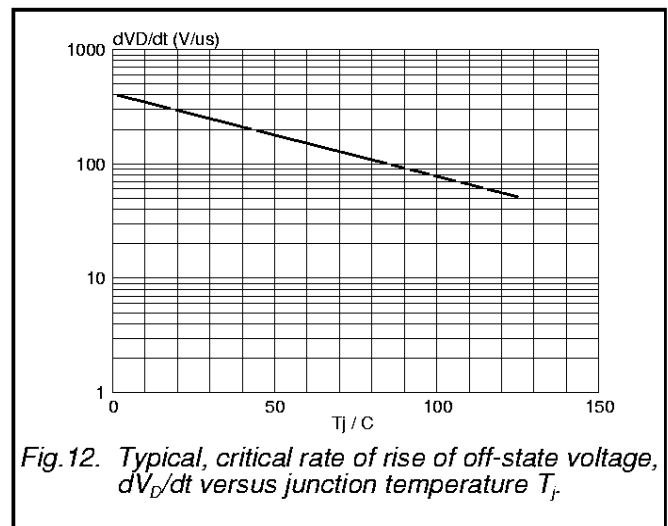
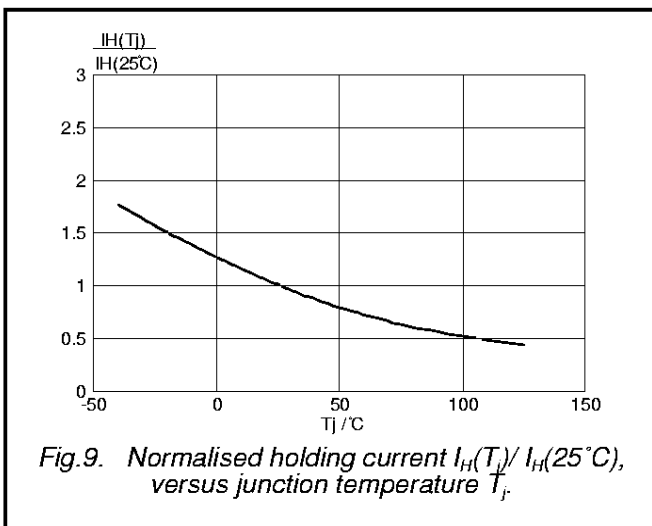
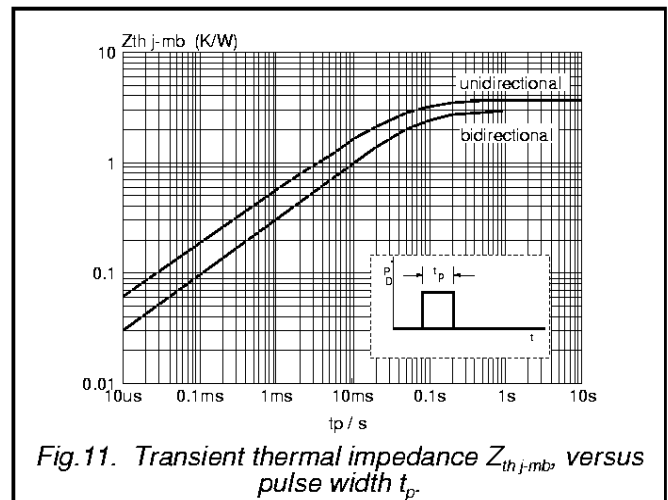
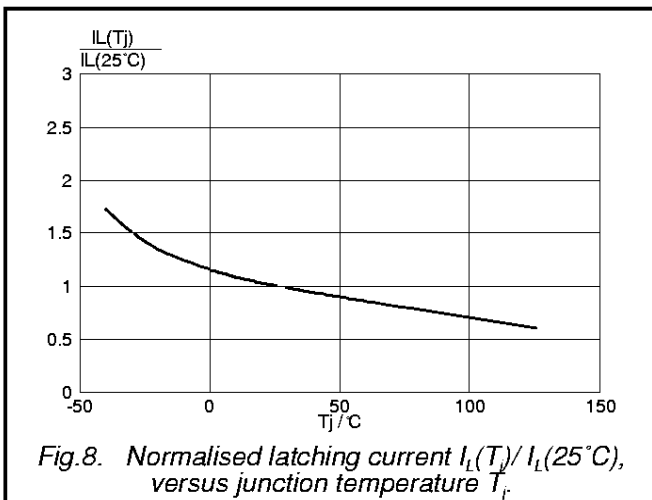
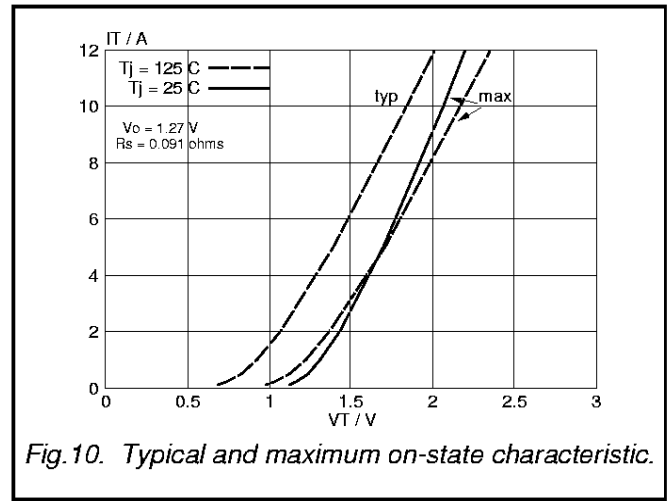
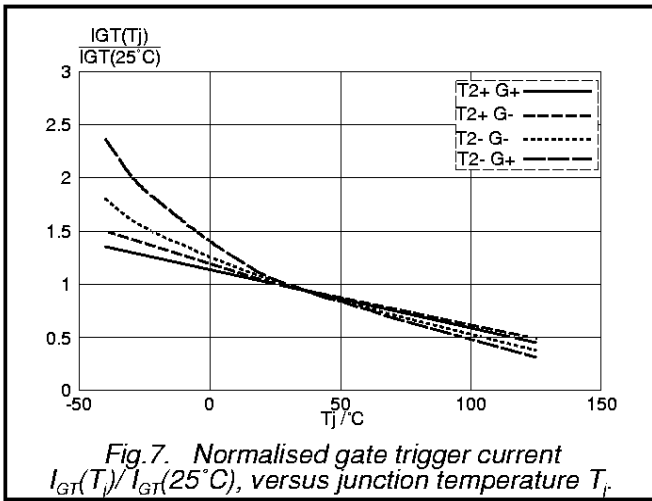
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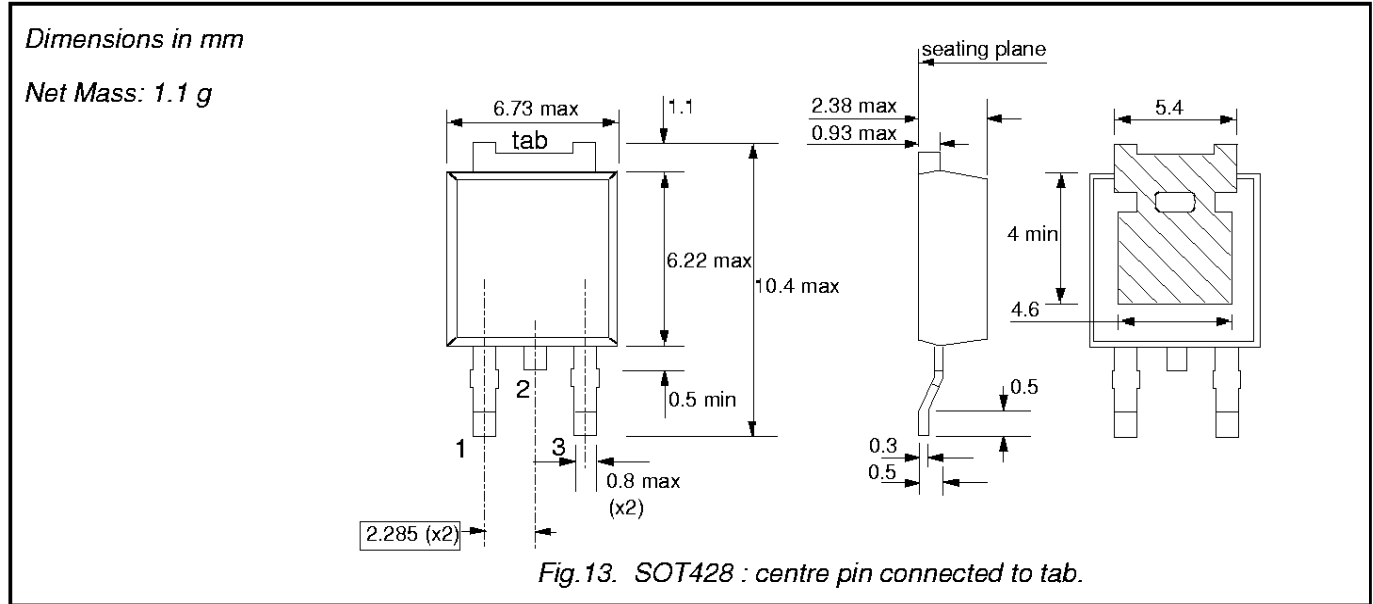
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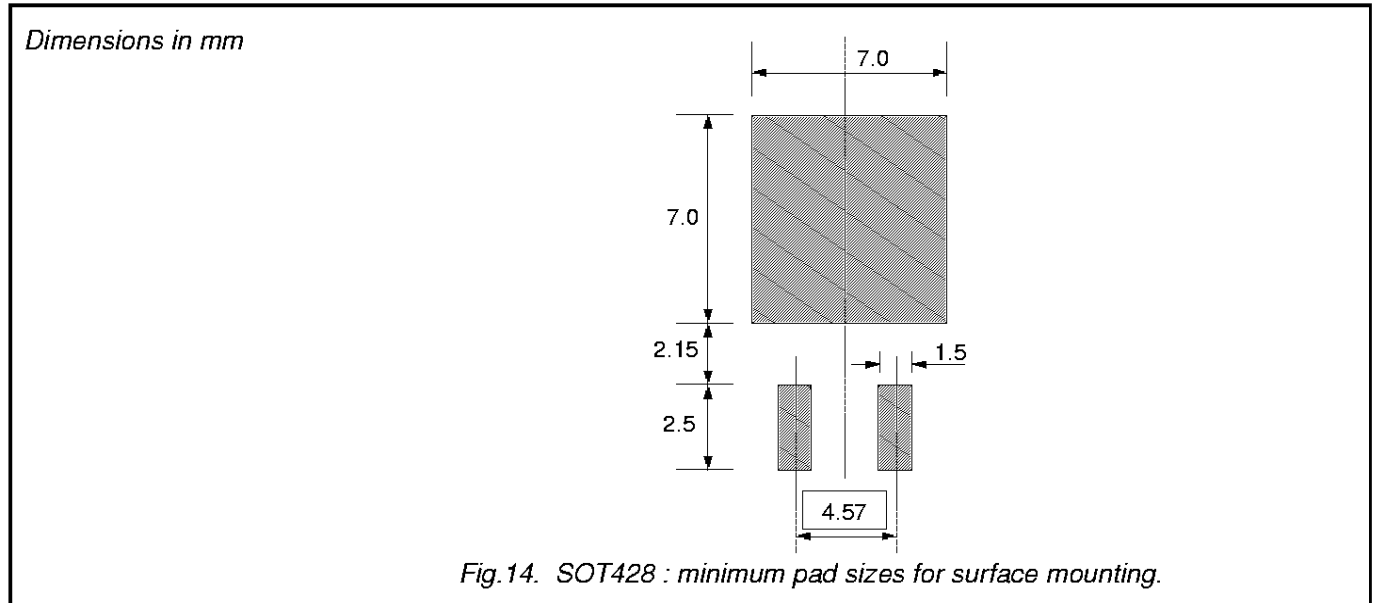
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MECHANICAL DATA



MOUNTING INSTRUCTIONS



Notes

- 1. Plastic meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	
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