
HM628128A Series

131,072-word \times 8-bit High Speed CMOS Static RAM

HITACHI

Description

The Hitachi HM628128A is a CMOS static RAM organized 128-kword \times 8-bit. It realizes higher density, higher performance and low power consumption by employing 0.8 μ m Hi-CMOS process technology. It offers low power standby power dissipation; therefore, it is suitable for battery backup systems. The device, packaged in a 525-mil SOP (460-mil body SOP) or a 600-mil plastic DIP, or a 8 \times 20 mm TSOP with thickness of 1.2 mm, is available for high density mounting. TSOP package is suitable for cards, and reverse type TSOP is also provided.

Features

- High speed
 - Fast access time: 55/70/85/100 ns (max)
- Low power
 - Active: 75 mW (typ)
 - Standby: 10 μ W (typ)
- Single 5 V supply
- Completely static memory
 - No clock or timing strobe required
- Equal access and cycle times
- Common data input and output
 - Three state output
- Directly TTL compatible
 - All inputs and outputs
- Capability of battery backup operation
 - 2 chip selection for battery backup



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Ordering Information

Type No.	Access Time	Package
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