## **Tentative**

DRC4	1144V
Total pages	page

## **DRC4144V**

Silicon NPN epitaxial planar type

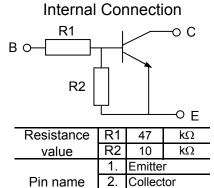
For digital circuits

Marking Symbol: NJ

Package Code: NS-B1-B-B

Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	VCBO	50	V
Collector-emitter voltage (Base open)	VCEO	50	V
Collector current	IC	100	mA
Total power dissipation	PT	300	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C



Base

## Electrical Characteristics Ta = 25 °C±3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	VCBO	IC = 10 μA, IE = 0	50			V
Collector-emitter voltage (Base open)	VCEO	IC = 2 mA, IB = 0	50			V
Collector-base cutoff current (Emitter open)	ICBO	VCB = 50 V, IE = 0			0.1	μA
Collector-emitter cutoff current (Base open)	ICEO	VCE = 50 V, IB = 0			0.5	μA
Emitter-base cutoff current (Collector open)	IEBO	VEB = 6 V, IC = 0			0.2	mA
Forward current transfer ratio	hFE	VCE = 10 V, IC = 5 mA	30			-
Collector-emitter saturation voltage	VCE(sat)	IC = 10 mA, IB = 0.5 mA			0.25	V
Input voltage	Vi(on)	VCE = 0.2 V, IC = 5 mA	6.3			V
input voltage	Vi(off)	VCE = 5 V, IC = 100 μA			1.9	V
Input resistance	R1		-30%	47	+30%	kΩ
Resistance ratio	R1/R2		3.7	4.7	5.7	-

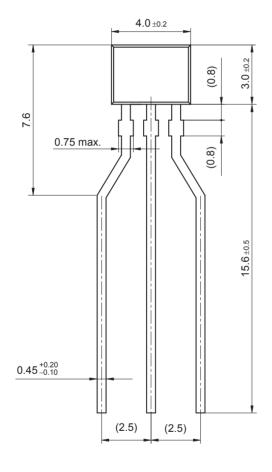
Note: Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

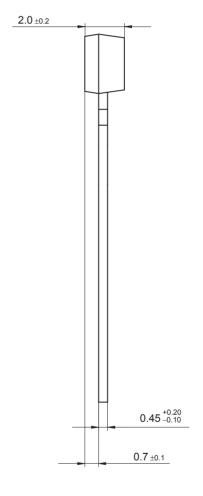
**Packing** 

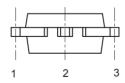
Radial type: 5 000 pcs / carton

2010.2.22	2010.8.17
Prepared	Revised

NS-B1-B-B Unit: mm







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