

KSC3076

I-PACK 1. Base 2. Collector 3. Emitter

# **NPN Epitaxial Silicon Transistor**

Absolute Maximum Ratings  $T_{C}=25^{\circ}C$  unless otherwise noted

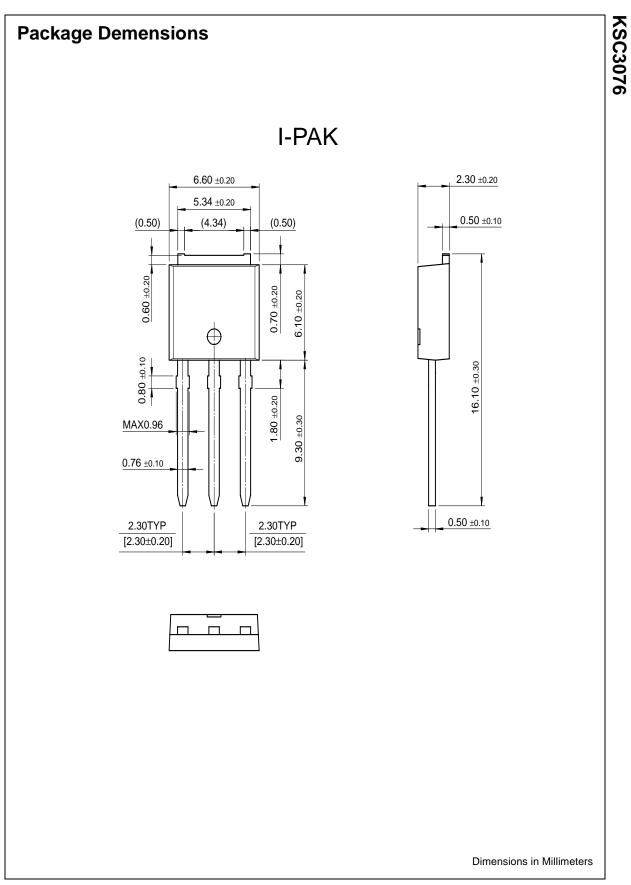
Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	50	V
V <sub>CEO</sub>	Collector-Emitter Voltage	50	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current	2	А
I <sub>B</sub>	Base Current	1	А
P <sub>C</sub>	Collector Dissipation (T <sub>a</sub> =25°C)	1	W
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	10	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

## Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10mA, I <sub>B</sub> = 0	50			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 50V, I_E = 0$			1	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$			1	μΑ
h <sub>FE1</sub> h <sub>FE2</sub>	DC Current Gain	$V_{CE} = 2V, I_{C} = 0.5A$ $V_{CE} = 2V, I_{C} = 1.5A$	70 40		240	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 0.05A			0.5	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 0.05A			1.2	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = 2V, I <sub>C</sub> = 0.5A		100		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = 10V, f = 1MHz		30		pF
t <sub>ON</sub>	Turn ON Time	V <sub>CC</sub> = 30V, I <sub>C</sub> = 1A		0.1		μs
t <sub>STG</sub>	Storage Time	1 <sub>B1</sub> = - I <sub>B2</sub> = 0.05A		1		μs
t <sub>F</sub>	Fall Time	$R_L = 30\Omega$		0.1		μs

# h<sub>FE1</sub> Classification

Classification	0	Y
h <sub>FE1</sub>	70 ~ 140	120 ~ 240



©2000 Fairchild Semiconductor International

Rev. A, February 2000

### TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx<sup>™</sup> Bottomless<sup>™</sup> CoolFET<sup>™</sup> CROSSVOLT<sup>™</sup> E<sup>2</sup>CMOS<sup>™</sup> FACT<sup>™</sup> FACT Quiet Series<sup>™</sup> FAST<sup>®</sup> FAST<sup>®</sup> FASTr<sup>™</sup> GTO<sup>™</sup> HiSeC<sup>™</sup> ISOPLANAR<sup>™</sup> MICROWIRE<sup>™</sup> POP<sup>™</sup> PowerTrench<sup>®</sup> QFET<sup>™</sup> QS<sup>™</sup> Quiet Series<sup>™</sup> SuperSOT<sup>™</sup>-3 SuperSOT<sup>™</sup>-6 SuperSOT<sup>TM</sup>-8 SyncFET<sup>TM</sup> TinyLogic<sup>TM</sup> UHC<sup>TM</sup> VCX<sup>TM</sup>

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

### As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### **PRODUCT STATUS DEFINITIONS**

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

file:///E /new/html/KSC3076.h	ntml		
Fairchild Semiconductor		— ſ	et Folders and Applica
find products  Products groups Analog and Mixed Signal Discrete Interface Logic Microcontrollers Non-Volatile Memory Optoelectronics Markets and applications New products Product selection and parametric search Cross-reference search technical information buy products	Home >> Find products >> KSC3076 NPN Epitaxial Silicon Transistor Contents Features   Applications   Product status/pricing/packaging Features Features • Low Collector-Emitter Saturation Voltage • Complement to KSA1241 back to top • Applications	Datasheet Download this datasheet PDF e-mail this datasheet [E- This pagePrint version.	Related Links Request samples Datted Line How to order products Datted Line Product Change Notices (PCNs) Datted Line Distributor and field sales representatives Datted Line Ouality and reliability Datted Line Design tools
technical support my Fairchild company	back to top		

Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
KSC3076YTU	Full Production	\$0.302	TO-251(IPAK)	3	RAIL
* 1 000 sizes Destadour Division					

\* 1,000 piece Budgetary Pricing

# back to top

Ţ

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor