

Product Features

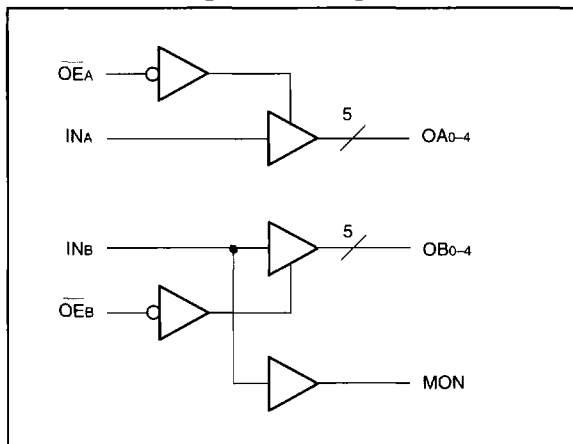
- 3.3V version of PI49FCT805/806
- Extremely low output skew: 0.5ns
- Monitor output pin
- Clock busing with Hi-Z state control
- TTL input and CMOS output compatible
- Industrial operation at -40°C to 85°C
- Extremely low static power (1mW, typ.)
- Hysteresis on all inputs
- Packages available:
 - 20-pin 300 mil wide DIP (P20)
 - 20-pin 300 mil wide SOIC (S20)
 - 20-pin 150 mil wide QSOP (Q20)
 - 20-pin 209 mil wide SSOP (H20)
- Device models available on request

Product Description

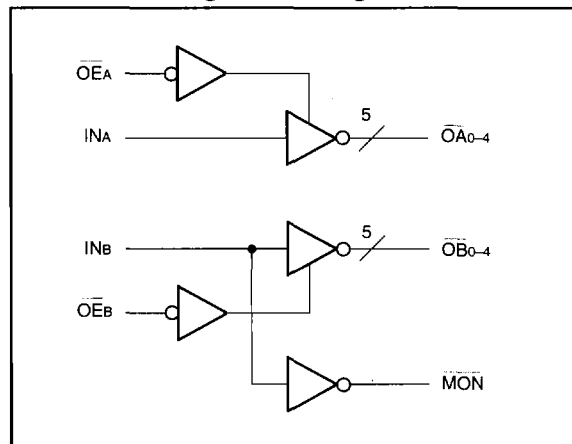
Pericom Semiconductor's PI49FCT series of logic circuits are produced using the Company's advanced 0.6 micron CMOS technology, achieving industry leading speed grades.

The PI49FCT3805 is a 3.3V non-inverting clock driver and the PI49FCT3806 is a 3.3V inverting clock driver designed with two independent groups of buffers. These buffers have Hi-Z state Output Enable inputs (active LOW) with a 1-in, 5-out configuration per group. Each clock driver consists of two banks of drivers, driving five outputs each from a standard TTL compatible CMOS input.

PI49FCT3805 Logic Block Diagram



PI49FCT3806 Logic Block Diagram



Product Pin Description

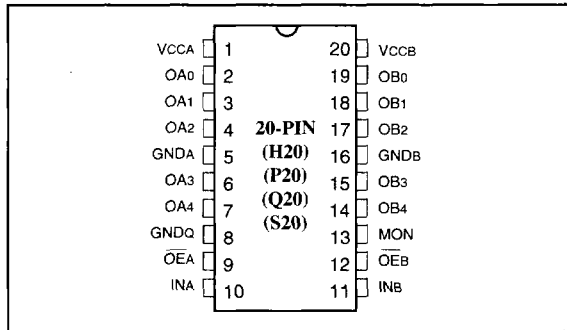
Pin Name	Description
$\overline{OE}_A, \overline{OE}_B$	Hi-Z State Output Enable Inputs (Active LOW)
INA, INB	Clock Inputs
OAN, OBN	Clock Outputs
MON	Monitor Output
GND	Ground
Vcc	Power

PI49FCT3805 Truth Table⁽¹⁾

Inputs		Outputs	
$\overline{OE}_A, \overline{OE}_B$	INA, INB	OAN, OBN	MON
L	L	L	L
L	H	H	H
H	L	Z	L
H	H	Z	H

NOTE: 1. H = High Voltage Level, L = Low Voltage Level,
Z = High Impedance

PI49FCT3805 Product Pin Configuration

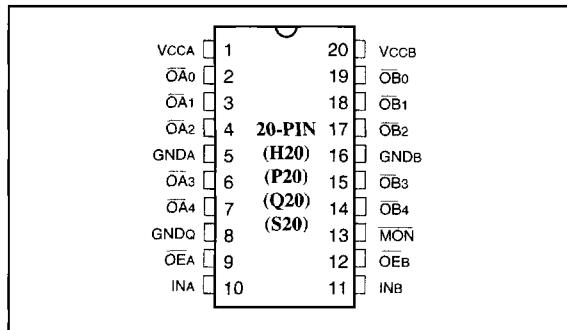


PI49FCT3806 Truth Table⁽¹⁾

Inputs		Outputs	
$\overline{OE}_A, \overline{OE}_B$	INA, INB	$\overline{OAN}, \overline{OBN}$	MON
L	L	H	H
L	H	L	L
H	L	Z	H
H	H	Z	L

NOTE: 1. H = High Voltage Level, L = Low Voltage Level,
Z = High Impedance

PI49FCT3806 Product Pin Configuration



Capacitance (TA = 25°C, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Typ	Max.	Units
CIN	Input Capacitance	VIN = 0V	4.5	6.0	pF
COUT	Output Capacitance	VOUT = 0V	5.5	8.0	pF

Notes:

1. This parameter is determined by device characterization but is not production tested.

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)	-0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only) .	-0.5V to +7.0V
DC Input Voltage	-0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Operating Range

Ambient Temperature = -40°C to +85°C
Vcc = 3.3V ± 0.3V

DC Electrical Characteristics (Over the Operating Range)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
V _{OH}	Output HIGH Voltage	V _{CC} = Min., V _{IN} = V _{IH} or V _{IL}		V _{CC} - 0.2 2.4 ⁽³⁾	— 3.0	—	V
V _{OL}	Output LOW Voltage	V _{CC} = Min., V _{IN} = V _{IH} or V _{IL}		— — —	— 0.2 0.3	0.2 0.4 0.5	V
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0	—	5.5	V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level		-0.5	—	0.8	V
I _{IH}	Input HIGH Current	V _{CC} = Max.	V _{IN} = V _{CC} (Input Pins)	—	—	1	μA
I _{IL}	Input LOW Current	V _{CC} = Max.	V _{IN} = GND (Input & I/O Pins)	—	—	-1	μA
I _{OZH}	High Impedance	V _{CC} = Max.		—	—	1	μA
I _{OZL}	Output Current	(Hi-Z State Output Pins)		—	—	-1	
V _{IK}	Clamp Diode Voltage	V _{CC} = Min., I _{IN} = -18mA		—	-0.7	-1.2	V
I _{ODH}	Output HIGH Current	V _{CC} = 3.3V, V _{IN} = V _{IH} or V _{IL} , V _{OUT} = 1.5V ⁽⁴⁾		-35	-60	-110	mA
I _{ODL}	Output LOW Current	V _{CC} = 3.3V, V _{IN} = V _{IH} or V _{IL} , V _{OUT} = 1.5V ⁽⁴⁾		50	90	200	mA
I _{OS}	Short Circuit Current ⁽⁵⁾	V _{CC} = Max., V _{OUT} = GND ⁽⁵⁾		-60	-135	-240	mA
V _H	Input Hysteresis			—	150	—	mV

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V_{CC} = 3.3V, +25°C ambient and maximum loading.
- V_{OH} = V_{CC} - 0.6V at rated current.
- This parameter is determined by device characterization but is not production tested.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}	—	3	30	μA
ΔI _{CC}	Supply Current per Inputs @ TTL HIGH	V _{CC} = Max.	V _{IN} = V _{CC} - 0.6V ⁽³⁾	—	2.0	300	μA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max., Outputs Open O _{EA} or O _{EB} = GND Per Output Toggling 50% Duty Cycle	V _{IN} = V _{CC} V _{IN} = GND	—	0.08	0.16	mA/ MHz
I _C	Total Power Supply Current ⁽⁶⁾	V _{CC} = Max., Outputs Open f _o = 10 MHz 50% Duty Cycle O _{EA} or O _{EB} = GND Mon. Output Toggling	V _{IN} = V _{CC} V _{IN} = GND	—	3.3	9.0 ⁽⁵⁾	mA
			V _{IN} = V _{CC} - 0.6V V _{IN} = GND	—	3.3	10.0 ⁽⁵⁾	
		V _{CC} = Max., Outputs Open f _o = 2.5 MHz 50% Duty Cycle O _{EA} or O _{EB} = GND Eleven Outputs Toggling	V _{IN} = V _{CC} V _{IN} = GND	—	1.8	6.0 ⁽⁵⁾	
			V _{IN} = V _{CC} - 0.6V V _{IN} = GND	—	1.8	7.0 ⁽⁵⁾	

Notes:

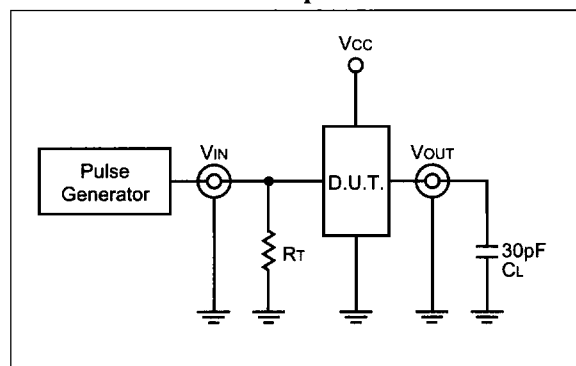
- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V_{CC} = 3.3V, +25°C ambient.
- Per TTL driven input (V_{IN} = V_{CC} - 0.6V); all other inputs at V_{CC} or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I_C formula. These limits are guaranteed but not tested.
- I_C = I_{QUIESCENT} + I_{INPUTS} + I_{DYNAMIC}
I_C = I_{CC} + ΔI_{CC} D_HN_T + I_{CCD} (f_oN_O)
I_{CC} = Quiescent Current
ΔI_{CC} = Power Supply Current for a TTL High Input (V_{IN} = V_{CC} - 0.6V)
D_H = Duty Cycle for TTL Inputs High
N_T = Number of TTL Inputs at D_H
I_{CCD} = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)
f_o = Output Frequency
N_O = Number of Outputs at f_o
All currents are in milliamps and all frequencies are in megahertz.

Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	3805/3806		3805A/3806A		3805B/3806B		3805C/3806C		Unit
			Com.		Com.		Com.		Com.		
			Min	Max	Min	Max	Min	Max	Min	Max	
t _{PLH} t _{PHL}	Propagation Delay I _{NA} to O _{AN} , I _{NB} to O _{BN}	C _L = 50 pF R _L = 500Ω	1.5	6.5	1.5	5.8	1.5	5.0	1.5	4.5	ns
t _{PZH} t _{PZL}	Output Enable Time O _{EA} to O _{AN} , O _{EB} to O _{BN}		1.5	8.0	1.5	8.0	1.5	6.5	1.5	6.2	ns
t _{PHZ} t _{PLZ}	Output Disable Time O _{EA} to O _{AN} , O _{EB} to O _{BN}		1.5	7.0	1.5	7.0	1.5	6.0	1.5	5.0	ns
tsk(o) ⁽³⁾	Skew between two outputs of same package (same transition)		—	0.7	—	0.7	—	0.5	—	0.5	ns
tsk(p) ⁽³⁾	Skew between opposite transitions (t _{PHL} -t _{PLH}) of the same output		—	1.0	—	0.7	—	0.5	—	0.5	ns
tsk(t) ⁽³⁾	Skew between two outputs of different package at same temperature (same transition)		—	1.5	—	1.2	—	1.0	—	0.8	ns

Notes:

1. See test circuit and wave forms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. Skew measured at worse cast temperature (max. temp).

Tests Circuits For All Outputs⁽¹⁾

Switch Position

Test	Switch
Disable LOW Enable LOW	6V
Disable HIGH Enable HIGH	GND
All Other Inputs	Open

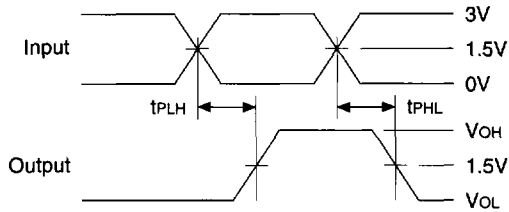
DEFINITIONS:

C_L = Load capacitance: includes jig and probe capacitance.

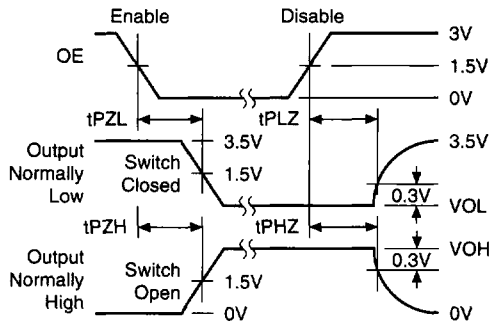
R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator.

SWITCHING WAVEFORMS

Propagation Delay



Enable and Disable Times



Ordering Information

Part Number	Marking Code
PI49FCT3805xP	PI49FCT3805px
PI49FCT3806xP	PI49FCT3806px

Note: x = Speed Grades: "blank", A, B, C.

p = Package Type:

H=209-mil SSOP

P=300-mil DIP

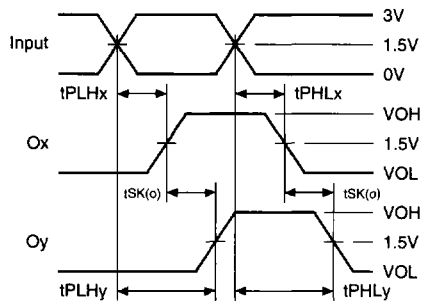
Q=150-mil SOIC

S=300-mil SOIC

Example:

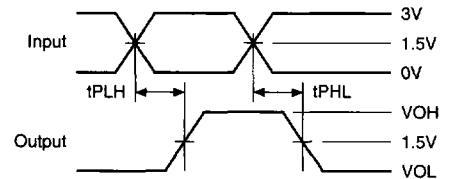
PI49FCT3805ATH = A grade, H pkg marked as PI49FCT3805THA

Output Skew – t_{SK(o)}



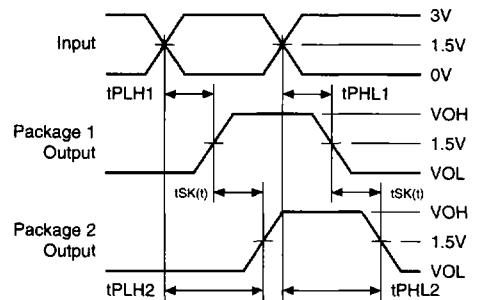
$$t_{SK(o)} = |t_{PLHy} - t_{PLHx}| \text{ or } |t_{PHLy} - t_{PHLx}|$$

Pulse Skew – t_{SK(p)}



$$t_{SK(p)} = |t_{PHL} - t_{PLH}|$$

Package Skew – t_{SK(t)}



$$t_{SK(t)} = |t_{PLH2} - t_{PLH1}| \text{ or } |t_{PHL2} - t_{PHL1}|$$