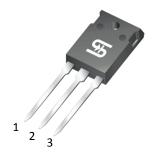


30A, 50V - 600V Glass Passivated Super Fast Rectifiers

FEATURES

- Dual rectifier construction, positive center-tap
- Glass passivated chip junctions
- Superfast recovery time, high voltage
- Low forward voltage, high current capability
- Low thermal resistance
- Low power loss, high efficiency
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21







MECHANICAL DATA

Case: TO-247AD (TO-3P)

Molding compound, UL flammability classification rating 94V-0

Part no. with suffix "H" means AEC-Q101 qualified

Packing code with suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102

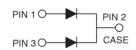
Meet JESD 201 class 2 whisker test

Polarity: As marked

Mounting torque: 10 in-lbs maximum

Weight: 5.6g (approximately)

TO-247AD (TO-3P)

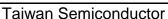


MAXIMUM RATINGS AND ELECTRICAL CHA	1					- OF	- SE	6 E	
		SF	SF	SF	SF	SF	SF	SF	
PARAMETER	SYMBOL	3001	3002	3003	3004	3005	3006	3008	UNIT
		PT	PT	PT	PT	PT	PT	PT	
Maximum repetitive peak reverse voltage	V_{RRM}	50	100	150	200	300	400	600	V
Maximum RMS voltage	V_{RMS}	35	70	105	140	210	280	420	V
Maximum DC blocking voltage	V_{DC}	50	100	150	200	300	400	600	V
Maximum average forward rectified current	I _{F(AV)}				30				Α
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	300					Α		
Maximum instantaneous forward voltage (Note 1) I _F = 15 A	V _F		0.95 1.3		1.7	V			
Maximum reverse current @ rated V_R $T_J=25^{\circ}C$ $T_J=125^{\circ}C$	I _R	10 500					μΑ		
Maximum reverse recovery time (Note 2)	t _{rr}				35				ns
Typical junction capacitance (Note 3)	СJ	175					pF		
Typical thermal resistance	$R_{\theta JC}$	1.0					°C/W		
Operating junction temperature range		- 55 to +150						°C	
Storage temperature range		- 55 to +150						°C	

Note 1: Pulse Test with PW=300 µs, 1% Duty Cycle

Note 2: Reverse Recovery Test Conditions: I_F =0.5A, I_R =1.0A, Recover to 0.25A.

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.





ORDERING INFORMATION						
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX ^(*)	PACKAGE	PACKING	
SF30xxPT (Note 1)	Н	C0	G	TO-247AD (TO-3P)	30 / Tube	

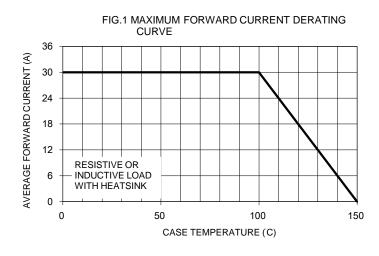
Note 1: "xx" defines voltage from 50V (SF3001PT) to 600V (SF3008PT)

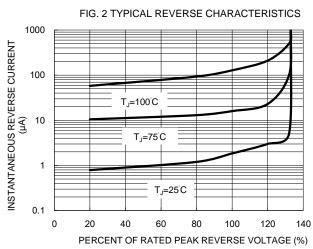
^{*:} Optional available

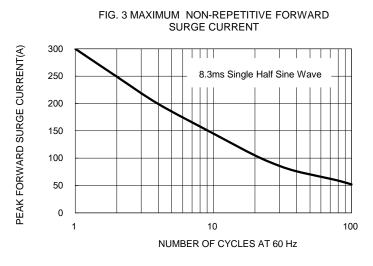
EXAMPLE						
PREFERRED P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION	
SF3006PTHC0G	SF3006PT	Н	C0	G	AEC-Q101 qualified Green compound	

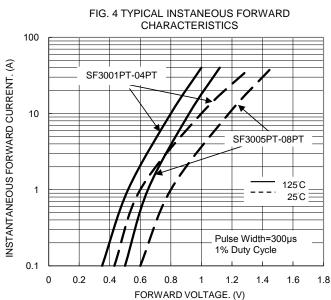
RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)











Taiwan Semiconductor

FIG. 5 TYPICAL JUNCTION CAPACITANCE

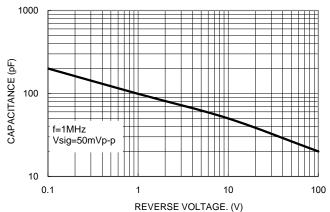
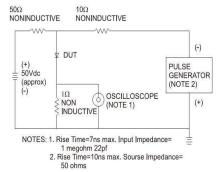
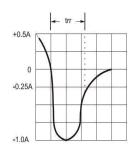


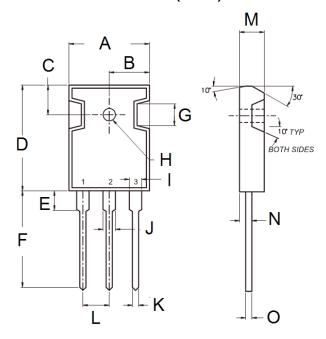
FIG.6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





PACKAGE OUTLINE DIMENSIONS

TO-247AD (TO-3P)



DIM.	Unit	(mm)	Unit (inch)		
DIW.	Min	Max	Min	Max	
Α	15.90	16.40	0.626	0.646	
В	7.90	8.20	0.311	0.323	
С	5.70	6.20	0.224	0.244	
D	20.80	21.30	0.819	0.839	
Е	3.50	4.10	0.138	0.161	
F	19.70	20.20	0.776	0.795	
G	-	4.30	-	0.169	
Н	2.90	3.40	0.114	0.134	
ı	1.93	2.18	0.076	0.086	
J	2.97	3.22	0.117	0.127	
K	1.12	1.22	0.044	0.048	
L	5.20	5.70	0.205	0.224	
М	4.90	5.16	0.193	0.203	
N	2.70	3.00	0.106	0.118	
0	0.51	0.76	0.020	0.030	

MARKING DIAGRAM



P/N = Marking Code
G = Green Compound
YWW = Date Code
F = Factory Code





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Version: I1809