

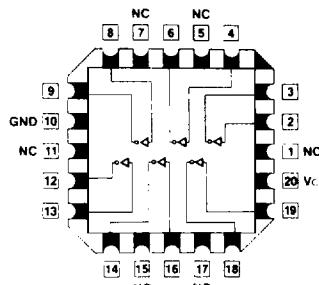
54AC/74AC04 • 54ACT/74ACT04

Hex Inverter

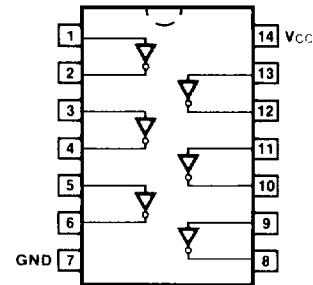
Connection Diagrams

- Outputs Source/Sink 24 mA
- 'ACT04 has TTL-Compatible Inputs

Ordering Code: See Section 6



Pin Assignment
for LCC



Pin Assignment
for DIP, Flatpak and SOIC

DC Characteristics (unless otherwise specified)

Symbol	Parameter	54AC/ACT	74AC/ACT	Units	Conditions
I _{cc}	Maximum Quiescent Supply Current	80	40	µA	V _{IN} = V _{CC} or Ground, V _{CC} = 5.5 V, TA = Worst Case
I _{cc}	Maximum Quiescent Supply Current	4.0	4.0	µA	V _{IN} = V _{CC} or Ground, V _{CC} = 5.5 V, TA = 25°C
I _{ccT}	Maximum Additional I _{cc} /Input ('ACT04)	1.6	1.5	mA	V _{IN} = V _{CC} - 2.1 V V _{CC} = 5.5 V, TA = Worst Case

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AC Characteristics

Symbol	Parameter	V _{CC} * (V)	74AC			54AC		74AC			Units	Fig. No.		
			TA = + 25°C CL = 50 pF			TA = - 55°C to + 125°C CL = 50 pF		TA = - 40°C to + 85°C CL = 50 pF						
			Min	Typ	Max	Min	Max	Min	Max	Min				
t _{PLH}	Propagation Delay	3.3 5.0	1.0 1.0	4.5 4.0	9.0 7.0	1.0 1.0	11.0 8.5	1.0 1.0	10.0 7.5	1.0 1.0	ns	3-5		
t _{PHL}	Propagation Delay	3.3 5.0	1.0 1.0	4.5 3.5	8.5 6.5	1.0 1.0	10.0 7.5	1.0 1.0	9.5 7.0	1.0 1.0	ns	3-5		

*Voltage Range 3.3 is 3.3 V ± 0.3 V

Voltage Range 5.0 is 5.0 V ± 0.5 V

Military parameters given herein are for general references only. For current military specifications and subgroup testing information please request Fairchild's Table I data sheet from your Fairchild sales engineer or account representative.

AC04 • ACT04

AC Characteristics

Symbol	Parameter	Vcc*	74ACT			54ACT			74ACT			Units	Fig. No.		
			TA = + 25°C CL = 50 pF			TA = - 55°C to + 125°C CL = 50 pF			TA = - 40°C to + 85°C CL = 50 pF						
			Min	Typ	Max	Min	Max	Min	Max	Min	Max				
tPLH	Propagation Delay	5.0		4.5								ns	3-6		
tPHL	Propagation Delay	5.0		3.9								ns	3-6		

*Voltage Range 5.0 is 5.0 V ± 0.5 V

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Capacitance

Symbol	Parameter	54/74AC/ACT		Units	Conditions
		Typ			
CIN	Input Capacitance	4.5	pF	Vcc = 5.5 V	
CPD	Power Dissipation Capacitance	30.0	pF	Vcc = 5.5 V	