

N-CHANNEL SILICON FIELD-EFFECT TRANSISTORS

General purpose symmetrical N-channel planar epitaxial junction field-effect transistors in a plastic TO-92 variant; intended for applications in I.f. and d.c. amplifiers, and in h.f. amplifiers.

QUICK REFERENCE DATA

Drain-source voltage	$\pm V_{DS}$	max.	30	V			
Gate-source voltage (open drain)	$-V_{GSO}$	max.	30	V			
Total power dissipation up to $T_{amb} = 75^\circ\text{C}$	P_{tot}	max.	300	mW			
Drain current $V_{DS} = 15\text{ V}; V_{GS} = 0$	I_{DSS}	BF245A/0	A	B	C		
		>	0,5	2,0	6	12	mA
		<	2,1	6,5	15	25	mA
Gate-source cut-off voltage $I_D = 10\text{ nA}; V_{DS} = 15\text{ V}$	$-V_{(P)GS}$			0,25 to 8,0	V		
Feedback capacitance at $f = 1\text{ MHz}$ $V_{DS} = 20\text{ V}; -V_{GS} = 1\text{ V}; T_{amb} = 25^\circ\text{C}$	C_{rs}	typ.	1,1	pF			
Transfer admittance (common source) $V_{DS} = 15\text{ V}; V_{GS} = 0; f = 1\text{ kHz}; T_{amb} = 25^\circ\text{C}$	$ Y_{fs} $		3,0 to 6,5	mS			

MECHANICAL DATA

Dimensions in mm

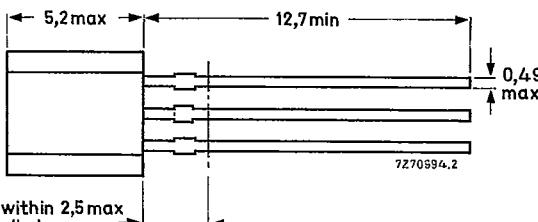
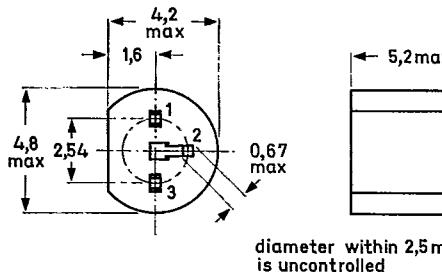
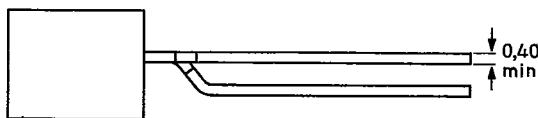
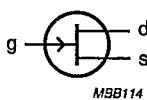
Fig. 1 TO-92 variant.

Pinning:

1 = drain

2 = source

3 = gate



Note: Drain and source are interchangeable

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Drain-source voltage	$\pm V_{DS}$	max.	30 V
Drain-gate voltage (open source)	V_{DGO}	max.	30 V
Gate-source voltage (open drain)	$-V_{GSO}$	max.	30 V
Drain current	I_D	max.	25 mA
Gate current	I_G	max.	10 mA
Power dissipation up to $T_{amb} = 75^\circ\text{C}$	P_{tot}	max.	300 mW
up to $T_{amb} = 90^\circ\text{C}$	P_{tot}	max.	300 mW 1)
Storage temperature	T_{stg}	—	—65 to + 150 °C
Junction temperature	T_j	max.	150 °C

THERMAL RESISTANCE

From junction to ambient in free air	$R_{th\ j-a}$	=	250 K/W
From junction to ambient	$R_{th\ j-a}$	=	200 K/W

CHARACTERISTICS $T_j = 25^\circ\text{C}$ unless otherwise specified

		BF245A	B	C
Gate cut-off current $-V_{GS} = 20 \text{ V}; V_{DS} = 0$	$-I_{GSS}$	< 5	5	5 nA
$-V_{GS} = 20 \text{ V}; V_{DS} = 0; T_j = 125^\circ\text{C}$	$-I_{GSS}$	< 0,5	0,5	0,5 μA
Drain current 2) $V_{DS} = 15 \text{ V}; V_{GS} = 0$	I_{DSS} 3)	> 2 < 6,5	6,0 15,0	12 mA 25 mA
Gate-source breakdown voltage $-I_G = 1 \mu\text{A}; V_{DS} = 0$	$-V_{(BR)GSS}$	> 30	30	30 V
Gate-source voltage $I_D = 200 \mu\text{A}; V_{DS} = 15 \text{ V}$	$-V_{GS}$ 3)	> 0,4 < 2,2	1,6 3,8	3,2 V 7,5 V

1) Transistor mounted on printed-circuit board, maximum lead length 3 mm, mounting pad for drain lead minimum 10 mm x 10 mm.

2) Measured under pulse conditions: $t_p = 300 \mu\text{s}; \delta \leq 0,02$.

3) BF245A/0: $I_{DSS} = 0,5$ to 2,1 mA; $-V_{GS} = 0,2$ to 1,0 V

BF245A/1: $I_{DSS} = 1,9$ to 3,0 mA; $-V_{GS} = 0,4$ to 1,0 V

BF245A/2: $I_{DSS} = 3,0$ to 4,5 mA; $-V_{GS} = 0,7$ to 1,4 V

BF245A/3: $I_{DSS} = 4,5$ to 6,5 mA; $-V_{GS} = 1,1$ to 2,2 V.

Gate-source cut-off voltage

 $I_D = 10 \text{ nA}$; $V_{DS} = 15 \text{ V}$ $-V(P)GS$ 0,25 to 8,0 Vy-parameters at $T_{amb} = 25^\circ\text{C}$ (common source) $V_{DS} = 15 \text{ V}$; $V_{GS} = 0$ $f = 1 \text{ kHz}$

Transfer admittance

 $|y_{fs}|$ 3,0 to 6,5 mS

Output admittance

 $|y_{os}|$ typ. 25 μS $f = 200 \text{ MHz}$

Input conductance

 g_{is} typ. 250 μS

Reverse transfer admittance

 $|y_{rs}|$ typ. 1,4 mS

Transfer admittance

 $|y_{fs}|$ typ. 6 mS

Output conductance

 g_{os} typ. 40 μS $V_{DS} = 20 \text{ V}$; $-V_{GS} = 1 \text{ V}$ $f = 1 \text{ MHz}$

Input capacitance

 C_{is} typ. 4,0 pF

Feedback capacitance

 C_{rs} typ. 1,1 pF

Output capacitance

 C_{os} typ. 1,6 pF

Cut-off frequency*

 $V_{DS} = 15 \text{ V}$; $V_{GS} = 0$ f_{gfs} typ. 700 MHzNoise figure at $f = 100 \text{ MHz}$; $R_G = 1 \text{ k}\Omega$ (common source) $V_{DS} = 15 \text{ V}$; $V_{GS} = 0$; $T_{amb} = 25^\circ\text{C}$

F typ. 1,5 dB

input tuned to minimum noise

* The frequency at which g_{fs} is 0,7 of its value at 1 kHz.

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BF245A TO C

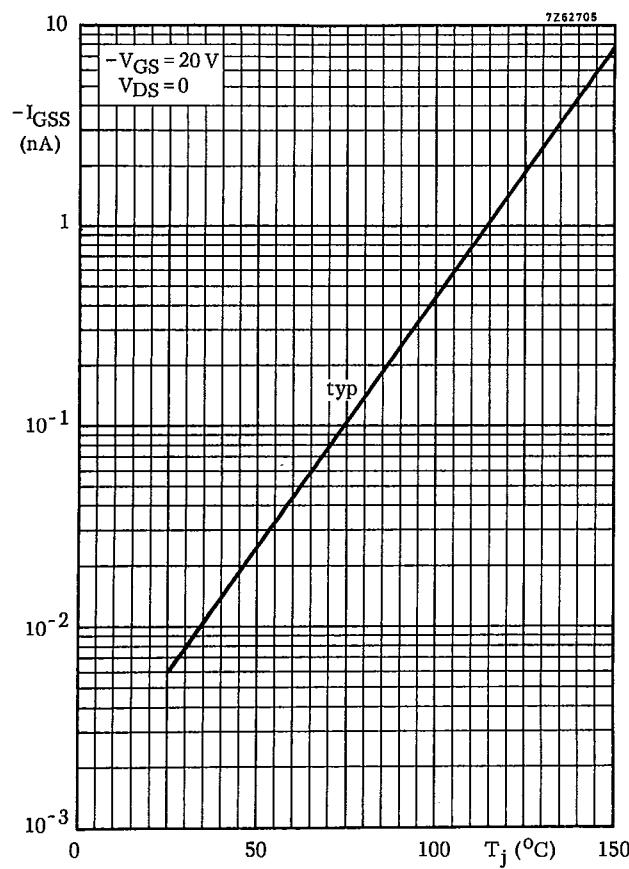


Fig. 2

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N-channel silicon field-effect transistors

BF245A TO C

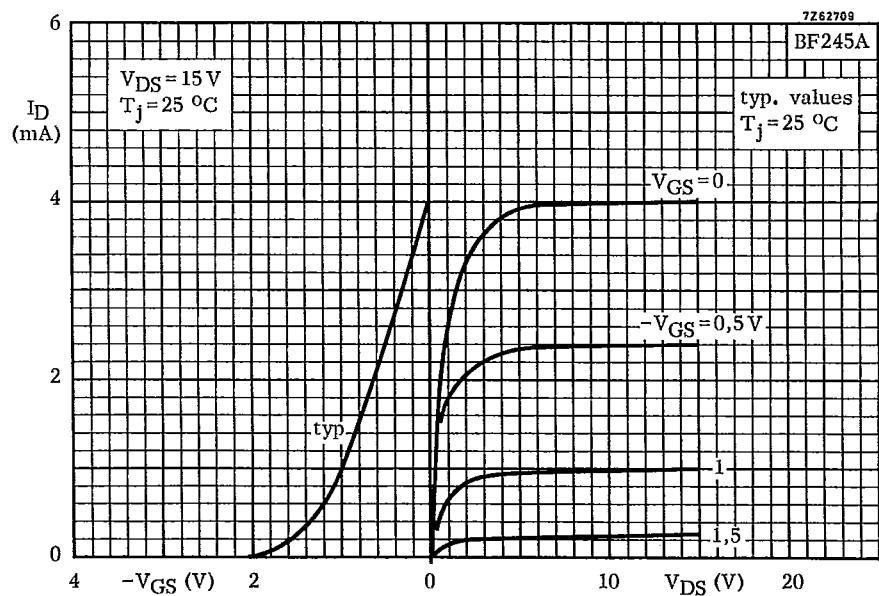


Fig. 3

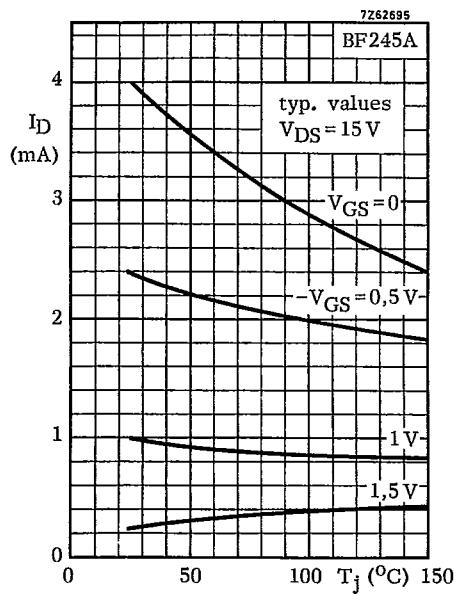
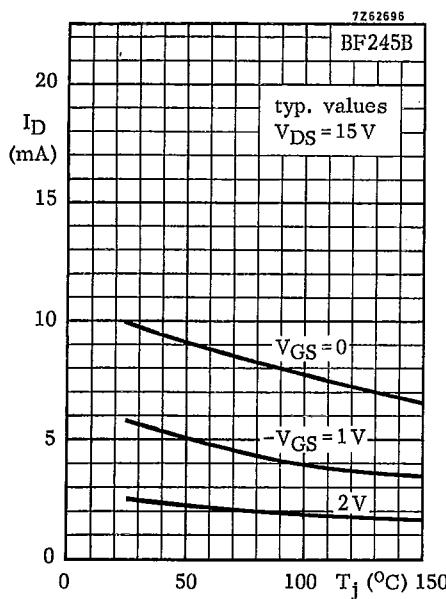
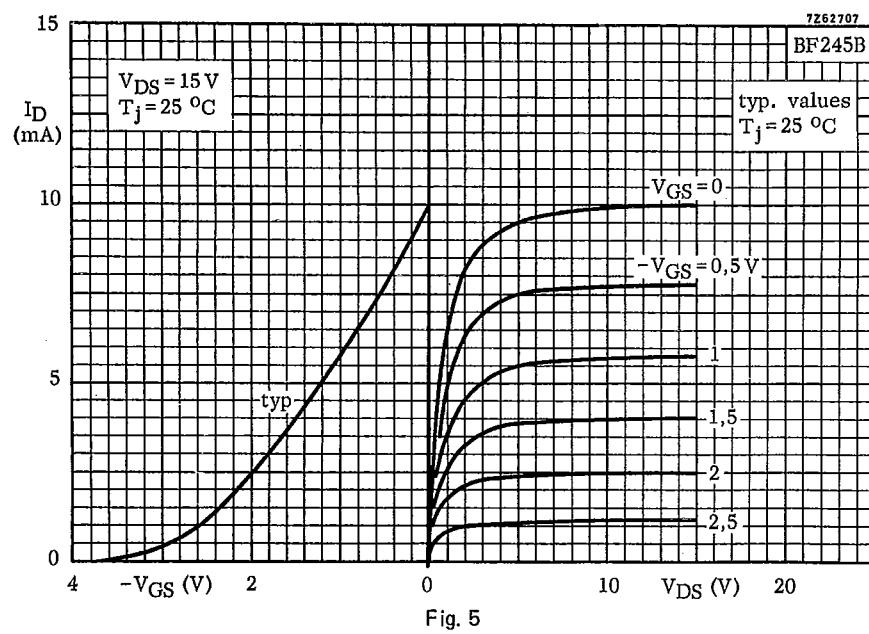


Fig. 4



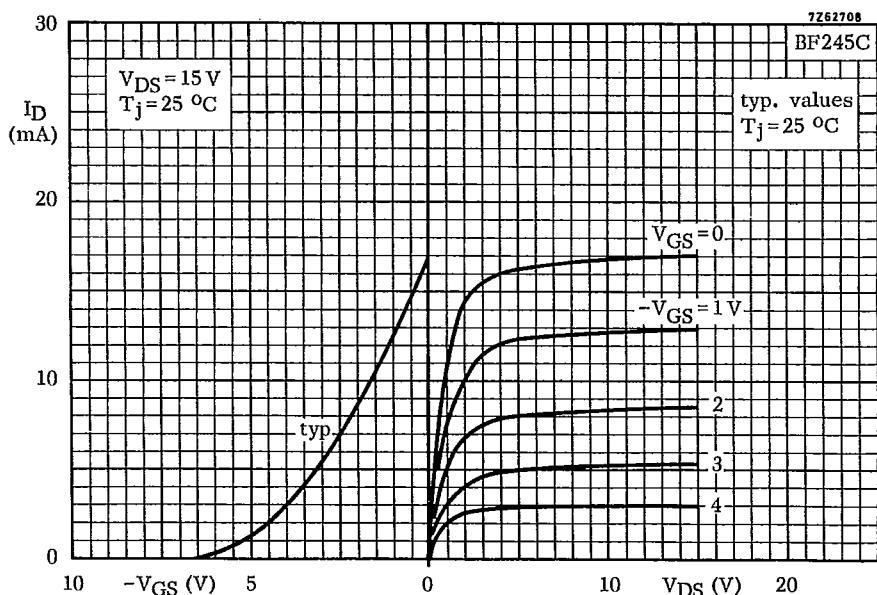


Fig. 7

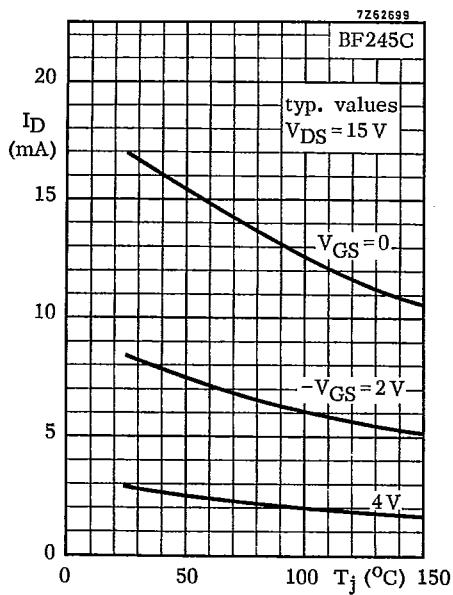


Fig. 8

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BF245A TO C

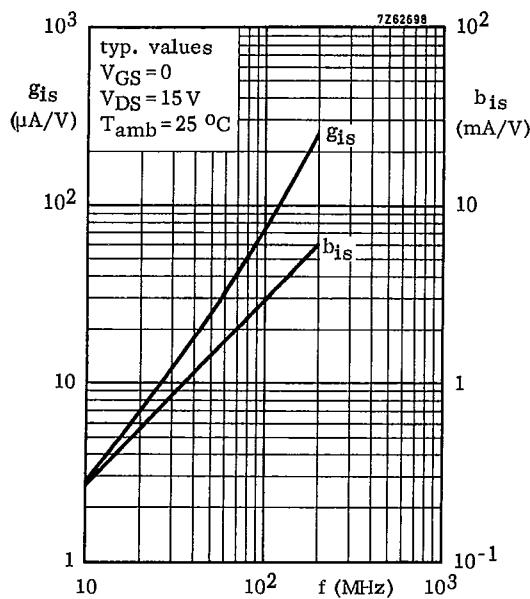


Fig. 9

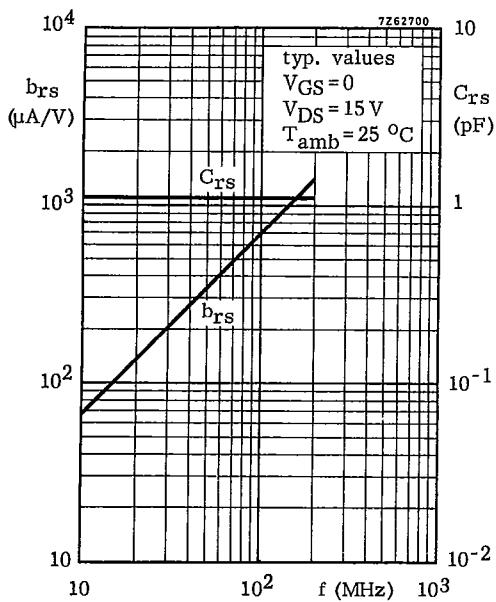


Fig. 10

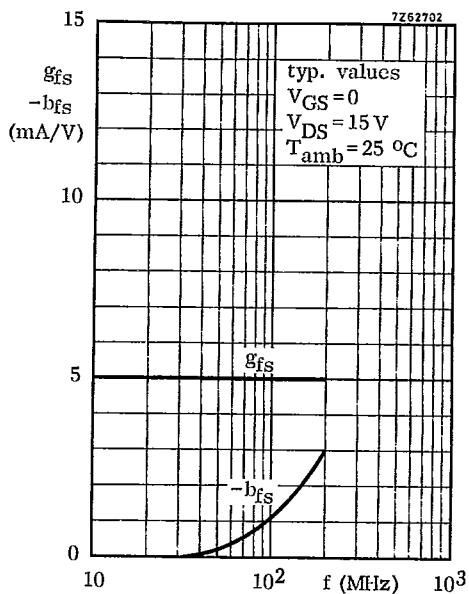


Fig. 11

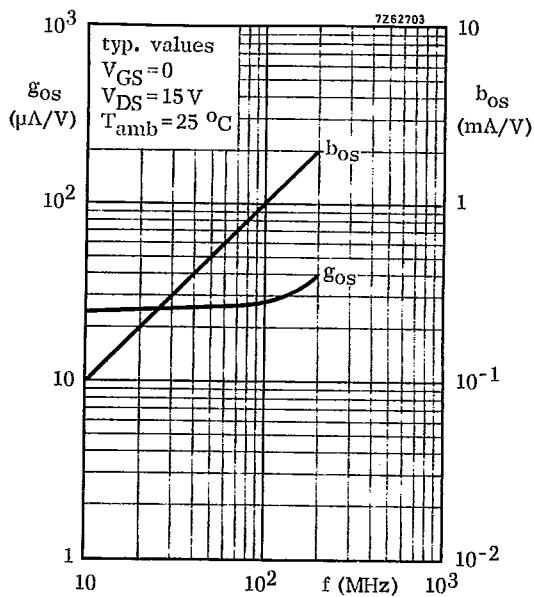


Fig. 12

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BF245A TO C

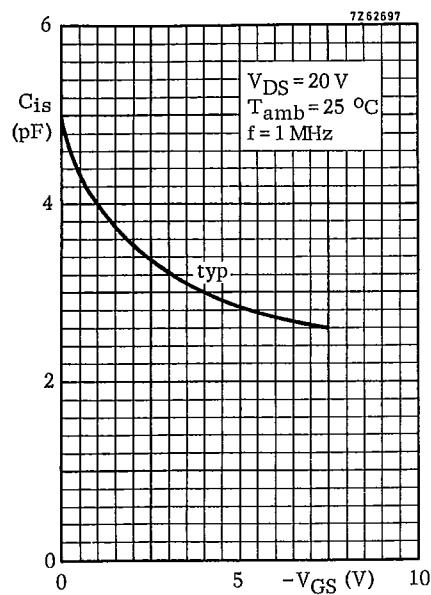


Fig. 13

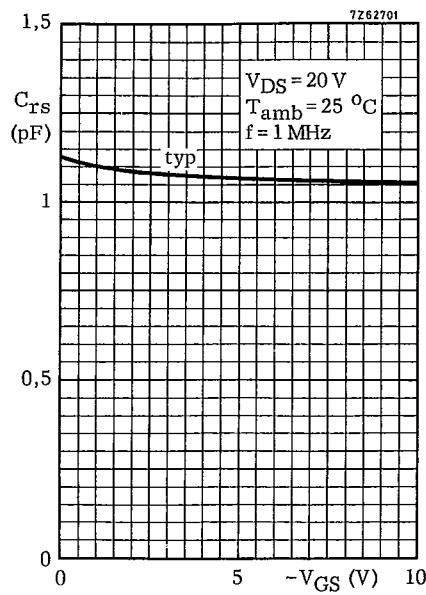


Fig. 14

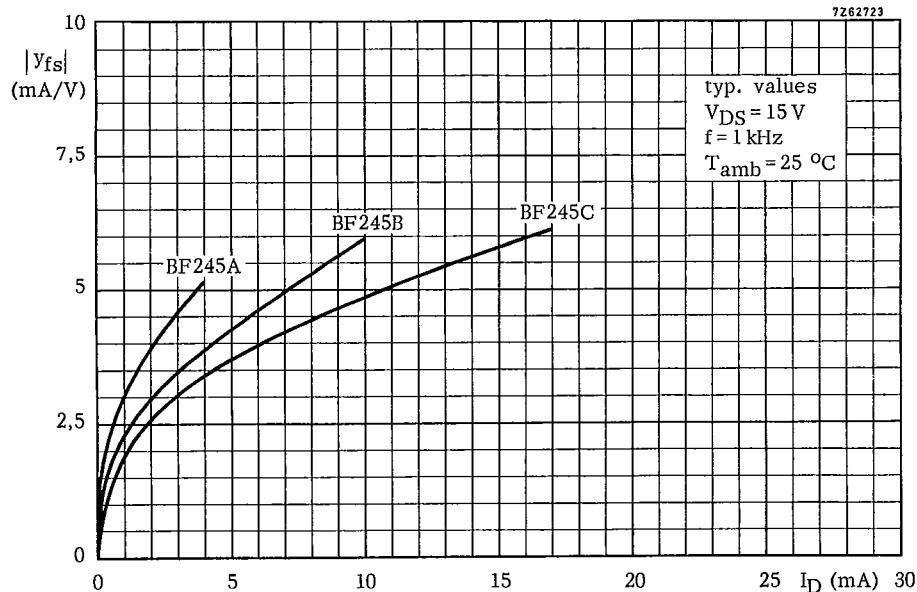


Fig. 15

