

## -8 Power Supply ICs

### Voltage regulators

Part number	Input voltage (V-max.)	Output voltage (V)	Output current (mA-typ.)	Current consumption ( $\mu$ A-typ.)	Package
SCI7710YHA	15	1.5	10 ( $V_I=3V$ )	1.5	SOT89-3pin
SCI7710YGA		1.8	10 ( $V_I=3V$ )	1.6	
SCI7710YFA		2.2	10 ( $V_I=3V$ )	1.8	
SCI7710YLA		2.6	30 ( $V_I=5V$ )	2.0	
SCI7710YDA		3.0	30 ( $V_I=5V$ )	2.0	
SCI7710YCA		3.2	30 ( $V_I=5V$ )	2.0	
SCI7710YNA		3.5	50 ( $V_I=6V$ )	2.2	
SCI7710YKA		3.9	40 ( $V_I=6V$ )	2.2	
SCI7710YMA		4.5	50 ( $V_I=7V$ )	2.4	
SCI7710YBA		5.0	50 ( $V_I=7V$ )	2.4	
SCI7710YAA		6.0	50 ( $V_I=7V$ )	2.4	
SCI7711YBA	-15	-5.0	50 ( $V_I=-7V$ )	1.4	
SCI7711YPA		-4.0	30 ( $V_I=-6V$ )	1.2	
SCI7711YDA		-3.0	30 ( $V_I=-5V$ )	1.0	
SCI7711YGA		-1.8	30 ( $V_I=-3V$ )	1.0	

### Voltage detectors

Part number	Output level	Detection voltage level (V)			Operating voltage range (V)	Current consumption ( $\mu$ A-typ.)	Package
		min.	typ.	max.			
SCI7700YVA	N-channel open drain	0.9	0.95	1.00	0.8-5.0	1.4 ( $V_{DD}=1.5V$ )	SOT89-3pin
SCI7700YAA		1.00	1.05	1.10	0.9-5.0	1.4 ( $V_{DD}=1.5V$ )	
SCI7700YAS		1.05	1.10	1.15	0.9-10.0	1.4 ( $V_{DD}=1.5V$ )	
SCI7700YBA		1.10	1.15	1.20	0.9-5.0	1.4 ( $V_{DD}=1.5V$ )	
SCI7700YNA		1.85	1.90	1.95	1.2-10.0	1.8 ( $V_{DD}=3.0V$ )	
SCI7700YCA		2.10	2.15	2.20	0.9-5.0	1.8 ( $V_{DD}=3.0V$ )	
SCI7700YFA		2.60	2.70	2.80	1.5-10.0	1.8 ( $V_{DD}=3.0V$ )	
SCI7700YTA		3.80	4.00	4.20	1.5-10.0	2.6 ( $V_{DD}=6.0V$ )	
SCI7701YCA	CMOS	2.10	2.15	2.20	1.5-10.0	1.8 ( $V_{DD}=3.0V$ )	
SCI7701YCB#		2.10	2.15	2.20		1.8 ( $V_{DD}=3.0V$ )	
SCI7701YPA		2.20	2.25	2.30		1.8 ( $V_{DD}=3.0V$ )	
SCI7701YSA		2.30	2.35	2.40		1.8 ( $V_{DD}=3.0V$ )	
SCI7701YEA		2.50	2.55	2.60		1.8 ( $V_{DD}=3.0V$ )	
SCI7701YFA		2.60	2.70	2.80		1.8 ( $V_{DD}=3.0V$ )	
SCI7701YFB#		2.60	2.70	2.80		1.8 ( $V_{DD}=3.0V$ )	
SCI7701YRA		2.70	2.80	2.90		1.8 ( $V_{DD}=3.0V$ )	
SCI7701YGA		2.90	3.00	3.10		2.2 ( $V_{DD}=4.5V$ )	
SCI7701YHA		3.10	3.20	3.30		2.2 ( $V_{DD}=4.5V$ )	
SCI7701YTA		3.80	4.00	4.20		2.6 ( $V_{DD}=6.0V$ )	
SCI7701YMA		4.00	4.15	4.30		2.6 ( $V_{DD}=6.0V$ )	
SCI7701YJA		4.30	4.45	4.60		2.6 ( $V_{DD}=6.0V$ )	
SCI7701YKA		4.60	4.75	4.90		2.6 ( $V_{DD}=6.0V$ )	
SCI7701YLA		4.90	5.10	5.30		2.6 ( $V_{DD}=6.0V$ )	

# : Output polarity reversed versions

Note) Temperatures during reflow soldering must remain within the limits set out under LSI Device Precautions in this catalog. Do not immerse QFP and SOT89 packages during soldering, as the rapid temperature gradient during dipping can cause damage.