2SD2249

Silicon NPN epitaxial planer type

For low-frequency power amplification

Features

- Low collector to emitter saturation voltage V_{CE(sat)}.
- Satisfactory operation performances at high efficiency with the low-voltage power supply.
- Allowing supply with the radial taping.

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	40	V
Collector to emitter voltage	V_{CEO}	20	V
Emitter to base voltage	$V_{\rm EBO}$	7	V
Peak collector current	I_{CP}	8	A
Collector current	I_{C}	5	A
Collector power dissipation	P _C *	1	W
Junction temperature	T _j	150	°C
Storage temperature	T_{stg}	−55 ~ +150	°C

 $^{^{\}ast}$ Printed circuit board: Copper foil area of $1 cm^2$ or more, and the board thickness of 1.7mm for the collector portion

Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 10V, I_E = 0$			0.1	μА
Emitter cutoff current	I_{EBO}	$V_{EB} = 7V, I_{C} = 0$			0.1	μА
Collector to emitter voltage	V _{CEO}	$I_{C} = 1 \text{mA}, I_{B} = 0$	20			V
Emitter to base voltage	V _{EBO}	$I_E = 10 \mu A, I_C = 0$	7			V
Forward current transfer ratio	h _{FE1} *1	$V_{CE} = 2V, I_C = 0.5A^{*2}$	230		600	
	h _{FE2}	$V_{CE} = 2V, I_C = 2A^{*2}$	150			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_C = 3A, I_B = 0.1A^{*2}$		0.3	1	V
Transition frequency	f_T	$V_{CB} = 6V, I_E = -50 \text{mA}, f = 200 \text{MHz}^{*2}$		150		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 20V, I_E = 0, f = 1MHz$			50	pF

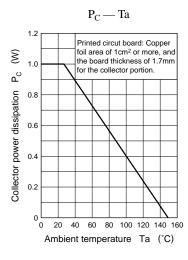
^{*2} Pulse measurement

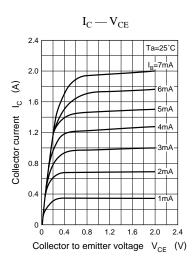
(HW type)

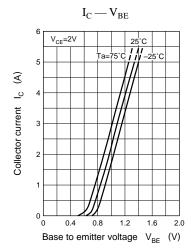
^{*1}h_{FE1} Rank classification

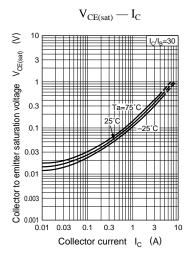
Rank	Q	R
h_{FE1}	230 ~ 380	340 ~ 600

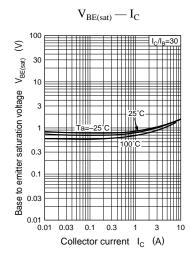
Transistor 2SD2249

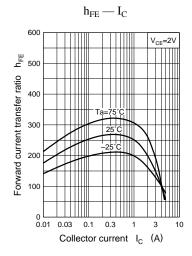


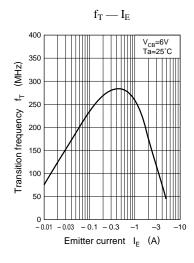


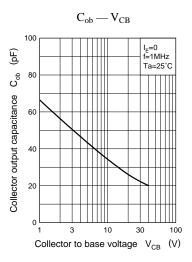












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