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Quad Analog Switches/Quad Multiplexers

RENESAS

ADE-205-538 (Z) 1st. Edition Sep. 2000

Description

This switch has low "on" resistance and low "off" leakage. It is a bidirectional switch, thus any analog input may be used as an output and vice-versa. Also the HD74HC4066 switch contains linearization circuitry which lowers the "on" resistance and increases switch linearity. The HD74HC4066 device allows control of up to 12 V (peak) analog signals with digital control signals of the same range. Each switch has its own control input which disables each switch when low.

Features

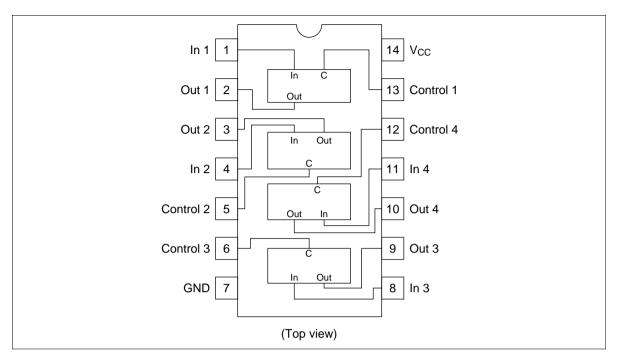
- High Speed Operation
- Wide Operating Voltage
- Low Quiescent Supply Current

Function Table

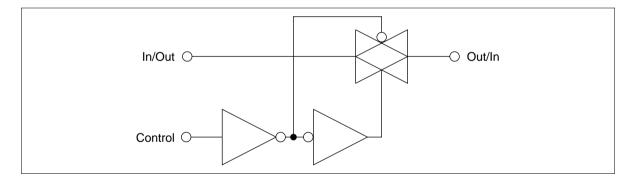
Control	Switch	
L	OFF	
Н	ON	
CND < Vin < V		

 $GND \le Vin \le V_{cc}$ $GND \le Vout \le V_{cc}$

Pin Arrangement



Logic Diagram (1/4)



Absolute Maximum Ratings

Item		Symbol	Rating	Unit
Supply voltage		V _{cc}	-0.5 to +7.0	V
Control input voltage	Control input voltage		V_{IN} –0.5 to V_{CC} + 0.5	
Switch I/O voltage	Switch I/O voltage		-0.5 to V_{cc} + 0.5	V
Supply current	(V _{cc})	I _{cc}	+50	mA
	(GND)		-50	mA
Switch I/O current (per p	Switch I/O current (per pin)		±25	mA
Control input diode curre	Control input diode current		±20	mA
Switch I/O diode current		I _{юк}	±20	mA
Power dissipation		P _T	500	mW
Storage temperature range		Tstg	-65 to +150	°C



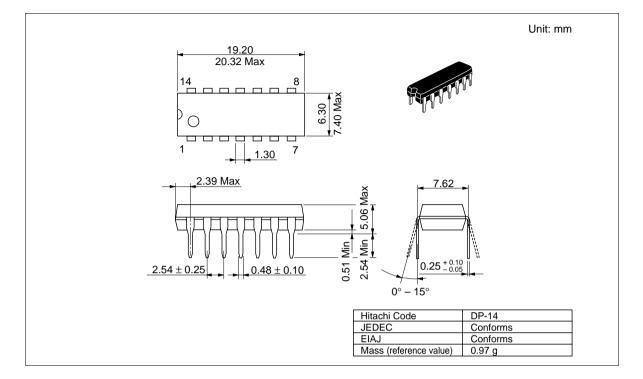
DC Characteristics

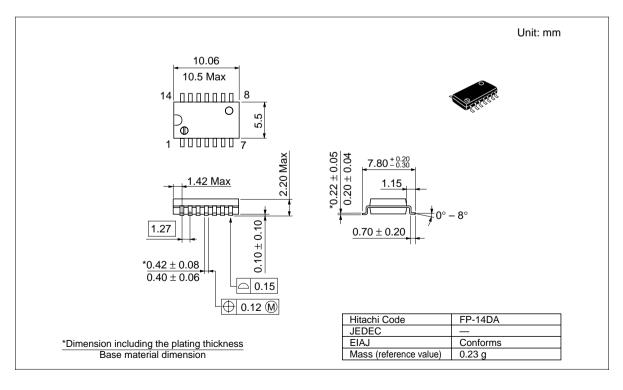
			Ta = 25°C		Ta = −40 to +85°C				
ltem	Symbol	V_{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Control input voltage	V _{IH}	2.0	1.5			1.5		V	
		4.5	3.15	—	—	3.15	—	_	
		6.0	4.2	_	_	4.2	_	_	
	VIL	2.0	—		0.5	—	0.5	V	
		4.5	—	—	1.35	—	1.35	_	
		6.0	—		1.8	—	1.8	_	
"ON" resistance	R _{on}	2.0	_	2000	5000	_	6250	Ω	$V_{\rm C} = V_{\rm IH}$
		4.5	—	100	200	—	250	_	Vin = 0 to V_{cc}
		6.0	—	60	170	—	210	_	lin/out = 1 mA
ΔON resistance	ΔR_{ON}	2.0	_	50	_	_		Ω	$V_{c} = V_{H}$, lin/out = 1 mA
between any two		4.5	—	3	—	—	—		between any two
channels		6.0	_	2	_	—	_	_	channels
OFF channel leakage current (switch off)	I _{S (OFF)}	6.0		_	±0.1	_	±1.0	μA	$\label{eq:V_c} \begin{split} V_{c} &= V_{\text{\tiny IL}} \\ V_{\text{\tiny IN}} &= V_{\text{\tiny CC}}, \text{ Vout = GND} \\ \text{or, Vin = GND,} \\ \text{Vout = } V_{\text{\tiny CC}} \end{split}$
OFF channel leakage current (switch on)	I _{S (ON)}	6.0	_	_	±0.1	_	±1.0	μA	$V_c = V_{H}$ Vin = V_{cc} or GND
Control input current	lin	6.0	_		±0.1	—	±1.0	μA	Vin = V_{cc} or GND
Quiescent supply current	I _{cc}	6.0	—	—	1.0	—	10.0	μA	Vin = V_{cc} or GND

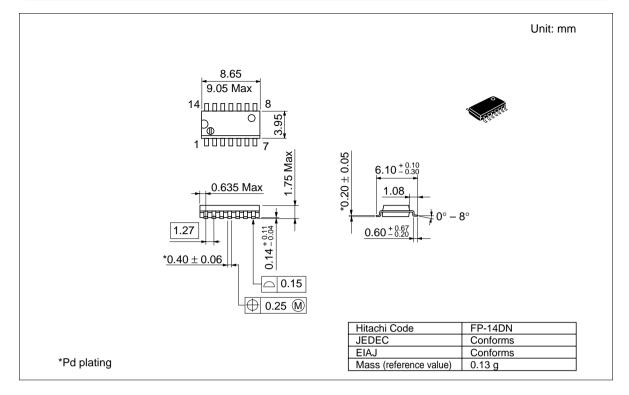
AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

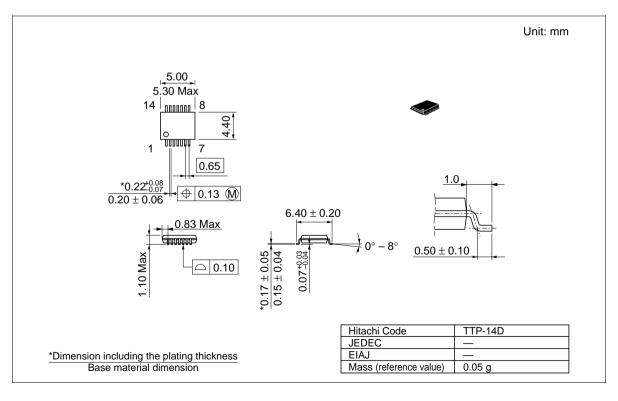
			Ta =	Ta = −40 = 25°C +85°C					
ltem	Symbol	V_{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	—	—	50		65	ns	$R_{L} = 10 \text{ k}\Omega$
time	t _{PHT}	4.5	—	4	10		13	_	
		6.0	—	—	9		11	_	
Output enable	t _{zH}	2.0	—	—	115		145	ns	$R_{L} = 1 k\Omega$
time		4.5	—	10	23		29	_	
		6.0	_	_	20	_	25	-	
Output disable	t _{LZ}	2.0	_	_	115	_	145	ns	$R_{L} = 1 k\Omega$
time	t _{HZ}	4.5	_	14	23	_	29		
		6.0	_	_	20	_	25		
Sine wave distortion		4.5	_	0.05	_		_	%	$\begin{aligned} R_{L} &= 10 \text{ k}\Omega, C_{L} = 50 \text{ pF}, \\ f_{IN} &= 1 \text{ kHz} \end{aligned}$
Band width (-3 dB)		4.5	—	30	_		_	MHz	$R_{L} = 600 \Omega, C_{L} = 50 pF,$ 20 log ₁₀ Vout/Vin = -3dB
Feedthrouth attenuation		4.5	—	-50	—	—	—	dB	$R_L = 600 \Omega, C_L = 50 pF,$ f _{IN} = 1 MHz
Cross talk between		2.0	_	25	_	_	_	mA	$R_{L} = 600 \ \Omega, \ C_{L} = 50 \ pF,$
control input to		4.5	_	60	_		_	-	$f_{IN} = 1 \text{ MHz}$
signal I/O		6.0	_	75	_		_	-	
Cross talk between any two switches		4.5	—	-50	—		—	dB	$\begin{aligned} \textbf{R}_{L} &= 600 \ \Omega, \ \textbf{C}_{L} &= 50 \ \textbf{pF}, \\ \textbf{f}_{\text{IN}} &= 1 \ \textbf{MHz} \end{aligned}$
Maximum control		2.0	_	20		_	_	MHz	$R_{L} = 1 \text{ k}\Omega, C_{L} = 15 \text{ pF},$
frequency		4.5	_	30	_	_	_	_	Vout = $1/2$ (V _{cc})
		6.0	_	30	_		_	-	
Control input capacitance	Cin		—	5	10	—	10	pF	
Switch I/O capacitance	Cin/out		_	6	—	—	—	pF	
Feed through capacitance	Cin/out		—	0.5	—	—	—	pF	
Power dissipation capacitance	C_{PD}			13				pF	

Package Dimensions









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