

SRA2211S

PNP Silicon Transistor

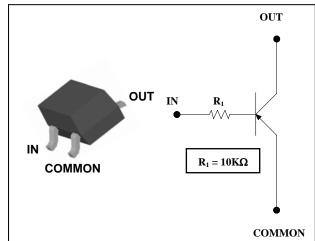
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRA2211S	<u>RAD</u> □ ① ②	SOT-23	_

①Device Code ②Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	Vı	-30, 5	V
Output current	I _O	-100	mA
Power dissipation	P_{D}	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	V _O =-5V, I _O =-10mA	120	-	-	-
Output voltage	V _{O(ON)}	I _O =-10mA, I _I =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_{O} = -0.2V$, $I_{O} = -5mA$	-	-0.9	-1.4	V
Input voltage (OFF)	V _{I(OFF)}	$V_0 = -5V$, $I_0 = -0.1$ mA	-0.3	-0.55	-	V
Transition frequency	f _T *	$V_O = -10V$, $I_O = -5$ mA, $f = 1$ MHz	-	200	_	MHz
Input current	I_1	$V_1 = -5V, I_0 = 0$	-	-	-0.88	mA
Input resistor (Input to base)	R ₁	-	7	10	13	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 Pc - Ta

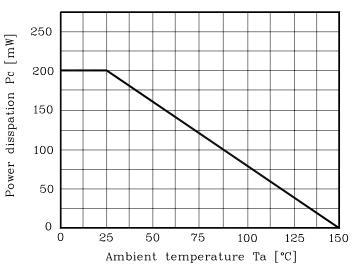


Fig. 2 I_O - $V_{I(ON)}$

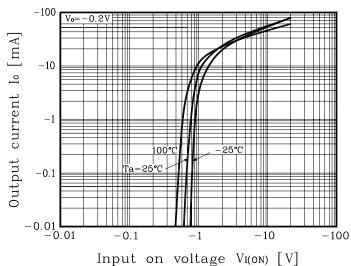


Fig. 3 $I_{\rm O}$ - $V_{\rm I(OFF)}$

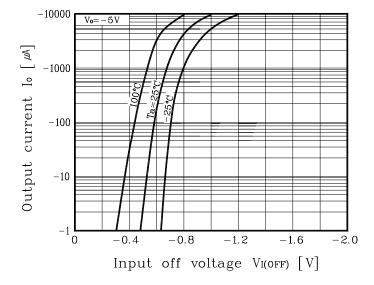
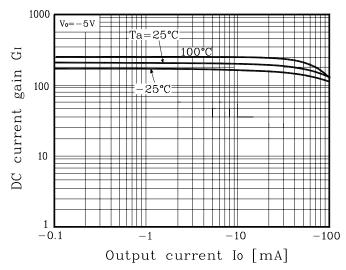
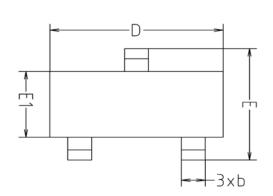
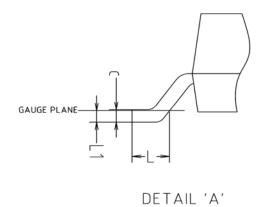


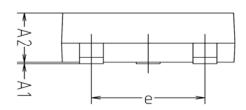
Fig. 4 G_I - I_O



Outline Dimension



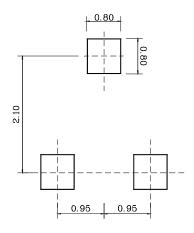






SYMBOL	MILLIMETERS			NOTE
STILLOCE	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
Ε	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

***Recommend PCB solder land [Unit: mm]**



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