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DMC366A3

Silicon NPN epitaxial planar type (Tr1)

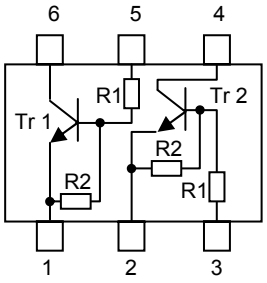
Silicon NPN epitaxial planar type (Tr2)

For digital circuits

Marking Symbol : J0

Package Code : SSSMini6-F2-B

Internal Connection



Resistance value	R1	47	kΩ
	R2	47	kΩ

Pin name

1.	Emitter(Tr1)	4.	Collector(Tr2)
2.	Emitter(Tr2)	5.	Base(Tr1)
3.	Base(Tr2)	6.	Collector(Tr1)

Absolute Maximum Ratings Ta = 25 °C

Parameter		Symbol	Rating	Unit
Tr1 Tr2	Collector-base voltage (Emitter open)	VCBO	50	V
	Collector-emitter voltage (Base open)	VCEO	50	V
	Collector current	IC	80	mA
Overall	Total power dissipation *1	PT	125	mW
	Junction temperature	Tj	150	°C
	Storage temperature	Tstg	-55 to +150	°C

Note: 1. *1 Measuring on substrate at 17 mm × 10 mm × 1 mm

Electrical Characteristics Ta = 25 °C±3 °C

Tr1,Tr2

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	VCBO	IC = 10 µA, IE = 0	50			V
Collector-emitter voltage (Base open) *1	VCEO	IC = 2 mA, IB = 0	50			V
Collector-base cutoff current (Emitter open)	ICBO	VCB = 50 V, IE = 0			0.1	µA
Collector-emitter cutoff current (Base open)	ICEO	VCE = 50 V, IB = 0			0.5	µA
Emitter-base cutoff current (Collector open)	IEBO	VEB = 6 V, IC = 0			0.1	mA
Forward current transfer ratio	hFE	VCE = 10 V, IC = 5 mA	80			-
Collector-emitter saturation voltage	VCE(sat)	IC = 10 mA, IB = 0.5 mA			0.25	V
Input voltage	Vi(on)	VCE = 0.2 V, IC = 5 mA	3.6			V
	Vi(off)	VCE = 5 V, IC = 100 µA			0.8	
Input resistance	R1		-30%	47	+30%	kΩ
Resistance ratio	R1/R2		0.8	1.0	1.2	-

Note: 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.
2. *1 Pulse measurement

Packing

Embossed type (Thermo-compression sealing) R specification : 10 000 pcs / reel

2010.3.15	2010.11.25	
Prepared	Revised	

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