



**High-Speed CMOS  
32Kx8 SRAM with  
Fast Chip Select**

**QS83289  
ADVANCE  
INFORMATION**

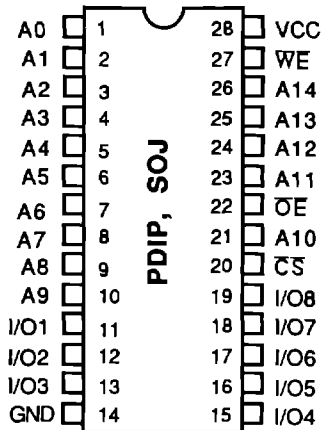
**FEATURES/BENEFITS**

- Equal access and cycle times
- 12ns/15ns/20ns/25/30ns Commercial
- 15ns/20ns/25/30ns Military
- Available in 28-pin 300/600-mil DIP, SOJ
- Military product compliant to MIL-STD-883
- Fast chip select access
- 6-Transistor cell for high reliability
- TTL compatible I/O
- High performance QCMOS™ technology

**DESCRIPTION**

The QS83289 is a high-speed 256K SRAM organized as 32K words of 8 bits. It achieves a fast chip select access time by not powering down the array when the chip is disabled. It is manufactured in a high-performance CMOS process, and it based on a 6-transistor cell design for high reliability of data retention. The high-speed access times of the QS83289 make it useful in cache data RAM, cache tag RAMs, high-speed scratchpad memories, look-up tables, pipelined DSP and bit-slice systems. Low operating power and excellent latch-up and ESD protection are provided.

**PIN CONFIGURATION**



**ALL PINS TOP VIEW**