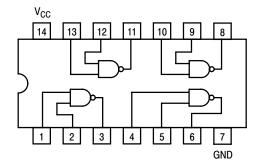
Quad 2-Input NAND Gate

• ESD > 3500 Volts



GUARANTEED OPERATING RANGES

Symbol	Parameter	Min	Тур	Max	Unit			14
V _{CC}	Supply Voltage	4.75	5.0	5.25	V		$\langle \rangle$	
Τ _Α	Operating Ambient Temperature Range	0	25	70	°C	20		6
I _{OH}	Output Current – High			-0.4	mA	5		R
I _{OL}	Output Current – Low			8.0	mA			U
	PLE P	SHA	RE	C .	ATIVE			DRDE
	Ÿ						Devid	e
						SN	74LS00I	N
						SN	74LS00I	C



ON Semiconductor™

http://onsemi.com

LOW POWER SCHOTTKY



N SUFFIX CASE 646



SOIC D SUFFIX CASE 751A



SOEIAJ M SUFFIX CASE 965

ORDERING INFORMATION

Device	Package	Shipping		
SN74LS00N	14 Pin DIP	2000 Units/Box		
SN74LS00D	SOIC-14	55 Units/Rail		
SN74LS00DR2	SOIC-14	2500/Tape & Reel		
SN74LS00M	SOEIAJ-14	See Note 1		
SN74LS00MEL	SOEIAJ-14	See Note 1		

 For ordering information on the EIAJ version of the SOIC package, please contact your local ON Semiconductor representative.

SN74LS00

		Limits						
Symbol	Parameter	Min	Тур	Max	Unit	Test C	onditions	
V _{IH}	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage for All Inputs		
V _{IL}	Input LOW Voltage			0.8	V	Guaranteed Input LOW Voltage for All Inputs		
V _{IK}	Input Clamp Diode Voltage		-0.65	-1.5	V	V _{CC} = MIN, I _{IN} = – 18 mA		
V _{OH}	Output HIGH Voltage	2.7	3.5		V	$\label{eq:VCC} \begin{array}{l} V_{CC} = MIN, \ I_{OH} = MAX, \ V_{IN} = V_{IH} \\ \text{ or } V_{IL} \ \text{per Truth Table} \end{array}$		
V _{OL}	Output LOW Voltage		0.25	0.4	V	I _{OL} = 4.0 mA	$V_{CC} = V_{CC} MIN,$ $V_{IN} = V_{IL} \text{ or } V_{IH}$ per Truth Table	
			0.35	0.5	V	I _{OL} = 8.0 mA		
				20	μΑ	$V_{CC} = MAX, V_{IN} = 2.7 V$		
I _{IH}	put HIGH Current			0.1	mA	$V_{CC} = MAX, V_{IN} = 7.0 V$		
IIL	Input LOW Current			-0.4	mA	$V_{CC} = MAX, V_{IN} = 0.4 V$		
I _{OS}	Short Circuit Current (Note 2)	-20		-100	mA	V _{CC} = MAX		
	Power Supply Current							
I _{CC}	Total, Output HIGH			1.6	mA	V _{CC} = MAX	~	
	Total, Output LOW			4.4		07.10		

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

2. Not more than one output should be shorted at a time, nor for more than 1 second.

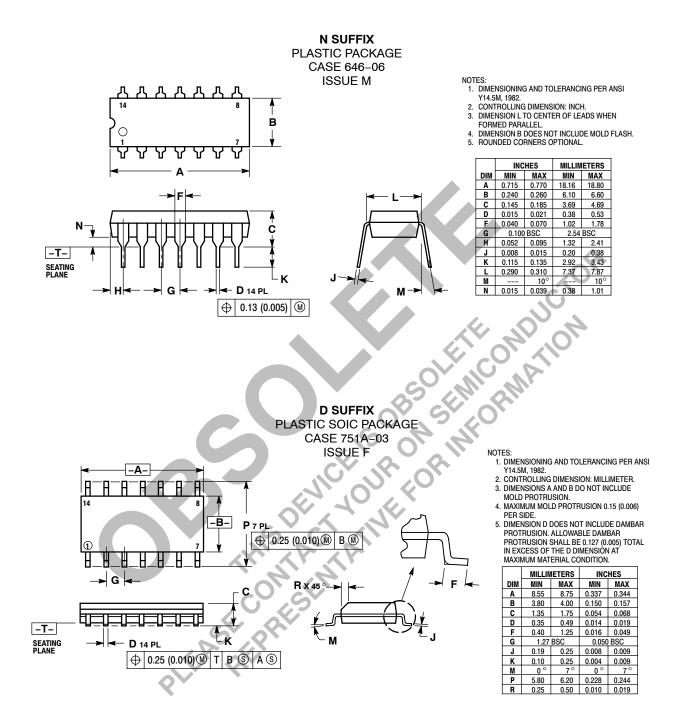
AC CHARACTERISTICS ($T_A = 25^{\circ}C$)

2. Not more than one output should be shorted at a time, nor for more than 1 second.						
AC CHARACTERISTICS (T _A = 25°C)						
			Limits	0	3.7	
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t _{PLH}	Turn-Off Delay, Input to Output		9.0	15	ns	V _{CC} = 5.0 V C _L = 15 pF
t _{PHL}	Turn-On Delay, Input to Output		10	15	ns	C _L = 15 pF

PI-FASE PRESENT

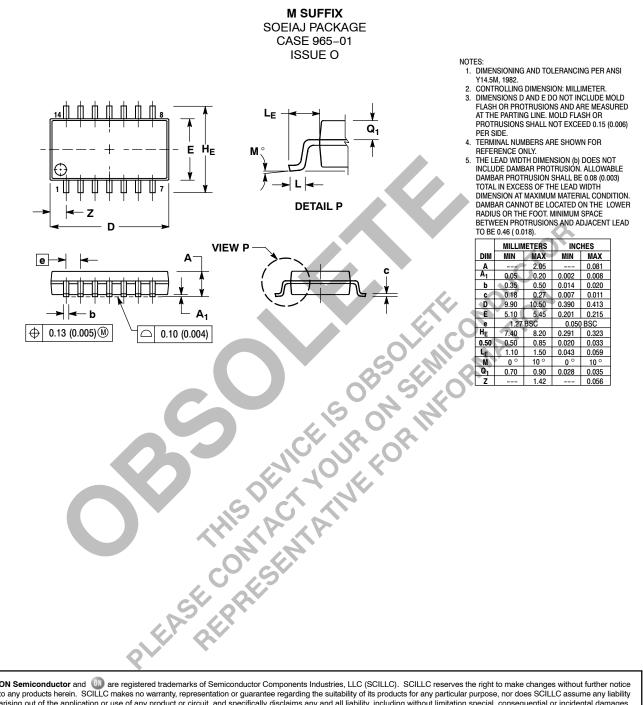
SN74LS00

PACKAGE DIMENSIONS



SN74LS00

PACKAGE DIMENSIONS



ON Semiconductor and I are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other application in which the SCILLC product create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use persons, and reasonable attorney fees and in or for esale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5773–3850 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative