

NEC

# BIPOLAR ANALOG INTEGRATED CIRCUIT

# $\mu$ PC78L00 SERIES

## THREE TERMINAL POSITIVE VOLTAGE REGULATORS

### DESCRIPTION

The  $\mu$ PC78L00 series are monolithic three terminal positive regulators which employ internally current limiting, thermal shut down, output transistor safe area protection make them essentially indestructible.

They are intended as fixed voltage regulators in a wide range of application including local on card regulation for elimination of distribution problems associated wide single point regulation.

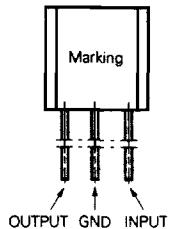
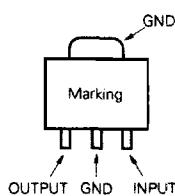
### FEATURES

- Output current in excess of 100 mA.
- Low noise.
- High Ripple Rejection.
- Internal output transistor safe area protection.
- Internal thermal overload protection.
- Internal short circuit current limiting.

### ORDER INFORMATION

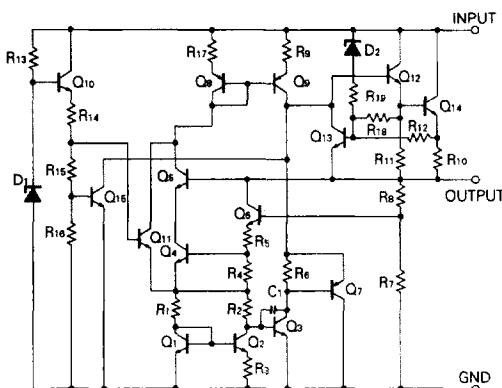
Type Number	Output Voltage	Package	Quality Grade
$\mu$ PC78L06J	5 V	TO-92	Standard
$\mu$ PC78L05T		SOT-89	
$\mu$ PC78L06J		TO-92	
$\mu$ PC78L06T		SOT-89	
$\mu$ PC78L07J		TO-92	
$\mu$ PC78L07T		SOT-89	
$\mu$ PC78L08J		TO-92	
$\mu$ PC78L08T		SOT-89	
$\mu$ PC78L10J		TO-92	
$\mu$ PC78L10T		SOT-89	
$\mu$ PC78L12J		TO-92	
$\mu$ PC78L12T		SOT-89	
$\mu$ PC78L15J		TO-92	
$\mu$ PC78L15T		SOT-89	

### CONNECTION DIAGRAM

 $\mu$ PC78L00J Series $\mu$ PC78L00T Series

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

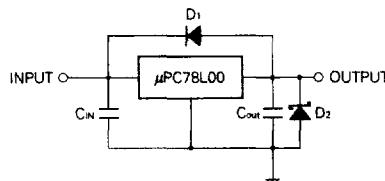
## EQUIVALENT CIRCUIT

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

PARAMETER	SYMBOL	RATINGS		UNIT
		$\mu$ PC78L00J Series	$\mu$ PC78L00T Series	
Input Voltage	$V_{IN}$	30/35 (Note1)		V
Internal Power Dissipation	$P_T$	700	400/2000 (Note2)	mW
Operating Ambient Temperature Range	$T_{opt}$	-20 to +85		°C
Operating Junction Temperature Range	$T_{opt(j)}$	-20 to +150		°C
Storage Temperature Range	$T_{stg}$	-55 to +150		°C
Thermal Resistance (junction to ambient)	$R_{th(j-a)}$	180	315/62.5 (Note2)	°C/W

(Note 1)  $\mu$ PC78L05, 06, 07, 08 : 30 V,  $\mu$ PC78L10, 12, 15 : 35 V(Note 2) with 2.5 cm<sup>2</sup> × 0.7 mm ceramic substrate

## TYPICAL CONNECTION



C1: Required if regulator is located an appreciable distance from power supply filter

C2: More than 0.1  $\mu$ FD1: Needed for  $V_{IN} < V_O$ D2: Needed for  $V_O < GND$

## RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	TYPE NUMBER	MIN.	TYP.	MAX.	UNIT
Input Voltage	$V_{IN}$	$\mu$ PC78L05	7	10	20	V
		$\mu$ PC78L06	8.5	11	21	
		$\mu$ PC78L07	9.5	12	22	
		$\mu$ PC78L08	10.5	14	23	
		$\mu$ PC78L10	12.5	17	25	
		$\mu$ PC78L12	14.5	19	27	
		$\mu$ PC78L15	17.5	23	30	
Output Current	$I_O$	All	0	40	70	mA
Operating Temperature Range	$T_{OPT}$	All	-20		+85	°C
Operating Junction Temperature Range	$T_{OPT(j)}$	All	-20		+125	°C

ELECTRICAL CHARACTERISTICS  $\mu$ PC78L05

(VIN = 10 V, IO = 40 mA, 0 °C ≤ TJ ≤ +125 °C, CIN = 0.33 μF, COUT = 0.1 μF)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Output Voltage	$V_O$	$T_J = 25^\circ C$	4.8	5.0	5.2	V	
		$7 V \leq V_{IN} \leq 20 V, 1 mA \leq I_O \leq 40 mA$	4.75		5.25		
		$V_{IN} = 10 V, 1 mA \leq I_O \leq 70 mA$	4.75		5.25		
Line Regulation	$REG_{IN}$	$T_J = 25^\circ C, 7 V \leq V_{IN} \leq 20 V$		6	150	mV	
		$T_J = 25^\circ C, 8 V \leq V_{IN} \leq 20 V$		4	100		
Load Regulation	$REG_L$	$T_J = 25^\circ C, 1 mA \leq I_O \leq 100 mA$		9	60	mV	
		$T_J = 25^\circ C, 1 mA \leq I_O \leq 40 mA$		4	30		
Quiescent Current	$I_{BIAS}$	$T_J = 25^\circ C$		2.3	5.5	mA	
Quiescent Current Change	$\Delta I_{BIAS}$	$8 V \leq V_{IN} \leq 20 V, I_O = 40 mA$			1.5	mA	
		$V_{IN} = 10 V, 1 mA \leq I_O \leq 40 mA$			0.1		
Output Noise Voltage	$V_n$	$T_J = 25^\circ C, 10 Hz \leq f \leq 100 kHz$		45	120	$\mu V_{rms}$	
Ripple Rejection	$R \cdot R$	$T_J = 25^\circ C, f = 120 Hz, 8 V \leq V_{IN} \leq 18 V$	55	75		dB	
Dropout Voltage	$V_{DIF}$	$T_J = 25^\circ C$			1.7	V	
Short Circuit Current	$I_{short}$	$T_J = 25^\circ C, V_{IN} = 20 V$		88		mA	
Peak Output Current	$I_{peak}$	$T_J = 25^\circ C$		125	160	205	mA
Temperature coefficient of Output Voltage	$\Delta V_O / \Delta T$	$I_O = 5 mA$			0.4	$mV/^{\circ}C$	

ELECTRICAL CHARACTERISTICS  $\mu$ PC78L06(V<sub>IN</sub> = 11 V, I<sub>O</sub> = 40 mA, 0 °C ≤ T<sub>j</sub> ≤ +125 °C, C<sub>IN</sub> = 0.33 μF, C<sub>OUT</sub> = 0.1 μF)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Output Voltage	V <sub>O</sub>	T <sub>j</sub> = 25 °C	5.76	6.0	6.24	V	
		8.5 V ≤ V <sub>IN</sub> ≤ 21 V, 1 mA ≤ I <sub>O</sub> ≤ 40 mA	5.70		6.30		
		V <sub>IN</sub> = 11 V, 1 mA ≤ I <sub>O</sub> ≤ 70 mA	5.70		6.30		
Line Regulation	REG <sub>IN</sub>	T <sub>j</sub> = 25 °C, 8.5 V ≤ V <sub>IN</sub> ≤ 21 V		6	155	mV	
		T <sub>j</sub> = 25 °C, 9 V ≤ V <sub>IN</sub> ≤ 21 V		4	105		
Load Regulation	REG <sub>L</sub>	T <sub>j</sub> = 25 °C, 1 mA ≤ I <sub>O</sub> ≤ 100 mA		10	65	mV	
		T <sub>j</sub> = 25 °C, 1 mA ≤ I <sub>O</sub> ≤ 40 mA		4	35		
Quiescent Current	I <sub>BIA</sub> S	T <sub>j</sub> = 25 °C		2.3	5.5	mA	
Quiescent Current Change	ΔI <sub>BIA</sub> S	9 V ≤ V <sub>IN</sub> ≤ 21 V, I <sub>O</sub> = 40 mA			1.5	mA	
		V <sub>IN</sub> = 11 V, 1 mA ≤ I <sub>O</sub> ≤ 40 mA			0.1		
Output Noise Voltage	V <sub>N</sub>	T <sub>j</sub> = 25 °C, 10 Hz ≤ f ≤ 100 kHz		55	145	μV <sub>r.m.s.</sub>	
Ripple Rejection	R · R	T <sub>j</sub> = 25 °C, f = 120 Hz, 9 V ≤ V <sub>IN</sub> ≤ 19 V	54	75		dB	
Dropout Voltage	V <sub>DIF</sub>	T <sub>j</sub> = 25 °C			1.7	V	
Short Circuit Current	I <sub>short</sub>	T <sub>j</sub> = 25 °C, V <sub>IN</sub> = 21 V		85		mA	
Peak Output Current	I <sub>peak</sub>	T <sub>j</sub> = 25 °C		125	160	205	mA
Temperature coefficient of Output Voltage	ΔV <sub>O</sub> /ΔT	I <sub>O</sub> = 5 mA			0.5	mV/°C	

ELECTRICAL CHARACTERISTICS  $\mu$ PC78L07(V<sub>IN</sub> = 12 V, I<sub>O</sub> = 40 mA, 0 °C ≤ T<sub>j</sub> ≤ +125 °C, C<sub>IN</sub> = 0.33 μF, C<sub>OUT</sub> = 0.1 μF)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Output Voltage	V <sub>O</sub>	T <sub>j</sub> = 25 °C	6.72	7.0	7.28	V	
		8.5 V ≤ V <sub>IN</sub> ≤ 22 V, 1 mA ≤ I <sub>O</sub> ≤ 40 mA	6.65		7.35		
		V <sub>IN</sub> = 12 V, 1 mA ≤ I <sub>O</sub> ≤ 70 mA	6.65		7.35		
Line Regulation	REG <sub>IN</sub>	T <sub>j</sub> = 25 °C, 9.5 V ≤ V <sub>IN</sub> ≤ 22 V		8	165	mV	
		T <sub>j</sub> = 25 °C, 10 V ≤ V <sub>IN</sub> ≤ 22 V		5	115		
Load Regulation	REG <sub>L</sub>	T <sub>j</sub> = 25 °C, 1 mA ≤ I <sub>O</sub> ≤ 100 mA		12	75	mV	
		T <sub>j</sub> = 25 °C, 1 mA ≤ I <sub>O</sub> ≤ 40 mA		5	35		
Quiescent Current	I <sub>BIA</sub> S	T <sub>j</sub> = 25 °C		2.3	5.5	mA	
Quiescent Current Change	ΔI <sub>BIA</sub> S	10 V ≤ V <sub>IN</sub> ≤ 22 V, I <sub>O</sub> = 40 mA			1.5	mA	
		V <sub>IN</sub> = 12 V, 1 mA ≤ I <sub>O</sub> ≤ 40 mA			0.1		
Output Noise Voltage	V <sub>N</sub>	T <sub>j</sub> = 25 °C, 10 Hz ≤ f ≤ 100 kHz		70	180	μV <sub>r.m.s.</sub>	
Ripple Rejection	R · R	T <sub>j</sub> = 25 °C, f = 120 Hz, 10 V ≤ V <sub>IN</sub> ≤ 20 V	52	74		dB	
Dropout Voltage	V <sub>DIF</sub>	T <sub>j</sub> = 25 °C			1.7	V	
Short Circuit Current	I <sub>short</sub>	T <sub>j</sub> = 25 °C, V <sub>IN</sub> = 22 V		83		mA	
Peak Output Current	I <sub>peak</sub>	T <sub>j</sub> = 25 °C		125	160	205	mA
Temperature coefficient of Output Voltage	ΔV <sub>O</sub> /ΔT	I <sub>O</sub> = 5 mA			0.6	mV/°C	

ELECTRICAL CHARACTERISTICS  $\mu$ PC78L08

(VIN = 14 V, IO = 40 mA, 0 °C ≤ TJ ≤ +125 °C, CIN = 0.33 μF, COUT = 0.1 μF)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Output Voltage	VO	Tj = 25 °C	7.7	8.0	8.3	V	
		10.5 V ≤ VIN ≤ 23 V, 1 mA ≤ IO ≤ 40 mA	7.6		8.4		
		VIN = 14 V, 1 mA ≤ IO ≤ 70 mA	7.6		8.4		
Line Regulation	REGIN	Tj = 25 °C, 10.5 V ≤ VIN ≤ 23 V		10	175	mV	
		Tj = 25 °C, 11 V ≤ VIN ≤ 23 V		6	125		
Load Regulation	REGL	Tj = 25 °C, 1 mA ≤ IO ≤ 100 mA		14	80	mV	
		Tj = 25 °C, 1 mA ≤ IO ≤ 40 mA		6	40		
Quiescent Current	IBIAS	Tj = 25 °C		2.4	5.5	mA	
Quiescent Current Change	ΔIBIAS	12 V ≤ VIN ≤ 23 V, IO = 40 mA			1.5	mA	
		VIN = 14 V, 1 mA ≤ IO ≤ 40 mA			0.1		
Output Noise Voltage	Vn	Tj = 25 °C, 10 Hz ≤ f ≤ 100 kHz		85	190	μV <sub>r.m.s.</sub>	
Ripple Rejection	R · R	Tj = 25 °C, f = 120 Hz, 12 V ≤ VIN ≤ 22 V	51	73		dB	
Dropout Voltage	V <sub>DIF</sub>	Tj = 25 °C		1.7		V	
Short Circuit Current	I <sub>short</sub>	Tj = 25 °C, VIN = 23 V		80		mA	
Peak Output Current	I <sub>peak</sub>	Tj = 25 °C		125	160	205	mA
Temperature coefficient of Output Voltage	ΔVO/ΔT	IO = 5 mA		0.6		mV/°C	

ELECTRICAL CHARACTERISTICS  $\mu$ PC78L10

(VIN = 17 V, IO = 40 mA, 0 °C ≤ TJ ≤ +125 °C, CIN = 0.33 μF, COUT = 0.1 μF)

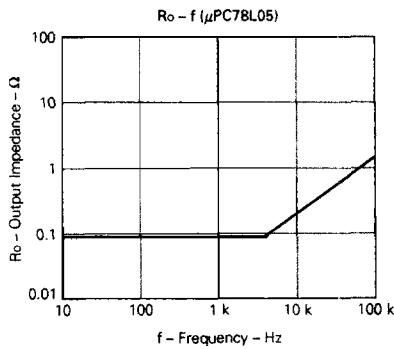
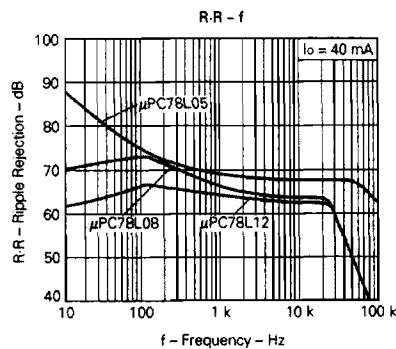
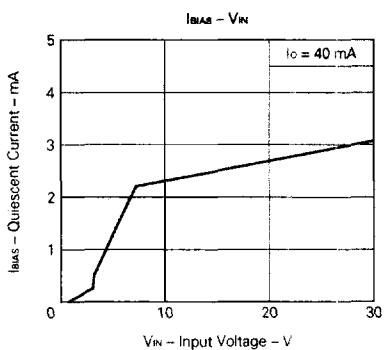
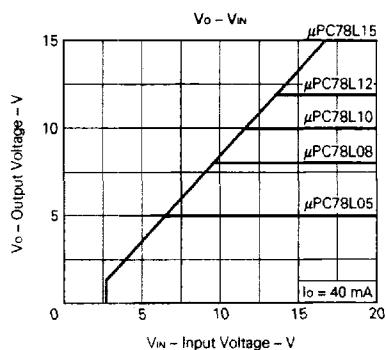
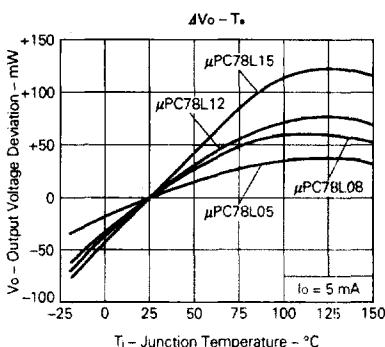
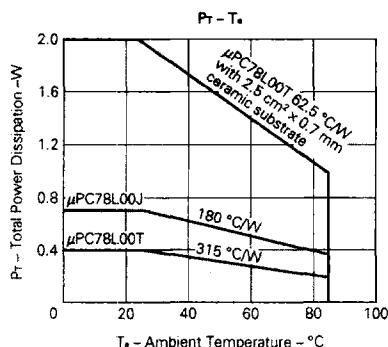
CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Output Voltage	VO	Tj = 25 °C	9.6	10	10.4	V	
		12.5 V ≤ VIN ≤ 25 V, 1 mA ≤ IO ≤ 40 mA	9.5		10.5		
		VIN = 17 V, 1 mA ≤ IO ≤ 70 mA	9.5		10.5		
Line Regulation	REGIN	Tj = 25 °C, 12.5 V ≤ VIN ≤ 25 V		12	200	mV	
		Tj = 25 °C, 13 V ≤ VIN ≤ 25 V		8	150		
Load Regulation	REGL	Tj = 25 °C, 1 mA ≤ IO ≤ 100 mA		18	90	mV	
		Tj = 25 °C, 1 mA ≤ IO ≤ 40 mA		8	45		
Quiescent Current	IBIAS	Tj = 25 °C		2.5	5.5	mA	
Quiescent Current Change	ΔIBIAS	13 V ≤ VIN ≤ 25 V, IO = 40 mA			1.5	mA	
		VIN = 17 V, 1 mA ≤ IO ≤ 40 mA			0.1		
Output Noise Voltage	Vn	Tj = 25 °C, 10 Hz ≤ f ≤ 100 kHz		100	230	μV <sub>r.m.s.</sub>	
Ripple Rejection	R · R	Tj = 25 °C, f = 120 Hz, 14 V ≤ VIN ≤ 24 V	49	69		dB	
Dropout Voltage	V <sub>DIF</sub>	Tj = 25 °C		1.7		V	
Short Circuit Current	I <sub>short</sub>	Tj = 25 °C, VIN = 25 V		70		mA	
Peak Output Current	I <sub>peak</sub>	Tj = 25 °C		125	160	205	mA
Temperature coefficient of Output Voltage	ΔVO/ΔT	IO = 5 mA		0.8		mV/°C	

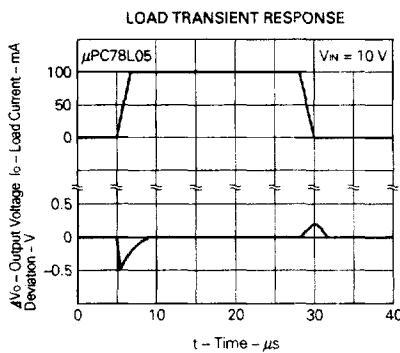
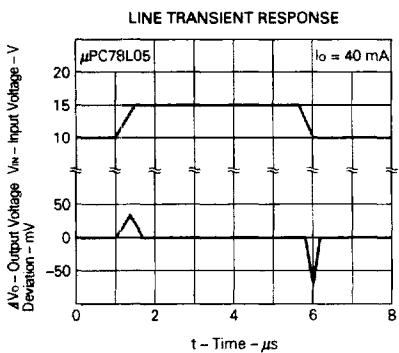
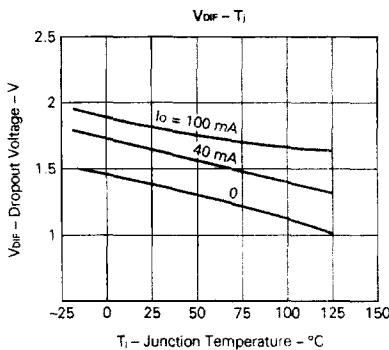
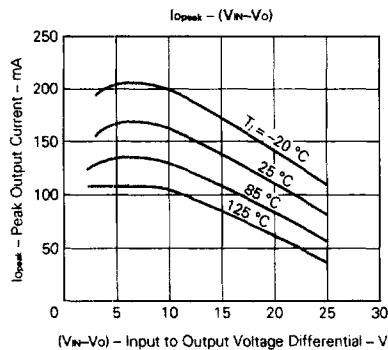
ELECTRICAL CHARACTERISTICS  $\mu$ PC78L12(V<sub>IN</sub> = 19 V, I<sub>O</sub> = 40 mA, 0 °C ≤ T<sub>J</sub> ≤ +125 °C, C<sub>IN</sub> = 0.33 μF, C<sub>OUT</sub> = 0.1 μF)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V <sub>O</sub>	T <sub>J</sub> = 25 °C	11.5	12	12.5	V
		14 V ≤ V <sub>IN</sub> ≤ 27 V, 1 mA ≤ I <sub>O</sub> ≤ 40 mA	11.4		12.6	
		V <sub>IN</sub> = 19 V, 1 mA ≤ I <sub>O</sub> ≤ 70 mA	11.4		12.6	
Line Regulation	REG <sub>IN</sub>	T <sub>J</sub> = 25 °C, 14.5 V ≤ V <sub>IN</sub> ≤ 27 V		14	250	mV
		T <sub>J</sub> = 25 °C, 16 V ≤ V <sub>IN</sub> ≤ 27 V		10	200	
Load Regulation	REG <sub>L</sub>	T <sub>J</sub> = 25 °C, 1 mA ≤ I <sub>O</sub> ≤ 100 mA		20	100	mV
		T <sub>J</sub> = 25 °C, 1 mA ≤ I <sub>O</sub> ≤ 40 mA		10	50	
Quiescent Current	I <sub>BIAS</sub>	T <sub>J</sub> = 25 °C		2.6	5.5	mA
Quiescent Current Change	ΔI <sub>BIAS</sub>	16 V ≤ V <sub>IN</sub> ≤ 27 V, I <sub>O</sub> = 40 mA		1.5		mA
		V <sub>IN</sub> = 19 V, 1 mA ≤ I <sub>O</sub> ≤ 40 mA		0.1		
Output Noise Voltage	V <sub>N</sub>	T <sub>J</sub> = 25 °C, 10 Hz ≤ f ≤ 100 kHz		115	280	μV <sub>r.m.s</sub>
Ripple Rejection	R · R	T <sub>J</sub> = 25 °C, f = 120 Hz, 15 V ≤ V <sub>IN</sub> ≤ 25 V	47	66		dB
Dropout Voltage	V <sub>DIF</sub>	T <sub>J</sub> = 25 °C		1.7		V
Short Circuit Current	I <sub>Oshort</sub>	T <sub>J</sub> = 25 °C, V <sub>IN</sub> = 27 V		64		mA
Peak Output Current	I <sub>Opeak</sub>	T <sub>J</sub> = 25 °C	125	160	205	mA
Temperature coefficient of Output Voltage	ΔV <sub>O</sub> /ΔT	I <sub>O</sub> = 5 mA		1.1		mV/°C

ELECTRICAL CHARACTERISTICS  $\mu$ PC78L15(V<sub>IN</sub> = 23 V, I<sub>O</sub> = 40 mA, 0 °C ≤ T<sub>J</sub> ≤ +125 °C, C<sub>IN</sub> = 0.33 μF, C<sub>OUT</sub> = 0.1 μF)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V <sub>O</sub>	T <sub>J</sub> = 25 °C	14.4	15	15.6	V
		17.5 V ≤ V <sub>IN</sub> ≤ 30 V, 1 mA ≤ I <sub>O</sub> ≤ 40 mA	14.25		15.75	
		V <sub>IN</sub> = 23 V, 1 mA ≤ I <sub>O</sub> ≤ 70 mA	14.25		15.75	
Line Regulation	REG <sub>IN</sub>	T <sub>J</sub> = 25 °C, 17.5 V ≤ V <sub>IN</sub> ≤ 30 V		18	300	mV
		T <sub>J</sub> = 25 °C, 20 V ≤ V <sub>IN</sub> ≤ 30 V		13	250	
Load Regulation	REG <sub>L</sub>	T <sub>J</sub> = 25 °C, 1 mA ≤ I <sub>O</sub> ≤ 100 mA		25	150	mV
		T <sub>J</sub> = 25 °C, 1 mA ≤ I <sub>O</sub> ≤ 40 mA		12	75	
Quiescent Current	I <sub>BIAS</sub>	T <sub>J</sub> = 25 °C		2.7	5.5	mA
Quiescent Current Change	ΔI <sub>BIAS</sub>	20 V ≤ V <sub>IN</sub> ≤ 30 V, I <sub>O</sub> = 40 mA		1.5		mA
		V <sub>IN</sub> = 23 V, 1 mA ≤ I <sub>O</sub> ≤ 40 mA		0.1		
Output Noise Voltage	V <sub>N</sub>	T <sub>J</sub> = 25 °C, 10 Hz ≤ f ≤ 100 kHz		135	350	μV <sub>r.m.s</sub>
Ripple Rejection	R · R	T <sub>J</sub> = 25 °C, f = 120 Hz, 18.5 V ≤ V <sub>IN</sub> ≤ 28.5 V	45	61		dB
Dropout Voltage	V <sub>DIF</sub>	T <sub>J</sub> = 25 °C		1.7		V
Short Circuit Current	I <sub>Oshort</sub>	T <sub>J</sub> = 25 °C, V <sub>IN</sub> = 30 V		53		mA
Peak Output Current	I <sub>Opeak</sub>	T <sub>J</sub> = 25 °C	125	160	205	mA
Temperature coefficient of Output Voltage	ΔV <sub>O</sub> /ΔT	I <sub>O</sub> = 5 mA		1.4		mV/°C

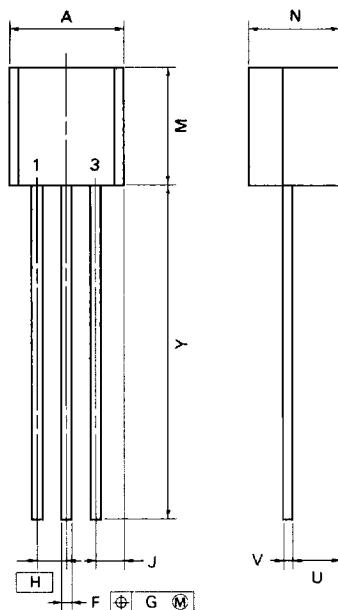
TYPICAL CHARACTERISTICS ( $T_s = 25^\circ\text{C}$ )



## PACKAGE DIMENSIONS (Unit: mm)

 $\mu$ PC78L00J Series

3 PIN PLASTIC SIP (TO-92)

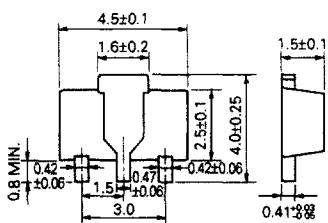


## NOTE

Each lead centerline is located within 0.12 mm (0.005 inch) of its true position (T.P.) at maximum material condition.

P3J-127B

ITEM	MILLIMETERS	INCHES
A	5.2 MAX.	0.205 MAX.
F	0.5 <sup>+0.1</sup>	0.02 <sup>.005</sup>
G	0.12	0.005
H	1.27	0.05
J	1.33 MAX.	0.053 MAX.
M	5.5 MAX.	0.217 MAX.
N	4.2 MAX.	0.166 MAX.
U	2.8 MAX.	0.111 MAX.
V	0.5 <sup>+0.1</sup>	0.02 <sup>.005</sup>
Y	15.0 <sup>+0.7</sup>	0.591 <sup>.028</sup>

$\mu$ PC78L00T Series

**RECOMMENDED SOLDERING CONDITIONS**

The following conditions (see table below) must be met when soldering this product.

Please consult with our sales offices in case other soldering process is used, or in case soldering is done under different conditions.

**<TYPES OF SURFACE MOUNT DEVICE>**

For more details, refer to our document "SMT MANUAL" (IEI-1207).

{ $\mu$ PC78L00T Series}

Soldering process	Soldering condition	Symbol
Infrared ray reflow	Peak package's surface temperature: 230 °C or below, Reflow time: 30 seconds or below (210 °C or higher), Number of reflow process: 1, Exposure limit*: None	IR30-00
VPS	Peak package's surface temperature: 215 °C or below, Reflow time: 40 seconds or below (200 °C or higher), Number of reflow process: 1, Exposure limit*: None	VP15-00

\*: Exposure limit before soldering after dry-pack package is opened. Storage conditions: 25 °C and relative humidity at 65% or less.

**Note:** Do not apply more than a single process at once, except for "Partial heating method".

**<TYPES OF THROUGH HOLE MOUNT DEVICE>**

{ $\mu$ PC78L00J Series}

Soldering process	Soldering condition	Symbol
Wave soldering	Solder temperature: 260 °C or below, Flow time: 10 seconds or below	