TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VCXR2245FT, TC74VCXR2245FK, TC74VCXR2245FTG

Low-Voltage Octal Bus Transceiver with 3.6-V Tolerant Inputs and Outputs

The TC74VCXR2245 is a high-performance CMOS octal bus transceiver. Designed for use in 1.8-V, 2.5-V or 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

It is also designed with overvoltage tolerant inputs and outputs up to 3.6 V.

The direction of data transmission is determined by the level of the DIR inputs. The OE inputs can be used to disable the device so that the busses are effectively isolated. The $26-\Omega$ series resistor helps reducing output overshoot and undershoot without external

All inputs are equipped with protection circuits against static discharge.

Features (Note 1)

- $26-\Omega$ series resistors on outputs
- Low-voltage operation: V_{CC} = 1.8 to 3.6 V
- High-speed operation: t_{pd} = 4.4 ns (max) (V_{CC} = 3.0 to 3.6 V)

 $t_{pd} = 5.6 \text{ ns (max) (VCC} = 2.3 \text{ to } 2.7 \text{ V)}$

 $t_{pd} = 9.8 \text{ ns (max) (V}_{CC} = 1.8 \text{ V)}$

Output current: IOH/IOL = ±12 mA (min) (VCC = 3.0 V)

 $I_{OH}/I_{OL} = \pm 8 \text{ mA (min)} (V_{CC} = 2.3 \text{ V})$

 $I_{OH}/I_{OL} = \pm 4 \text{ mA (min)} (V_{CC} = 1.8 \text{ V})$

- Latch-up performance: -300 mA
- ESD performance: Machine model ≥ ±200 V

Human body model ≥ ±2000 V

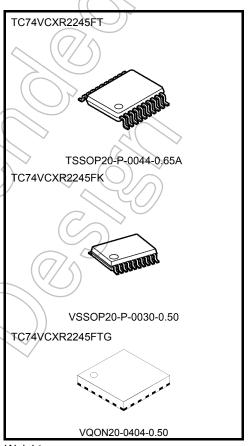
Package: TSSOP

VSSOP (US)

VQON

Note 1: When mounting VQON package, the type of recommended flux is RA or RMA.

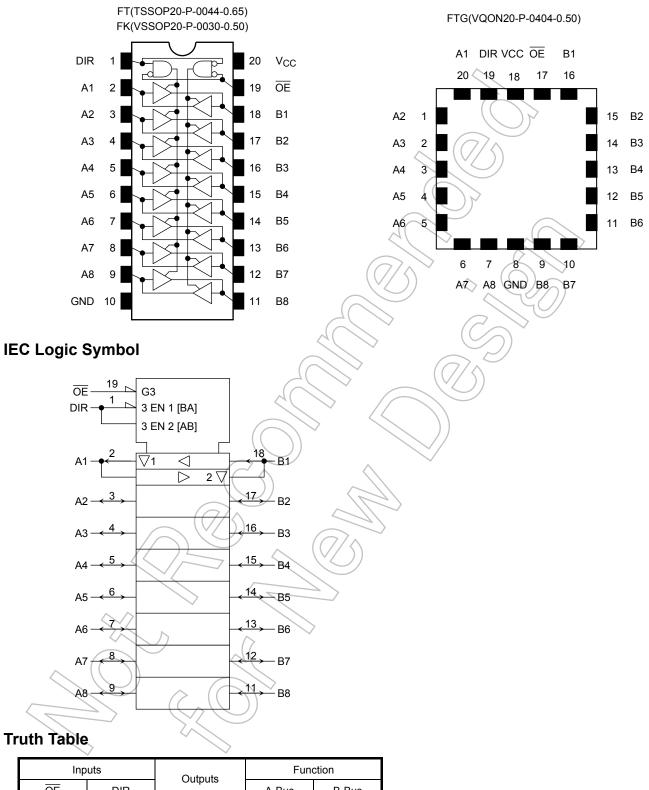
3.6-V tolerant function and power-down protection provided on all inputs and outputs



Weight

TSSOP20-P-0044-0.65A : 0.08 g (typ.) VSSOP20-P-0030-0.50 : 0.03 g (typ.) : 0.0145g (typ.) VQON20-P-0404-0.50

Pin Assignment (top view)



Ζ

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inp	uts	Outputs	Function			
OE	DIR	Outputs	A-Bus	B-Bus		
L	L	A = B	OUTPUT	INPUT		
L	Н	B = A	INPUT	OUTPUT		

Ζ

X: Don't care

Н

Z: High impedance

Χ



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5 to 4.6	V	
DC input voltage $(DIR, \ \overline{OE})$	V _{IN}	-0.5 to 4.6	V	
		-0.5 to 4.6 (Note 2)		
DC bus I/O voltage	V _{I/O}	-0.5 to V_{CC} + 0.5 (Note 3)	V (
Input diode current	I _{IK}	-50	mA	
Output diode current	lok	±50 (Note 4)	mA /	
DC output current	lout	±50	mA	
Power dissipation	P_{D}	180	mW	
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA	
Storage temperature	T _{stg}	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in 1C performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	$7/\hat{\mathbf{v}}_{cc}$	1.8 to 3.6	V
The stapping remarks		1.2 to 3.6 (Note 2)	•
Input voltage	VIN	-0.3 to 3.6	V
(DIR, OE)	- 114		•
Bus I/O voltage	V _{I/O}	0 to 3.6 (Note 3)	V
Dus 1/0 voltage	V1/O	0 to V _{CC} (Note 4)	V
		±12 (Note 5)	
Output current	I _{OH} /I _{OL}	±8 (Note 6)	mA
	41	±4 (Note 7)	
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 8)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs and bus inputs must be tied to either V_{CC} or GND. Please connect both bus inputs and the bus outputs with V_{CC} or GND when the I/O of the bus terminal changes by the function. In this case, please note that the output is not short-circuited.

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Note 2: Data retention only

Note 3: OFF state

Note 4: High or low state Note 5: $V_{CC} = 3.0$ to 3.6 V

Note 6: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 7: $V_{CC} = 1.8 \text{ V}$

Note 8: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V



Electrical Characteristics

DC Characteristics (Ta = -40 to 85° C, 2.7 V < $V_{CC} \le 3.6$ V)

Characteris	stics	Symbol	Test C	ondition	V _{CC} (V)	Min	Max	Unit
Input voltage	H-level	V _{IH}	-	_	2.7 to 3.6	2.0	_	V
iliput voltage	L-level	V _{IL}	_	_	2.7 to 3.6	_	8.0	V
				I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_	
	H-level	VoH	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -6 \text{ mA}$	//2.7	2.2	_	
				$I_{OH} = -8 \text{ mA}$	3.0	2.4	_	
Output voltage				I _{OH} = -12 mA	3.0	2.2	_	V
			$V_{IN} = V_{IH}$ or V_{IL}	$I_{OL} = 100 \mu A$	2.7 to 3.6		0.2	
	L-level	V _{OL}		I _{OL} = 6 mA	2.7	4)	0.4	
	L-level	L-level VOL		$I_{OL} = 8 \text{ mA}$	3.0		0.55	
				I _{OL} ≠ 12 mA	3.0	D) / /2	0.8	
Input leakage current		I _{IN}	V _{IN} = 0 to 3.6 V		2.7 to 3.6	4	±5.0	μΑ
3-state output OFF state current		I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 3.6 V		2.7 to 3.6	>_	±10.0	μА
Power-off leakage current		loff	V _{IN} , V _{OUT} = 0 to 3.6 V		> <	_	10.0	μΑ
Quiescent cumply current		loo	$V_{IN} = V_{CC}$ or GND		2.7 to 3.6	_	20.0	
Quiescent supply current		Icc	V _{CC} ≤ (V _{IN} , V _{OUT}) ≤ 3.6 V		2.7 to 3.6	_	±20.0	μΑ
Increase in I _{CC} per input		Δl _{CC}	$V_{IH} = V_{CC} - 0.6 V$ (per	r input)	2.7 to 3.6	_	750	

DC Characteristics (Ta = -40 to 85° C, 2.3 V \leq V_{CC} \leq 2.7 V)

Characterist	ics	Symbol	Test C	ondition	V _{CC} (V)	Min	Max	Unit
I	H-level	V _{1H}		$\overline{}$	2.3 to 2.7	1.6	_	.,
Input voltage	L-level	VIL)	2.3 to 2.7	_	0.7	V
		>		I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2	_	
\sim	H-level	V _{OH}	VIN = VIH OF VIL	I _{OH} = -4 mA	2.3	2.0	_	
7/			\wedge	I _{OH} = -6 mA	2.3	1.8	_	
Output voltage			1	$I_{OH} = -8 \text{ mA}$	2.3	1.7	_	V
			VIN = VIH or VIL	$I_{OL} = 100 \mu A$	2.3 to 2.7	_	0.2	
	L-level V _{OL}	VOL		I _{OL} = 6 mA	2.3	_	0.4	
		2		I _{OL} = 8 mA	2.3	_	0.6	
Input leakage current	· · · · · · · · · · · · · · · · · · ·	JIN.	V _{IN} = 0 to 3.6 V		2.3 to 2.7	_	±5.0	μΑ
3-state output OFF sta	ite current	loz	V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 3.6 V		2.3 to 2.7	_	±10.0	μА
Power-off leakage curi	rent	l _{OFF}	V_{IN} , $V_{OUT} = 0$ to 3.6 V	,	0	_	10.0	μА
Ouisseent supply surrent		laa	V _{IN} = V _{CC} or GND		2.3 to 2.7	_	20.0	
Quiescent supply curre	=111	Icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3$.6 V	2.3 to 2.7		±20.0	μА



DC Characteristics (Ta = -40 to 85° C, $1.8 \text{ V} \leq \text{V}_{CC} < 2.3 \text{ V}$)

Characteristi	cs	Symbol	Test Co	Test Condition				Min	Max	Unit
					V _{CC} (V)					
Input voltage	H-level	V _{IH}	_	_		$0.7 \times V_{CC}$	_	V		
input voitage	L-level	V _{IL}	_	_	1.8 to 2.3		0.2 × V _{CC}	V		
	H-level	el V _{OH} V _{IN} = V _{IH} or V _{IL}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.8	VCC 0.2				
Output voltage				I _{OH} = -4 mA	71.8	1.4		V		
	L-level V _{OL}	$V_{IN} = V_{IH}$ or V_{IL}	I _{OL} = 100 μA	1.8		0.2				
			I _{OL} = 4 mA	1.8		0.3				
Input leakage current		I _{IN}	$V_{IN} = 0$ to 3.6 V		J 1.8		±5.0	μΑ		
3-state output OFF state current		l _{OZ}	$V_{IN} = V_{IH}$ or V_{IL} $V_{OUT} = 0$ to 3.6 V	4	1.8	(\	±10.0	μА		
Power-off leakage current		loff	V_{IN} , $V_{OUT} = 0$ to 3.6 V		0	7-//	> 10.0	μΑ		
Outgoognt ounnly ourrent		Icc	V _{IN} = V _{CC} or GND		1.8		20.0	Δ		
Quiescent supply curre	Quiescent supply current		$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.$	6 V	1.8		±20.0	μА		

AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω) (Note 1)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	+		1.8	1.5	9.8	
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.5 ± 0.2	8.0	5.6	ns
	t _{pHL}		3.3 ± 0.3	0.6	4.4	
	+		1.8	1.5	9.8	
3-state output enable time	t _{pZL}	Figure 1, Figure 3	2.5 ± 0.2	8.0	6.6	ns
	(tpZH \		3.3 ± 0.3	0.6	5.0	
//)]		$\langle \langle \langle // $	1.8	1.5	8.5	
3-state output disable time	t _{pLZ}	Figure 1, Figure 3	2.5 ± 0.2	0.8	4.7	ns
	t _{pHZ}		3.3 ± 0.3	0.6	4.2	
$\wedge \wedge$.		1.8	_	0.5	
Output to output skew	tosLH	(Note 2)	2.5 ± 0.2	_	0.5	ns
	tosHL	A (3.3 ± 0.3	_	0.5	

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Note 1: For C_L = 50 pF, add approximately 300 ps to the AC maximum specification.

Note 2: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{DLHm} - t_{DLHn}|, t_{OSHL} = |t_{DHLm} - t_{DHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condi	ition		Тур.	Unit
	-,			V _{CC} (V)	. 7 F	
		V _{IH} = 1.8 V, V _{IL} = 0 V	(Note)	1.8	0.15	
Quiet output maximum dynamic V_{OL}	V_{OLP}	V _{IH} = 2.5 V, V _{IL} = 0 V	(Note)	2.5	0.25	V
		V _{IH} = 3.3 V, V _{IL} = 0 V	(Note)	3.3	0.35	
		V _{IH} = 1.8 V, V _{IL} = 0 V	(Note)	1.8	-0.15	
Quiet output minimum dynamic $V_{\mbox{OL}}$	V_{OLV}	V _{IH} = 2.5 V, V _{IL} = 0 V	(Note)	2.5	-0.25	V
		V _{IH} = 3.3 V, V _{IL} = 0 V	(Note)	3.3	-0.35	
		V _{IH} = 1.8 V, V _{IL} = 0 V	(Note)	1.8	1.55	
Quiet output minimum dynamic V _{OH}	V_{OHV}	V _{IH} = 2.5 V, V _{IL} = 0 V	(Note)	2.5	2.05	V
		V _{IH} = 3.3 V, V _{IL} = 0 V	(Note)	3.3	2.65	

Note: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

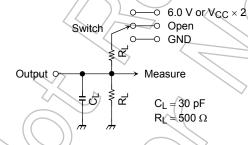
Characteristics	Symbol	Test Condition		Vcc (V)	Тур.	Unit
Input capacitance	C _{IN}	DIR, OE	$\left\langle \cdot \right\rangle$	1.8, 2.5, 3.3	6	pF
Bus I/O capacitance	C _{I/O}	An, Bn	//\	1.8, 2.5, 3.3	7	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz	(Note)	1.8, 2.5, 3.3	20	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$

AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
t _{pLZ} , t _{pZL}	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		
t _{pHZ} , t _{pZH}	GND		

Figure 1

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AC Waveform

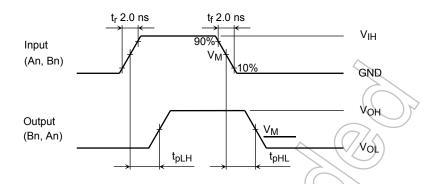


Figure 2 t_{pLH}, t_{pHL}

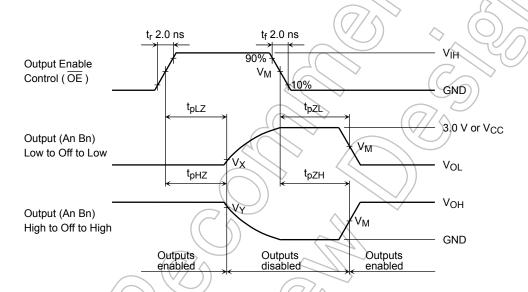


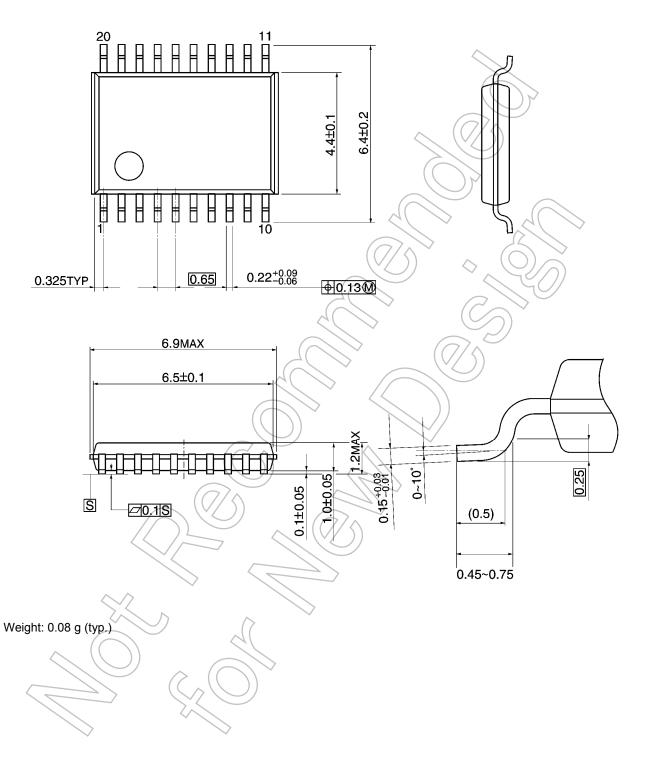
Figure 3 t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}

Symbol	V _{CC}						
Symbol	$3.3\pm0.3\text{V}$	$2.5\pm0.2\mathrm{V}$	1.8 V				
V _{IH}	2.7 V	V _{CC}	V _{CC}				
V _M	1.5 V	V _{CC} /2	V _{CC} /2				
VX	V _{OL} + 0.3 V	V _{OL} + 0.15 V	V _{OL} + 0.15 V				
\bigvee_{Y} (V _{OH} – 0.3 V	V _{OH} – 0.15 V	V _{OH} – 0.15 V				

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Package Dimensions

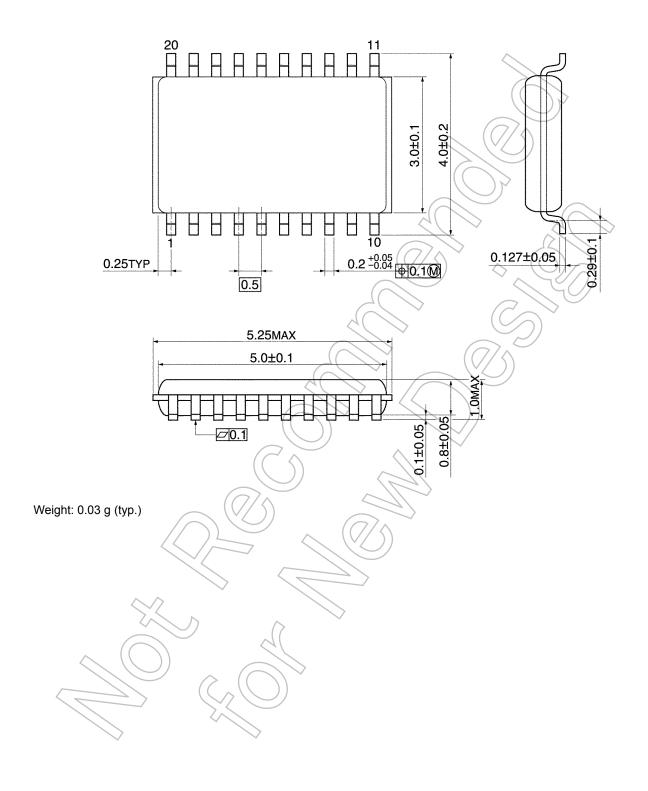
TSSOP20-P-0044-0.65A Unit: mm



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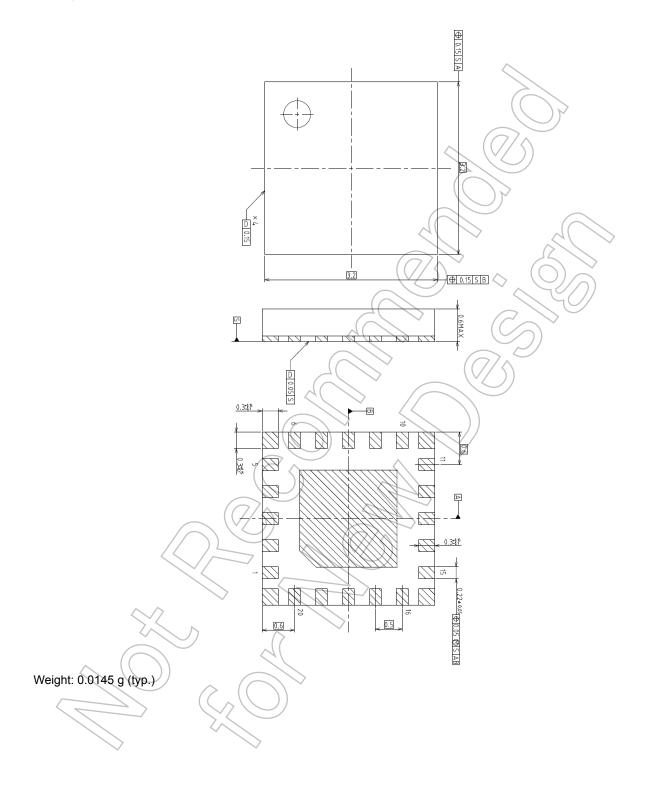
Package Dimensions

VSSOP20-P-0030-0.50 Unit: mm



Package Dimensions

VQON20-P-0404-0.50 Unit: mm



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