



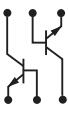
CMLT2222A
CMLT2222AG*

SURFACE MOUNT
DUAL NPN SMALL SIGNAL
SILICON SWITCHING TRANSISTORS

PICOmini™



SOT-563 CASE



* Device is *Halogen Free* by design

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

Collector-Base Voltage	V_{CBO}	75	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Collector Current	I_C	600	mA
Power Dissipation (Note 1)	P_D	350	mW
Power Dissipation (Note 2)	P_D	300	mW
Power Dissipation (Note 3)	P_D	150	mW
Operating and Storage Junction Temperature	T_J, T_{Stg}	-65 to +150	$^\circ\text{C}$
Thermal Resistance	Θ_{JA}	357	$^\circ\text{C}/\text{W}$

SYMBOL		UNITS
V_{CBO}	75	V
V_{CEO}	40	V
V_{EBO}	6.0	V
I_C	600	mA
P_D	350	mW
P_D	300	mW
P_D	150	mW
T_J, T_{Stg}	-65 to +150	$^\circ\text{C}$
Θ_{JA}	357	$^\circ\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_{CBO}	$V_{CB}=60\text{V}$		10	nA
I_{CBO}	$V_{CB}=60\text{V}$ ($T_A=125^\circ\text{C}$)		10	μA
I_{CEV}	$V_{CE}=60\text{V}$, $V_{EB}=3.0\text{V}$		10	nA
I_{EBO}	$V_{EB}=3.0\text{V}$		10	nA
BV_{CBO}	$I_C=10\mu\text{A}$	75		V
BV_{CEO}	$I_C=10\text{mA}$	40		V
BV_{EBO}	$I_E=10\mu\text{A}$	6.0		V
$V_{CE(\text{SAT})}$	$I_C=150\text{mA}$, $I_B=15\text{mA}$		0.3	V
$V_{CE(\text{SAT})}$	$I_C=500\text{mA}$, $I_B=50\text{mA}$		1.0	V
$V_{BE(\text{SAT})}$	$I_C=150\text{mA}$, $I_B=15\text{mA}$	0.6	1.2	V
$V_{BE(\text{SAT})}$	$I_C=500\text{mA}$, $I_B=50\text{mA}$		2.0	V
h_{FE}	$V_{CE}=10\text{V}$, $I_C=0.1\text{mA}$	35		
h_{FE}	$V_{CE}=10\text{V}$, $I_C=1.0\text{mA}$	50		
h_{FE}	$V_{CE}=10\text{V}$, $I_C=10\text{mA}$	75		
h_{FE}	$V_{CE}=1.0\text{V}$, $I_C=150\text{mA}$	50		
h_{FE}	$V_{CE}=10\text{V}$, $I_C=150\text{mA}$	100	300	
h_{FE}	$V_{CE}=10\text{V}$, $I_C=500\text{mA}$	40		

Notes: (1) Ceramic or aluminum core PC Board with copper mounting pad area of 4.0 mm^2

(2) FR-4 Epoxy PC Board with copper mounting pad area of 4.0 mm^2

(3) FR-4 Epoxy PC Board with copper mounting pad area of 1.4 mm^2

CentralTM
Semiconductor Corp.

DESCRIPTION:

These CENTRAL SEMICONDUCTOR devices consist of two (2) isolated 2222A NPN silicon transistors, manufactured by the epitaxial planar process and epoxy molded in an SOT-563 surface mount package. These PICOmini™ devices have been designed for small signal general purpose and switching applications.

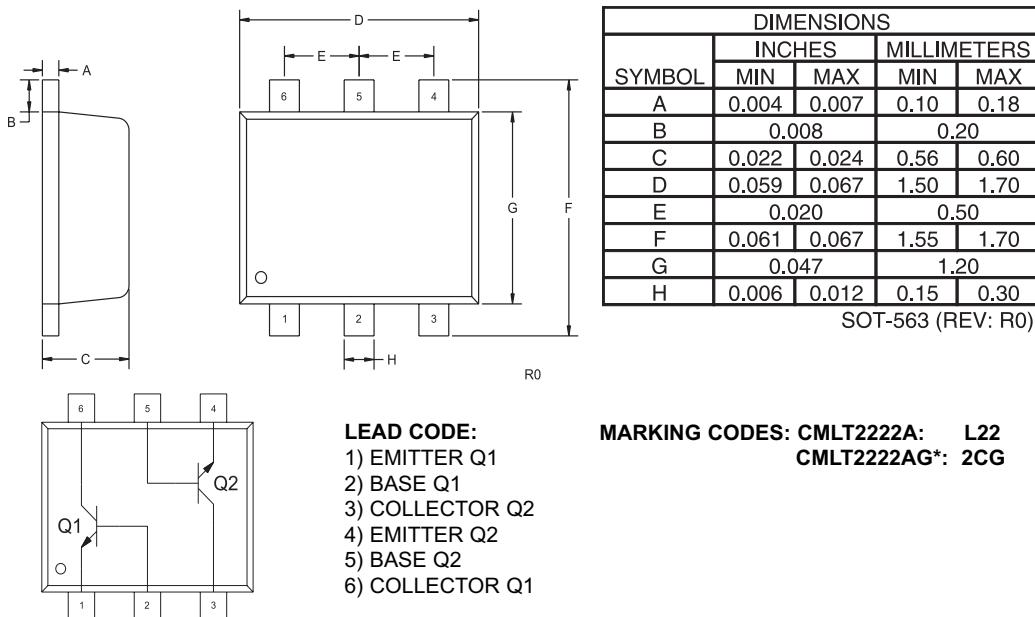
MARKING CODES: CMLT2222A: L22
CMLT2222AG*: 2CG

SURFACE MOUNT
DUAL NPN SMALL SIGNAL
SILICON SWITCHING TRANSISTORS

ELECTRICAL CHARACTERISTICS PER TRANSISTOR - Continued: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
f_T	$V_{CE}=20\text{V}$, $I_C=20\text{mA}$, $f=100\text{MHz}$	300		MHz
C_{ob}	$V_{CB}=10\text{V}$, $I_E=0$, $f=1.0\text{MHz}$		8.0	pF
C_{ib}	$V_{EB}=0.5\text{V}$, $I_C=0$, $f=1.0\text{MHz}$		25	pF
h_{ie}	$V_{CE}=10\text{V}$, $I_C=1.0\text{mA}$, $f=1.0\text{k}\Omega$	2.0	8.0	$\text{k}\Omega$
h_{ie}	$V_{CE}=10\text{V}$, $I_C=10\text{mA}$, $f=1.0\text{k}\Omega$	0.25	1.25	$\text{k}\Omega$
h_{re}	$V_{CE}=10\text{V}$, $I_C=1.0\text{mA}$, $f=1.0\text{k}\Omega$		8.0	$\times 10^{-4}$
h_{re}	$V_{CE}=10\text{V}$, $I_C=10\text{mA}$, $f=1.0\text{k}\Omega$		4.0	$\times 10^{-4}$
h_{fe}	$V_{CE}=10\text{V}$, $I_C=1.0\text{mA}$, $f=1.0\text{k}\Omega$	50	300	
h_{fe}	$V_{CE}=10\text{V}$, $I_C=10\text{mA}$, $f=1.0\text{k}\Omega$	75	375	
h_{oe}	$V_{CE}=10\text{V}$, $I_C=1.0\text{mA}$, $f=1.0\text{k}\Omega$	5.0	35	μs
h_{oe}	$V_{CE}=10\text{V}$, $I_C=10\text{mA}$, $f=1.0\text{k}\Omega$	25	200	μs
$r_b' C_c$	$V_{CB}=10\text{V}$, $I_E=20\text{mA}$, $f=31.8\text{MHz}$		150	ps
NF	$V_{CE}=10\text{V}$, $I_C=100\text{mA}$, $R_S=1.0\text{k}\Omega$, $f=1.0\text{k}\Omega$		4.0	dB
t_d	$V_{CC}=30\text{V}$, $V_{BE}=0.5\text{V}$, $I_C=150\text{mA}$, $I_{B1}=15\text{mA}$		10	ns
t_r	$V_{CC}=30\text{V}$, $V_{BE}=0.5\text{V}$, $I_C=150\text{mA}$, $I_{B1}=15\text{mA}$		25	ns
t_s	$V_{CC}=30\text{V}$, $I_C=150\text{mA}$, $I_{B1}=I_{B2}=15\text{mA}$		225	ns
t_f	$V_{CC}=30\text{V}$, $I_C=150\text{mA}$, $I_{B1}=I_{B2}=15\text{mA}$		60	ns

SOT-563 CASE - MECHANICAL OUTLINE



* Device is **Halogen Free** by design

R2 (6-March 2009)