

RS1A/B - RS1M/B

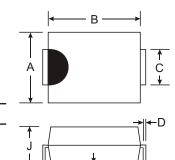
1.0A SURFACE MOUNT FAST RECOVERY RECTIFIER

Features

- Glass Passivated Die Construction
- Fast Recovery Time For High Efficiency
- Low Forward Voltage Drop and High Current Capability
- Surge Overload Rating to 30A Peak
- Ideally Suited for Automated Assembly
- Plastic Material: UL Flammability Classification Rating 94V-0

Mechanical Data

- Case: Molded Plastic
- Terminals: Solder Plated Terminal -Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band or Cathode Notch
- SMA Weight: 0.064 grams (approx.)
- SMB Weight: 0.093 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



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	SI	ΛA	SMB				
Dim	Min	Max	Min	Max			
Α	2.29	2.92	3.30	3.94			
В	4.00	4.60	4.06	4.57			
С	1.27	1.63	1.96	2.21			
D	0.15	0.31	0.15	0.31			
Е	4.80	5.59	5.00	5.59			
G	0.10	0.20	0.10	0.20			
Н	0.76	1.52	0.76	1.52			
J	2.01	2.62	2.00	2.62			



Maximum Ratings and Electrical Characteristics @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

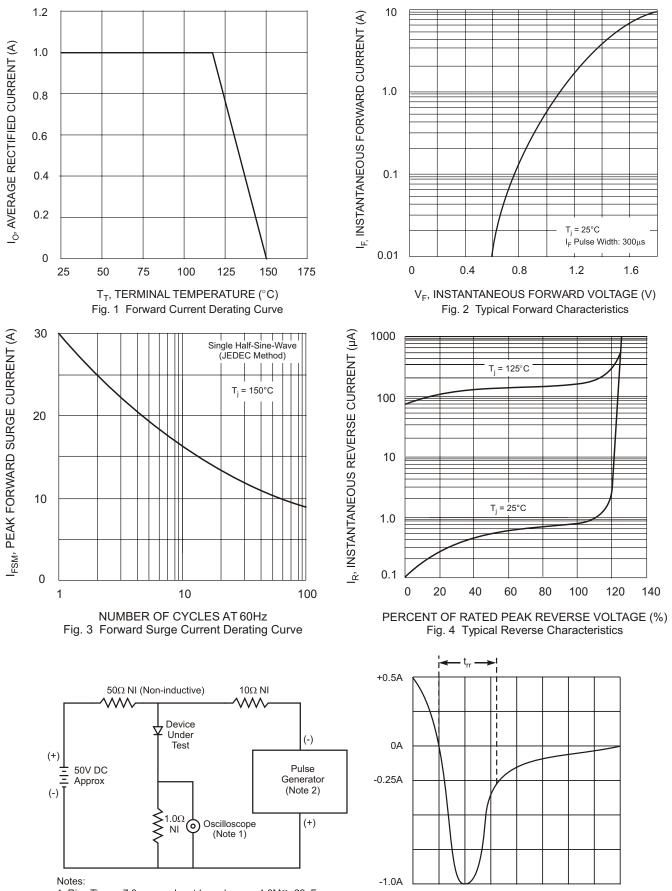
Characteristic		RS1 A/AB	RS1 B/BB	RS1 D/DB	RS1 G/GB	RS1 J/JB	RS1 K/KB	RS1 M/MB	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		50	100	200	400	600	800	1000	V
RMS Reverse Voltage		35	70	140	280	420	560	700	V
Average Rectified Output Current @ $T_T = 120^{\circ}C$		1.0						Α	
Non-Repetitive Peak Forward Surge Current, 8.3ms single half sine-wave Superimposed on Rated Load (JEDEC Method)		30						А	
Forward Voltage Drop $@ I_F = 1.0A$		1.3						V	
Peak Reverse Current@ $T_A = 25^{\circ}C$ at Rated DC Blocking Voltage@ $T_A = 125^{\circ}C$		5.0 200						μA	
Reverse Recovery Time (Note 3)		150 250			250	500		ns	
Typical Junction Capacitance (Note 2)		15						pF	
Typical Thermal Resistance, Junction to Terminal (Note 1)		20						K/W	
Operating and Storage Temperature Range		-65 to +150						°C	

Notes: 1. Valid provided that terminals are kept at ambient temperature.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Reverse Recovery Test Conditions: $I_F = 0.5A$, $I_R = 1.0A$, $I_{rr} = 0.25A$. See figure 5.





1. Rise Time = 7.0ns max. Input Impedance = $1.0M\Omega$, 22pF. 2. Rise Time = 10ns max. Input Impedance = 50Ω .

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

Set time base for 50/100 ns/cm