

Quad Three-State Buffer

The CD54HC125F3A and CD54HCT125F3A contain four independent three-state buffers, each having its own output enable input, which when "HIGH" puts the output in the high-impedance state.

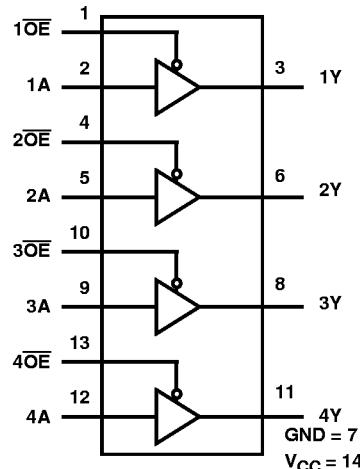
HCT INPUT LOAD TABLE

INPUT	UNIT LOAD (NOTE 1)
nA, nOE	1

NOTE:

1. Unit load is ΔI_{CC} limit specified in DC Electrical Specifications Table, e.g., 360 μ A Max at +25°C.

Functional Diagram



Absolute Maximum Ratings

DC Supply Voltage, V_{CC}

Voltages Referenced to GND -0.5V to +7.0V

DC Input Voltage Range, All Inputs, V_{IN}

-0.5V to V_{CC} +0.5V

DC Output Voltage Range, All Outputs, V_{OUT}

-0.5V to V_{CC} +0.5V

DC Input Diode Current, I_{IK}

For V_I < -0.5V or V_I > V_{CC} + 0.5V ±20mA

DC Output Diode Current, I_{OK}

For V_O < -0.5V or V_O > V_{CC} + 0.5V ±20mA

DC Drain Current, Per Output, I_O, For -0.5V < V_O < V_{CC} + 0.5V

Standard Output ±25mA

Bus Driver Output ±35mA

DC V_{CC} or GND Current, I_{CC}

Standard Output ±50mA

Bus Driver Output ±70mA

Power Dissipation Per Package, P_D

T_A = -55°C to +100°C (Package F) 500mW

T_A = +100°C to +125°C (Package F) Derate Linearly at 8mW/°C to 300mW

Operating Temperature Range, T_A

Package Type F -55°C to +125°C

Storage Temperature, T_{STG} -65°C to +150°C

Lead Temperature (During Soldering)

At Distance 1/16in. ± 1/32in. (1.59mm ± 0.79mm) -55°C to +125°C

From Case For 10s Max +265°C

Unit Inserted Into a PC Board (Min Thickness 1/16in., 1.59mm) +300°C

With Solder Contacting Lead Tips Only +300°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Recommended Operating Conditions

Supply Voltage Range, V_{CC}

T_A = Full Package Temperature Range

CD54HC Types 2V to 6V

CD54HCT Types 4.5V to 5.5V

DC Input or Output Voltage, V_{IN}, V_{OUT}

Operating Temperature Range, T_A

-55°C to +125°C

Input Rise and Fall Times, t_R, t_F

at 2V 0ns to 1000ns

at 4.5V 0ns to 500ns

at 6V 0ns to 400ns