

## 7A Ultra Low Dropout Voltage Regulator Fast Response, Adjustable & Fixed

### FEATURES

- Low Dropout Voltage 800mV at 7A Load Current
- Adjustable Output Down to 1.2V
- Fixed Output Voltages of 3.3V, 2.8V & 2.5V
- Extremely Tight Voltage and Line Regulation
- Standard 5-Terminal Low Cost TO-220 & TO-263
- Replacement to Industry Standard LX8580

### APPLICATIONS

- 3.3V to 2.5V PC Power Supplies
- Micro Processor Lower Supplies
- Post Regulator for Switch Mode PSU
- High-Current Regulator

### PRODUCT DESCRIPTION

The Alpha Semiconductor AS8580 is a 7A Low Dropout Regulator with extremely low dropout voltage. The adjustable version requires only two external resistors to set the output voltage. The fixed version has a preset output of 3.3V, 2.8V or 2.5V and does not require any external resistors. The AS8580 features a low dropout of 700mV(Typ.) and offers fast transient response. This device is suitable for Pentium applications requiring 2.8V or 2.5V from 3.3V PC power supplies, where a low current input voltage 1V greater than the output voltage is needed. With an external sense pin the load regulation is less than 1mV. This device is an excellent choice for the use in powering low voltage microprocessors that require a lower dropout, fast transient response to regulate from 3.3V and 5V supplies. The AS8580 is also an excellent choice as a post regulator for switching supplies applications.

The AS8580 offers protection against over-current faults, reversed input polarity, over temperature operation and positive and negative transient voltage.

The AS8580 is offered in a 5 pin TO-220 and TO-263 compatible with industry standard 5-terminal regulators. For ultra low dropout versions, 7A, 5A, 3A and 1.5A refer to AS1580, AS1581, AS1582 and AS1583 data sheets respectively

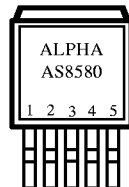
### ORDERING INFORMATION

TO-220 5 PIN	TO-263 5 PIN	OUTPUT
AS8580U	AS8580T	Adjustable
AS8580U-X	AS8580T-X	Fixed

Consult with factory for other fixed output voltages.  
X= Output Voltage ( 3.3V, 2.8V or 2.5V)

### PIN CONNECTIONS

TO-263-5 (T)



Top View

1. SENSE
2. ADJ or FIXED
3.  $V_{OUT}$
4.  $V_{CTRL}$
5.  $V_{IN}$

TO-220-5 (U)



Front View

1. SENSE
2. ADJ or FIXED
3.  $V_{OUT}$
4.  $V_{CTRL}$
5.  $V_{IN}$

## ABSOLUTE MAXIMUM RATINGS

Power Dissipation.....	Internally Limited	Input Supply Voltage.....	6V
Lead Temp (soldering, 10 seconds).....	300°C	V <sub>CTRL</sub> Input Voltage.....	13V
Storage Temperature Range.....	-65°C to +150°C		
Operating Junction Temperature Range			
AS8580 Control Section .....	0°C to +125°C		
AS8580 Power Transistor .....	0°C to +150°C		

**ELECTRICAL CHARACTERISTICS** at V<sub>S</sub>=14V, T<sub>A</sub>=25°C, I<sub>O</sub>=10mA, C<sub>2</sub>=100μF, unless otherwise specified. (Note 1)  
 (Boldface applies over full temperature range).

Parameters	Conditions	AS8580			Units
		Min	Typ	Max	
<b>2.5V Version</b>					
Output Voltage	V <sub>CTRL</sub> =6.0V to 12V, V <sub>IN</sub> =3.0V to 5.0V, I <sub>O</sub> =10mA I <sub>O</sub> =10mA to 7A	2.450 2.400	2.5 2.5	2.550 2.600	V
<b>2.8V Version</b>					
Output Voltage	V <sub>CTRL</sub> =6.3V to 12V, V <sub>IN</sub> =3.3V to 5.0V, I <sub>O</sub> =10mA I <sub>O</sub> =10mA to 7A	2.744 2.688	2.8 2.8	2.856 2.912	V
<b>3.3V Version</b>					
Output Voltage	V <sub>CTRL</sub> =6.3V to 12V, V <sub>IN</sub> =3.3V to 5.0V, I <sub>O</sub> =10mA I <sub>O</sub> =10mA to 7A	3.234 3.168	3.3 3.3	3.366 3.432	V
<b>All Voltage Options</b>					
Reference Voltage (Note 3)	V <sub>CTRL</sub> =2.75V, V <sub>IN</sub> =2.00V, I <sub>O</sub> =10mA V <sub>CTRL</sub> =2.7V to 12V, V <sub>IN</sub> =2.05V to 5.5V, I <sub>O</sub> =10mA to 7A	1.2375	1.25	1.375	V
Line Regulation	V <sub>CTRL</sub> =2.5V to 12V, V <sub>IN</sub> =1.75V to 5.5V, I <sub>O</sub> =10mA V <sub>ADJ</sub> =0V		1.0	3.0	mV
Load Regulation (Note1)	V <sub>CTRL</sub> =2.75V, V <sub>IN</sub> =2.1V, I <sub>O</sub> =10mA to 7A, V <sub>ADJ</sub> =0V		1.0	5.0	mV
Dropout Voltage Minimum V <sub>CTRL</sub> (Note2) (V <sub>CTRL</sub> - V <sub>OUT</sub> )	V <sub>ADJ</sub> =0V V <sub>IN</sub> =2.05V, I <sub>O</sub> =1A		1.00	1.20	V
Dropout Voltage Minimum V <sub>IN</sub> (Note2) (V <sub>IN</sub> - V <sub>OUT</sub> )	V <sub>ADJ</sub> =0V V <sub>CTRL</sub> =2.75V, I <sub>O</sub> =7A		0.70	0.80	V
Current Limit	V <sub>CTRL</sub> =2.75V, V <sub>IN</sub> =2.05V, dV <sub>O</sub> =100mV @ V <sub>ADJ</sub> =0V	7	8		A
Minimum Load Current	V <sub>CTRL</sub> =5V, V <sub>IN</sub> =3.3V, V <sub>ADJ</sub> =0V		5	10	mA
Thermal Regulation	30ms Pulse		0.002	0.02	%W
Ripple Rejection	V <sub>CTRL</sub> =3.75V V <sub>IN</sub> =3.75V, I <sub>O</sub> =2.7A, V <sub>ADJ</sub> =0V T <sub>f</sub> =25, V <sub>RIPPLE</sub> =1V <sub>pp</sub> at 120Hz	60	80		dB
Control Pin Current	V <sub>ADJ</sub> =0V V <sub>CTRL</sub> =2.75V, V <sub>IN</sub> =2.05V, I <sub>O</sub> =7A		60	130	mA
Adjustable Pin Current	V <sub>CTRL</sub> =2.75V, V <sub>IN</sub> =2.05V, V <sub>ADJ</sub> =0V I <sub>O</sub> =10mA		50	120	μA
Thermal Resistance	TO-220-5	Junction to Case (θ <sub>JC</sub> )		3	°C/W
		Junction to Ambient (θ <sub>JA</sub> )		50	°C/W
	TO-263-5	Junction to Case (θ <sub>JC</sub> )		3	°C/W
		Junction to Ambient (θ <sub>JA</sub> )		60	°C/W

The Bold specifications applying to the over full operating temperature range.

Note 1: Low duty cycle pulse testing with Kelvin connections is required to order to maintain accurate data.

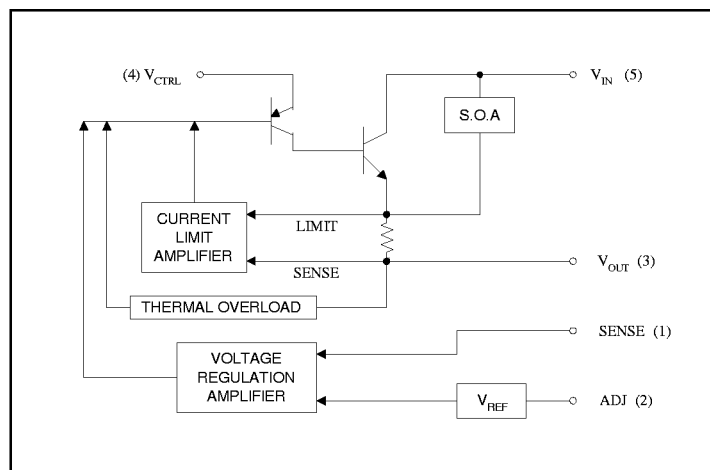
Note 2: Dropout voltage is defined as the minimum differential between V<sub>IN</sub> and V<sub>OUT</sub> or V<sub>CTRL</sub> and V<sub>OUT</sub> required to maintain regulation at V<sub>OUT</sub> 99% Nominal V<sub>OUT</sub>.

Note 3: V<sub>REF</sub> is measured across Adjust pin to Sense pin.

## PIN DESCRIPTION

1. Sense = Allows Kelvin sense of  $V_{OUT}$  at the load. (Positive side of the reference voltage of the device).
2. ADJ = Negative side of the reference voltage for the device. Adding a small bypass capacitor from the ADJ pin to ground will improve the transient response.
3.  $V_{OUT}$  = Power output of the device.
4.  $V_{CTRL}$  = Supply pin for the control circuitry of the device. The current flow into this pin will be about 1% of the output current.  $V_{CTRL}$  must be between 1.0V and 1.3V greater than the output voltage for the device to regulate.
5.  $V_{IN}$  = Output load current is supplied through this pin.  $V_{IN}$  must be between 0.1V and 0.8V greater than the output voltage for the device to regulate.

## BLOCK DIAGRAM



## APPLICATIONS NOTES

The AS8580 is designed as a high performance and low cost solution for application requiring a lower dropout than traditional NPN regulators.

The AS8580 uses a separate input voltage  $V_{CTRL}$  ( $V_{CTRL} \geq V_{OUT} + 1.3V$ ) to minimize the dropout voltage. This allows the 2.5V power for the load to come from a 3.3V system supply. As added benefit this will reduce the heat dissipation\*, and lower heatsink and cooling fan cost. A typical application would use 5V for  $V_{IN}$  and 3.3V for  $V_{CTRL}$  from a motherboard power supply to provide a nominal 2.5V output. Using the sense pin allows to Kelvin measure the output, reducing resistive-associated errors.

The AS8580 can power the 2.5V core voltage for microprocessors such as Pentium™, P55C™, AMD5k86™ and K6™ and the IBM PowerPC™ 603EV and 604EV processors.

\*The reduction of heat dissipation is a result of the increase of the regulator efficiency ( $\text{efficiency} = V_{OUT} / V_{IN}$ ).

### Adjustable Regulator Design

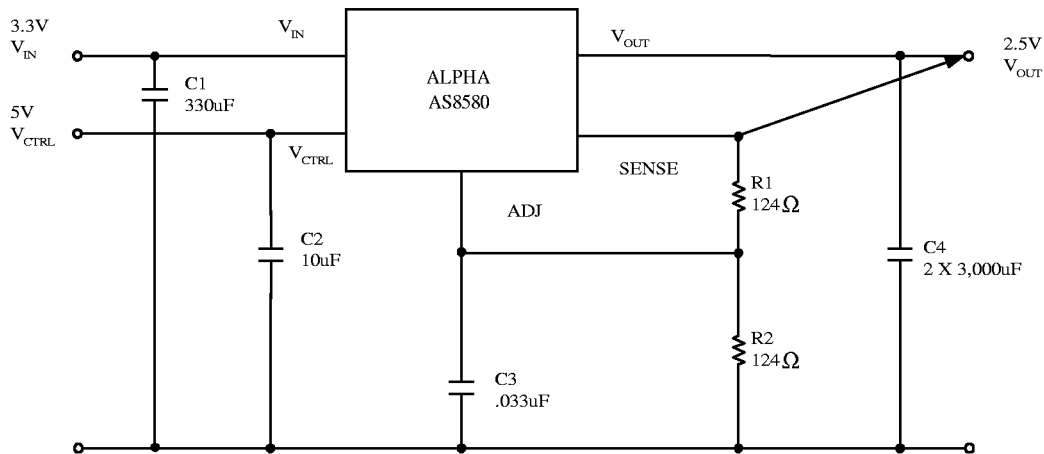
1.25V reference voltage is being developed between the SENSE pin and the ADJ pin of the AS8580. Adding two external resistors (see fig 1.) will allow setting the output voltage from 1.25V to 6V.  $R_1$  is chosen so that this current is specified minimum load current of 10mA.  $R_2$  is given by the formula:  $V_{OUT} = V_{REF} (1 + R_2/R_1) + I_{ADJ} (R_2)$ . The current flowing from the ADJ pin is typically 50 $\mu$ A. This ADJ pin contributes to the final  $V_{OUT}$  but is usually neglected. Connecting the sense pin to the top of the resistor divider will improve load regulation.

### Lowering Noise

Using the SENSE pin to Kelvin the load will increase accuracy of the output voltage during load regulation. For the fixed voltage devices, adding a capacitor at the GND pin will improve transient response. This capacitor is chosen in the range of 1 $\mu$ F to 0.1 $\mu$ F and will depend on the amount of output capacitance in the system.

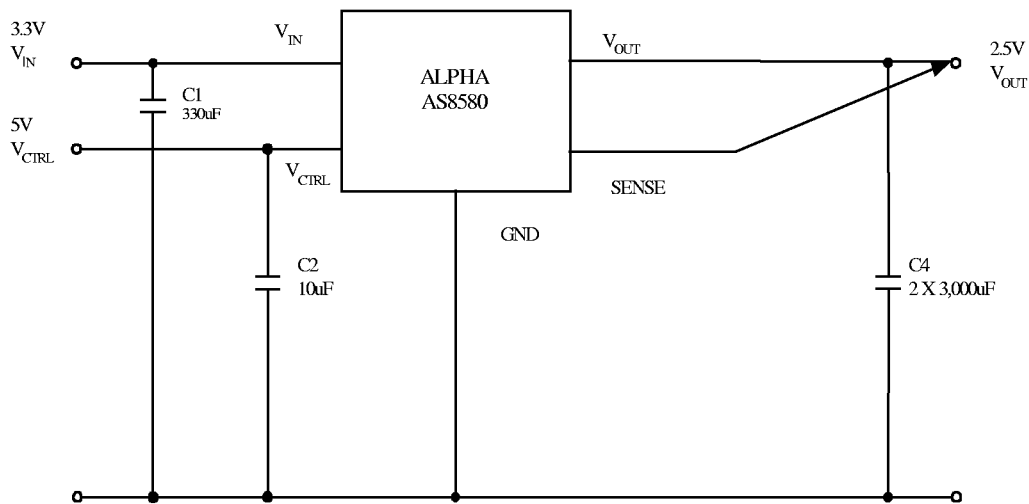
## TYPICAL APPLICATION

Fig. 1 Adjustable Regulator



- (1)  $V_{CTRL}$  needed when  $V_{IN} < 5V$ .
- (2)  $V_{OUT} = V_{REF}(1 + R2/R1) + I_{ADJ} R2$ .
- (3)  $V_{REF}$  is measured across adjust to sense.

Fig.2 Typical Fixed Regulator



- (1)  $V_{CTRL}$  is needed when  $V_{IN} < 5V$ .