

2SB1270/2SD1906

High-Current Switching Applications

Applications

 Suitable for relay drivers, high-speed inverters, converters, and other general high-current switching applications.

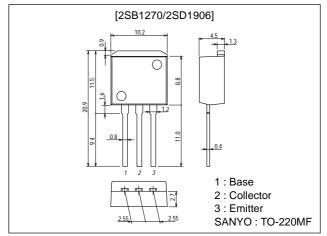
Features

- · Suitable for sets whose height is restricted.
- · Low collector to emitter saturation voltage.
- · Large current capacity.

Package Dimensions

unit:mm

2049C



(): 2SB1270

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-)90	V
Collector-to-Emitter Voltage	V _{CEO}		(-)80	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	lС		(-)5	Α
Collector Current (Pulse)	I _{CP}		(-)9	Α
Collector Dissipation	Po		1.65	W
	PC	Tc=25°C	30	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Offic
Collector Cutoff Current	ICBO	V _{CB} =(-)80V, I _E =0			(-)0.1	mA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(–)0.1	mA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)1A	70*		280*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)3A	30			
Gain-Bandwidth Product	f _T	V _{CE} =(-)5V, I _C =(-)1A		20		MHz

^{* :} The 2SB1270/2SD1906 are classified by 1A h_{FE} as follows :

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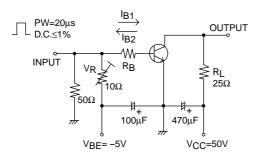
Rank	Q	R	S		
h _{FE}	70 to 140	100 to 200	140 to 280		

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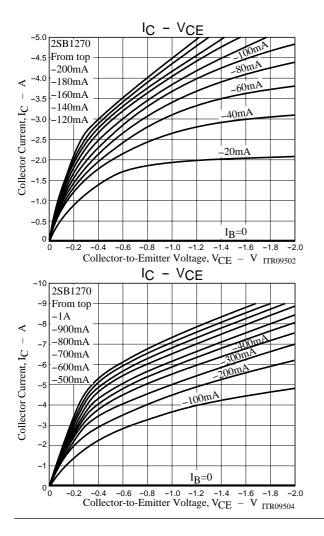
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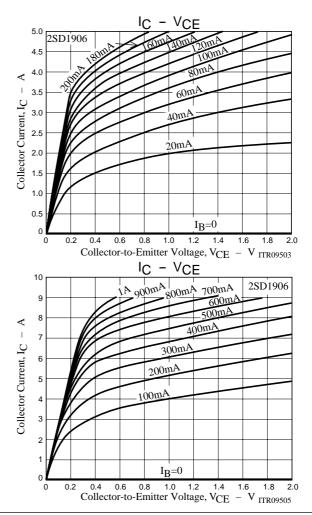
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)3A, I _B =(-)0.3A			0.4	V
					(-0.5)	V
Collector-to-Base Breakdown Voltage	V _(BR) CBO	I _C =(-)1mA, I _E =0	(-)90			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(–)1mA, R _{BE} =∞	(–)80			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =(-)1mA, I _C =0	(–)6			V
Turn-ON Time	ton	See specified test circuit.		(0.2)		μs
Turn-ON Time				0.1		μs
Storage Time	t _{stg}	See specified test circuit.		(0.7)		μs
Storage Time				1.2		μs
Fall Time	t _f	See specified test circuit.		(0.2)		μs
				0.4		μs

Switching Time Test Circuit

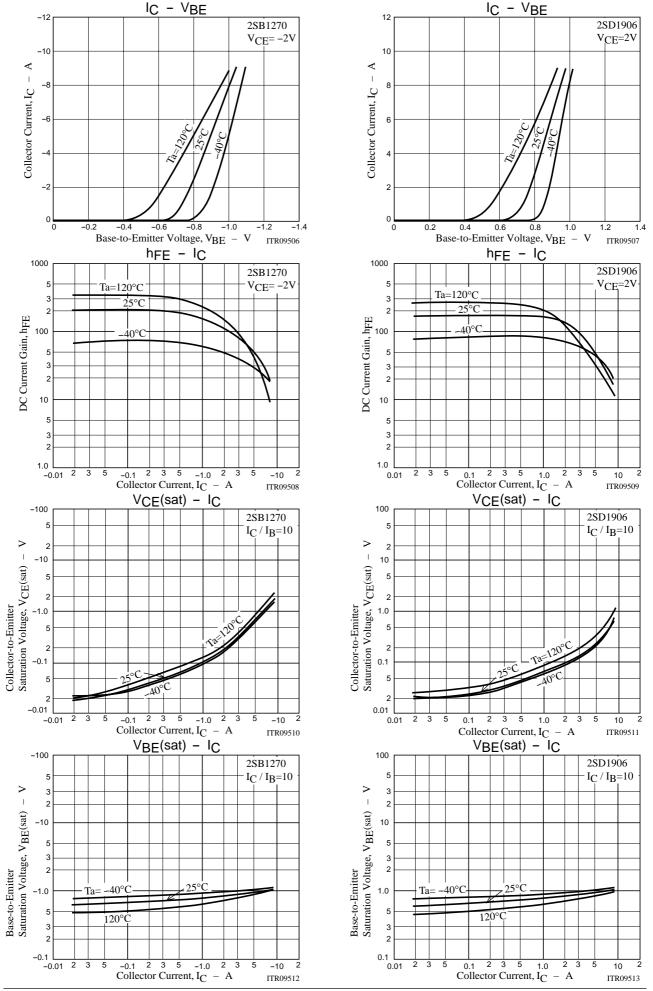


(For PNP, the polarity is reversed.)

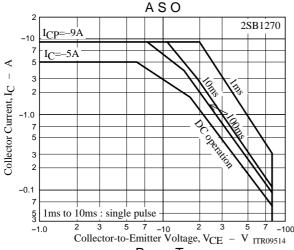


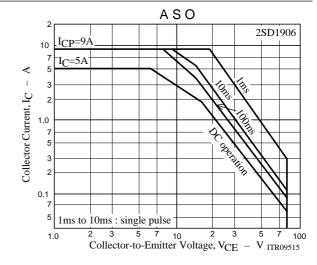


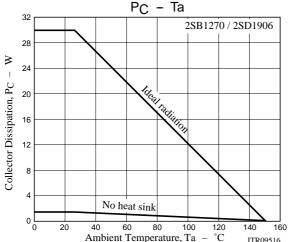
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