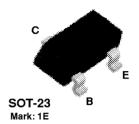


Discrete POWER & Signal **Technologies**

MPSA43







NPN High Voltage Amplifier

This device is designed for application as a video output to drive color CRT and other high voltage applications. Sourced from Process 48. See MPSA42 for characteristics.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

| Symbol | Parameter | Value | Units |
|-----------------------------------|--|-------------|-------|
| V _{CES} | Collector-Emitter Voltage | 200 | V |
| V _{CBO} | Collector-Base Voltage | 200 | V |
| V _{EBO} | Emitter-Base Voltage | 6.0 | V |
| Ic | Collector Current - Continuous | 200 | mA |
| T _J , T _{stg} | Operating and Storage Junction Temperature Range | -55 to +150 | °C |

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

These ratings are based on a maximum junction temperature of 150 degrees C.
 These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

TA = 25°C unless otherwise noted

| Symbol | Characteristic | Max | | Units |
|-----------------|---|--------|----------|-------|
| | | MPSA43 | *MMBTA43 | |
| P _D | Total Device Dissipation | 625 | 350 | mW |
| | Derate above 25°C | 5.0 | 2.8 | mW/°C |
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case | 83.3 | | °C/W |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient | 200 | 357 | °C/W |

Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

NPN High Voltage Amplifier (continued)

| Electrical Characteristics TA = 25°C unless otherwise noted | | | | | |
|--|---|--|----------------|------------|-------|
| Symbol | Parameter | Test Conditions | Min | Max | Units |
| OFF CHA | ARACTERISTICS | | | | |
| V _{(BR)CEO} | Collector-Emitter Breakdown Voltage* | $I_{\rm C} = 1.0 \text{mA}, I_{\rm B} = 0$ | 200 | | V |
| V _{(BR)CBO} | Collector-Base Breakdown Voltage | $I_{C} = 100 \mu\text{A}, I_{E} = 0$ | 200 | | V |
| V _{(BR)EBO} | Emitter-Base Breakdown Voltage | $I_E = 100 \mu A, I_C = 0$ | 6.0 | | V |
| I _{CBO} | Collector-Cutoff Current | V _{CB} = 160 V, I _E = 0 | | 0.1 | μΑ |
| I _{EBO} | Emitter-Cutoff Current | $V_{EB} = 4.0 \text{ V}, I_{C} = 0$ | <u> </u> | 0.1 | |
| | | VEB = 4.0 V, 10 = 0 | <u> </u> | 0.1 | μА |
| | RACTERISTICS* DC Current Gain | $I_{C} = 1.0 \text{ mA}, V_{CE} = 10 \text{ V}$ $I_{C} = 10 \text{ mA}, V_{CE} = 10 \text{ V}$ | 25 40 50 | | μΛ |
| ON CHAF | | I _C = 1.0 mA, V _{CE} = 10 V | 1 | 200 | V |
| ON CHAF | DC Current Gain | $I_{C} = 1.0 \text{ mA}, V_{CE} = 10 \text{ V}$ $I_{C} = 10 \text{ mA}, V_{CE} = 10 \text{ V}$ $I_{C} = 30 \text{ mA}, V_{CE} = 10 \text{ V}$ | 40 | 200 | |
| ON CHAP h _{FE} V _{CE(sat)} V _{BE(sat)} | DC Current Gain Collector-Emitter Saturation Voltage | $\begin{split} I_{C} &= 1.0 \text{ mA}, \ V_{CE} = 10 \text{ V} \\ I_{C} &= 10 \text{ mA}, \ V_{CE} = 10 \text{ V} \\ I_{C} &= 30 \text{ mA}, \ V_{CE} = 10 \text{ V} \\ I_{C} &= 20 \text{ mA}, \ I_{B} = 2.0 \text{ mA} \end{split}$ | 40 | 200 0.4 | V |

^{*}Pulse Test: Pulse Width £ 300 ms, Duty Cycle £ 2.0%

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEX™ ISOPLANAR™ CoolFET™ MICROWIRE™

CROSSVOLTTM POPTM

E²CMOS™ PowerTrench™

FACTTM QSTM

FACT Quiet Series $^{\text{TM}}$ Quiet Series $^{\text{TM}}$ SuperSOT $^{\text{TM}}$ -3 FAST $^{\text{TM}}$ SuperSOT $^{\text{TM}}$ -6 GTO $^{\text{TM}}$ SuperSOT $^{\text{TM}}$ -8 HiSeC $^{\text{TM}}$

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS. NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

| Datasheet Identification | Product Status | Definition |
|--------------------------|---------------------------|---|
| Advance Information | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. |
| Preliminary | First Production | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| Obsolete | Not In Production | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only. |