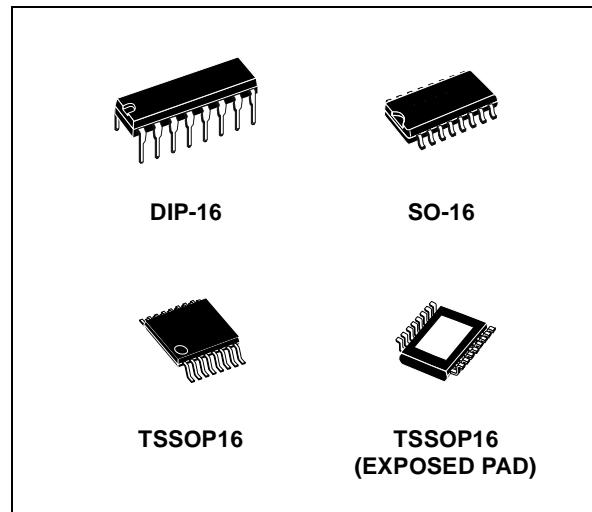




# STP08CL596

## LOW VOLTAGE 8-BIT CONSTANT CURRENT LED SINK DRIVER

- LOW VOLTAGE POWER SUPPLY DOWN TO 3V
- 8 CONSTANT CURRENT OUTPUT CHANNELS
- ADJUSTABLE OUTPUT CURRENT THROUGH EXTERNAL RESISTOR
- SERIAL DATA IN/PARALLEL DATA OUT
- SERIAL OUT CHANGES STATE ON THE FAILING EDGES OF CLOCK
- 3.3V MICRO DRIVER-ABLE
- OUTPUT CURRENT: 15-90 mA
- 25 MHz CLOCK FREQ.
- AVAILABLE IN HIGH THERMAL EFFICIENCY TSSOP EXPOSED PAD



### DESCRIPTION

The STP08CL596 is a monolithic, medium-voltage, low current power 8-bit shift register designed for LED panel display.

The STP08CL596 contains a 8-bit serial-in, parallel-out shift register that feeds a 8-bitD-type storage register. In the output stage, sixteen regulated current sources were designed to provide 15-90mA constant current to drive the LEDs.

Compared with the STPIC6C595, the device provides great flexibility and improved performance in LED panel system design.

Through an external resistor, users may adjust the STP08CL596 output current, controlling in this way the light intensity of LEDs.

The STP08CL596 guarantees 16V output driving capability, allowing users to connect more LEDs in series. The high clock frequency, 25 MHz, also satisfies the system requirement of high volume data transmission. The 3.3V of voltage supply is well useful for applications that interface any micro from 3.3V to 5.5V. Compared with a standard TSSOP package, the TSSOP exposed pad increases heat dissipation capability by a 2.5 factor.

### ORDERING CODES

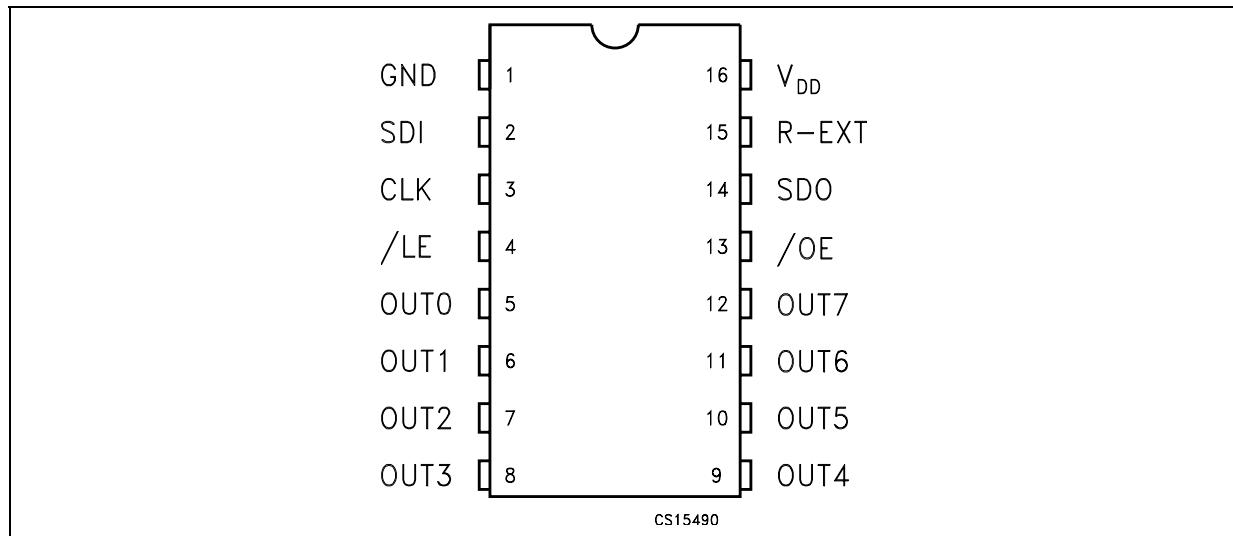
Type	Temp. Range	Package	Comments
STP08CL596B1	-40°C to 125°C	DIP-16	25 part per tube
STP08CL596M	-40°C to 125°C	SO-16 (Tube)	50 parts per tube
STP08CL596MTR	-40°C to 125°C	SO-16 (Tape & Reel)	1000 parts per reel
STP08CL596TTR	-40°C to 125°C	TSSOP16 (Tape & Reel)	2500 parts per reel
STP08CL596XTTR	-40°C to 125°C	TSSOP16 Exposed-Pad (Tape & Reel)	2500 parts per reel

## STP08CL596

### CURRENT ACCURACY

Output Voltage	Current accuracy		Output Current
	Between bits	Between ICs	
$\geq 0.7V$	$\pm 3\%$	$\pm 10\%$	15 to 90 mA

### PIN CONNECTION



### PIN DESCRIPTION

PIN N°	Symbol	Name and Function
1	GND	Ground Terminal
2	SDI	Serial data input terminal
3	CLK	Clock input terminal
4	/LE	Latch input terminal
5-12	OUT 0-7	Output terminal
13	/OE	Output enable input terminal (active low)
14	SDO	Serial data out terminal
15	R-EXT	Constant Current programming
16	V <sub>DD</sub>	5V Supply voltage terminal

### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>DD</sub>	Supply Voltage	0 to 7	V
V <sub>O</sub>	Output Voltage	-0.5 to 16	V
I <sub>O</sub>	Output Current	90	mA
V <sub>I</sub>	Input Voltage	-0.4 to V <sub>DD</sub> +0.4	V
I <sub>GND</sub>	GND Terminal Current	720	mA
f <sub>CLK</sub>	Clock Frequency	25	MHz
T <sub>OPR</sub>	Operating Temperature Range	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

**THERMAL DATA**

Symbol	Parameter	DIP-16	SO-16	TSSOP16	TSSOP16 (exposed pad)	Unit
R <sub>thj-amb</sub>	Thermal Resistance Junction-ambient	90	125	140	37.6	°C/W

**RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>DD</sub>	Supply Voltage		3.0	3.3	3.6	V
V <sub>O</sub>	Output Voltage				16.0	V
I <sub>O</sub>	Output Current	OUTn	15		120	mA
I <sub>OH</sub>	Output Current	SERIAL-OUT			+1	mA
I <sub>OL</sub>	Output Current	SERIAL-OUT			-1	mA
V <sub>IH</sub>	Input Voltage		0.7V <sub>DD</sub>		V <sub>DD</sub> +0.3	V
V <sub>IL</sub>	Input Voltage		-0.3		0.3V <sub>DD</sub>	V
t <sub>wLAT</sub>	/LE Pulse Width	V <sub>DD</sub> = 3.0 to 3.6V	20			ns
t <sub>wCLK</sub>	CLK Pulse Width		20			ns
t <sub>wEN</sub>	/OE Pulse Width		400			ns
t <sub>SETUP(D)</sub>	Setup Time for DATA		20			ns
t <sub>HOLD(D)</sub>	Hold Time for DATA		15			ns
t <sub>SETUP(L)</sub>	Setup Time for LATCH		15			ns
t <sub>HOLD(E)</sub>	Hold Time for ENABLE		60			ns
f <sub>CLK</sub>	Clock Frequency	Cascade Operation			25	MHz

**ELECTRICAL CHARACTERISTICS** (V<sub>DD</sub>=3.3V, T = 25°C, unless otherwise specified.)

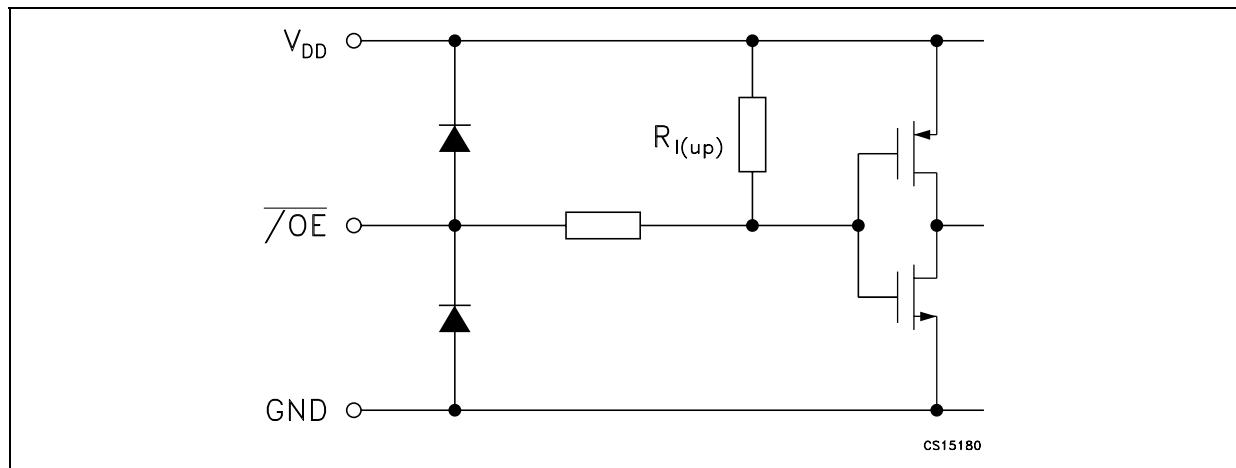
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>IH</sub>	Input Voltage High Level		0.7V <sub>DD</sub>		V <sub>DD</sub>	V
V <sub>IL</sub>	Input Voltage Low Level		GND		0.3V <sub>DD</sub>	V
I <sub>OH</sub>	Output Leakage Current	V <sub>OH</sub> = 16 V			10	µA
V <sub>OL</sub>	Output Voltage (Serial-OUT)	I <sub>OL</sub> = 1mA			0.4	V
V <sub>OH</sub>	Output Voltage (Serial-OUT)	I <sub>OH</sub> = -1mA	V <sub>DD</sub> -0.4V			V
I <sub>OL1</sub>	Output Current	V <sub>O</sub> = 0.7V R <sub>EXT</sub> = 910 Ω	20.0	20.1	20.3	mA
I <sub>OL2</sub>		V <sub>O</sub> = 0.7V R <sub>EXT</sub> = 360 Ω	49.7	50.2	50.7	mA
ΔI <sub>OL1</sub>	Output Current Error between bit (All Output ON)	V <sub>O</sub> = 0.7V R <sub>EXT</sub> = 910 Ω			± 3	%
ΔI <sub>OL2</sub>		V <sub>O</sub> = 0.7V R <sub>EXT</sub> = 360 Ω			± 3	%
R <sub>SIN(up)</sub>	Pull-up Resistor		150	300	600	KΩ
R <sub>SIN(down)</sub>	Pull-down Resistor		100	200	400	KΩ
I <sub>DD(OFF1)</sub>	Supply Current (OFF)	R <sub>EXT</sub> = OPEN OUT 0 to 15 = OFF		0.3	0.6	mA
I <sub>DD(OFF2)</sub>		R <sub>EXT</sub> = 470 Ω OUT 0 to 15 = OFF		5.5	7.7	
I <sub>DD(OFF3)</sub>		R <sub>EXT</sub> = 250 Ω OUT 0 to 15 = OFF		10.1	14.1	
I <sub>DD(ON1)</sub>	Supply Current (ON)	R <sub>EXT</sub> = 470 Ω OUT 0 to 15 = ON		5.5	7.7	
I <sub>DD(ON2)</sub>		R <sub>EXT</sub> = 250 Ω OUT 0 to 15 = ON		10.1	14.1	

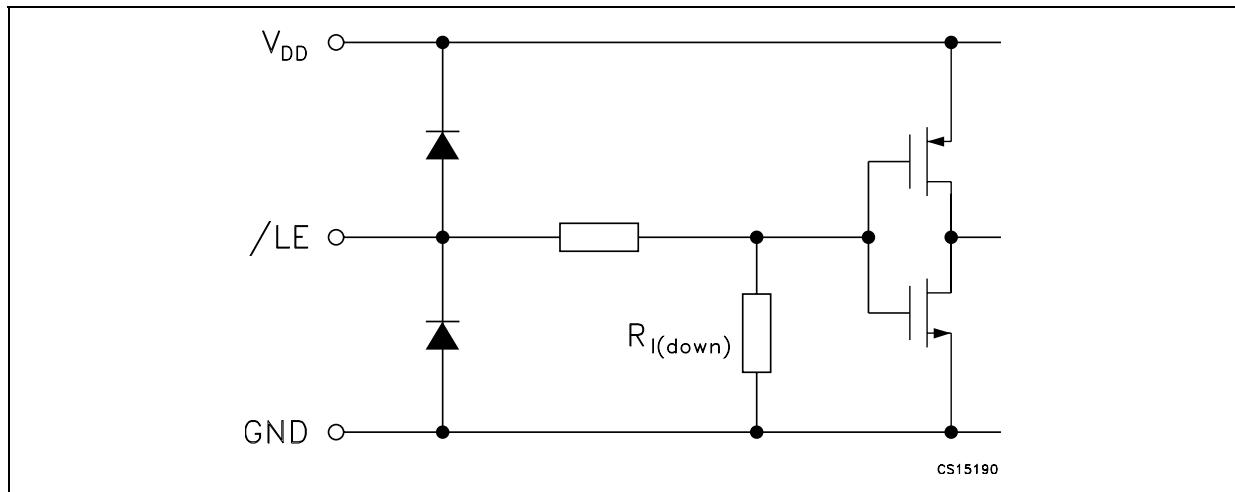
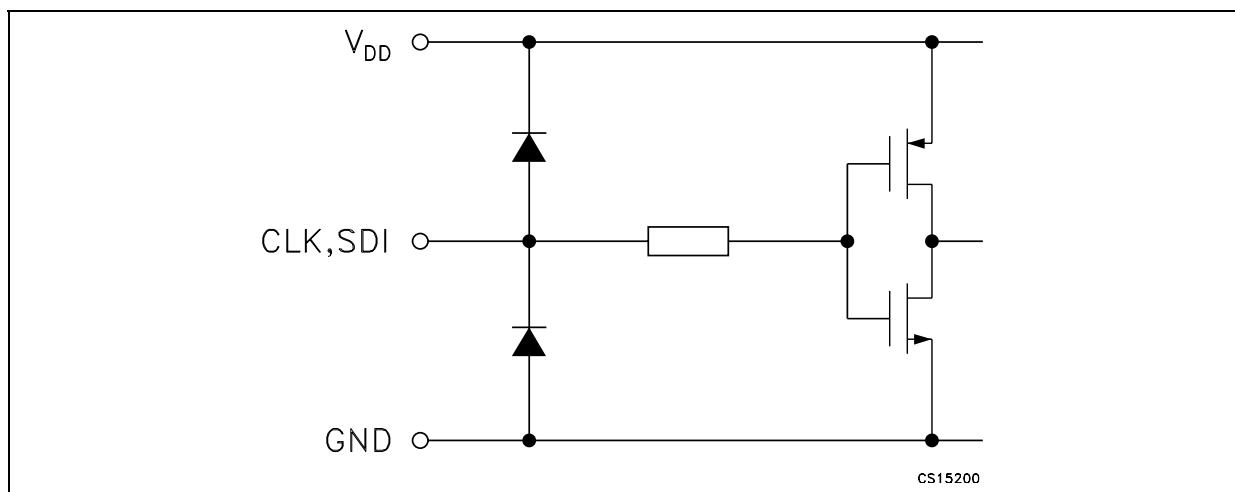
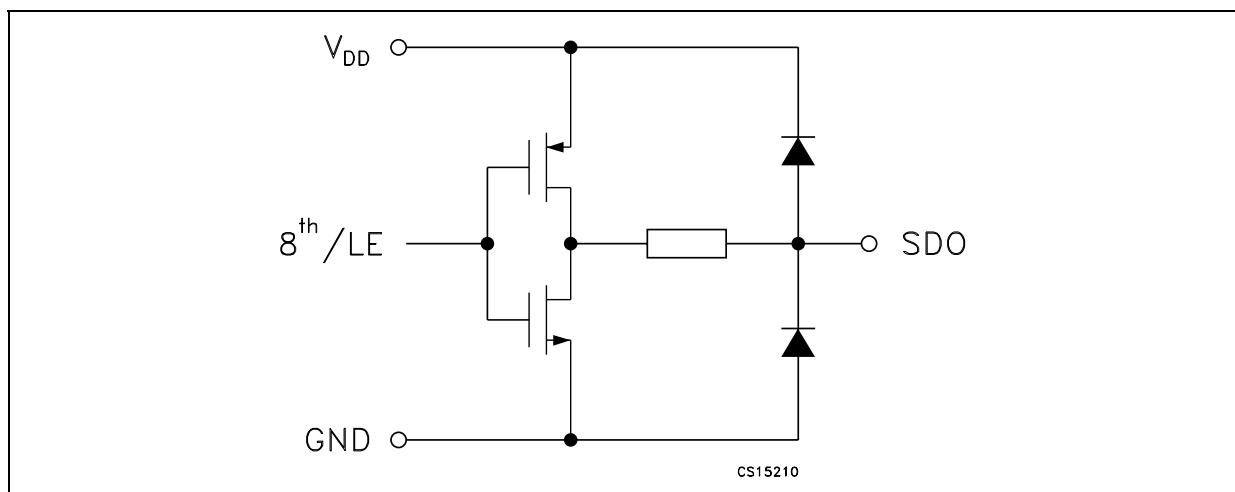
**SWITCHING CHARACTERISTICS ( $V_{DD}=3.3V$ ,  $T = 25^{\circ}C$ , unless otherwise specified.)**

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
$t_{PLH1}$	Propagation Delay Time, CLK-OUTn, /LE = H, /OE = L	$V_{DD} = 3 V$ $V_{IH} = V_{DD}$ $V_{IL} = GND$ $C_L = 13 pF$ $I_O = 40 mA$ $V_L = 3 V$ $R_{EXT} = 470 \Omega$ $R_L = 65 \Omega$			250	280	ns
$t_{PLH2}$	Propagation Delay Time, /LE-OUTn, /OE = L				250	280	ns
$t_{PLH3}$	Propagation Delay Time, /OE-OUTn, /LE = H				250	280	ns
$t_{PLH}$	Propagation Delay Time, CLK-SDO				25	35	ns
$t_{PHL1}$	Propagation Delay Time, CLK-OUTn, /LE = H, /OE = L				40	60	ns
$t_{PHL2}$	Propagation Delay Time, /LE-OUTn, /OE = L				30	50	ns
$t_{PHL3}$	Propagation Delay Time, /OE-OUTn, /LE = H				60	70	ns
$t_{PHL}$	Propagation Delay Time, CLK-SDO				30	40	ns
$t_r$	Output Rise Time				280		ns
$t_f$	Output Fall Time				20		ns

**EQUIVALENT CIRCUIT OF INPUTS AND OUTPUTS**

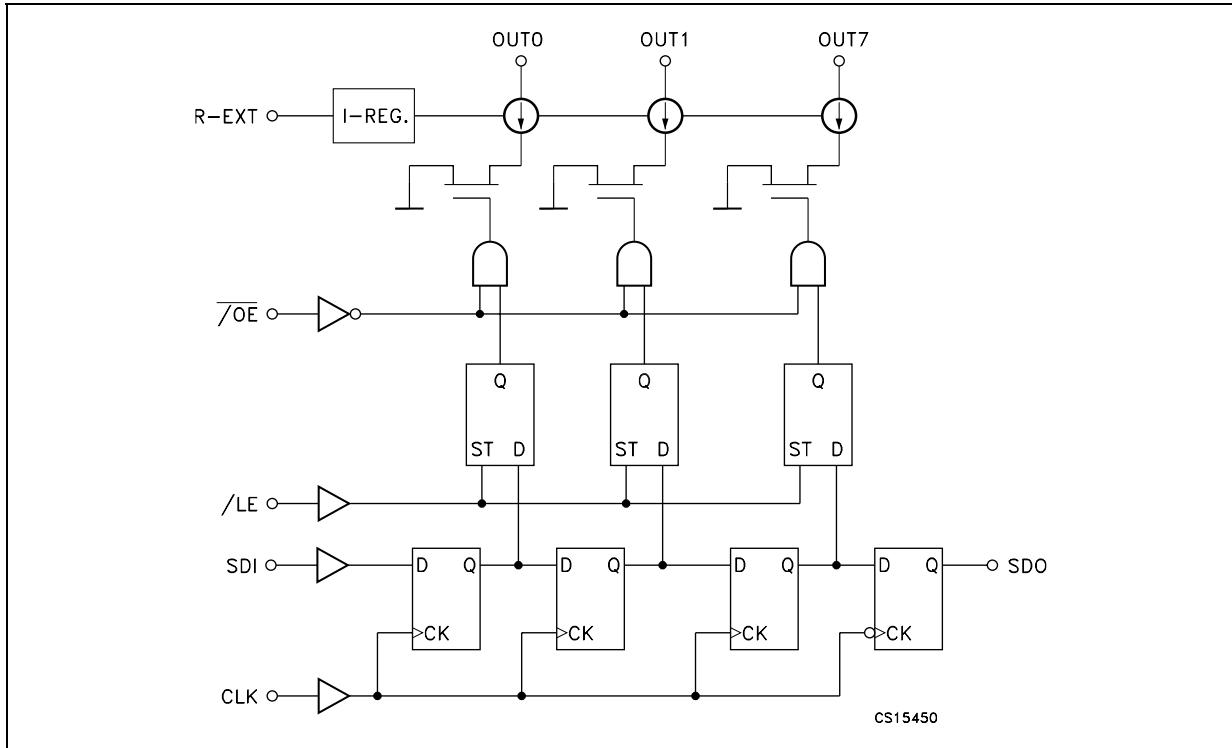
**Figure 1 :  $\overline{/OE}$  Terminal**



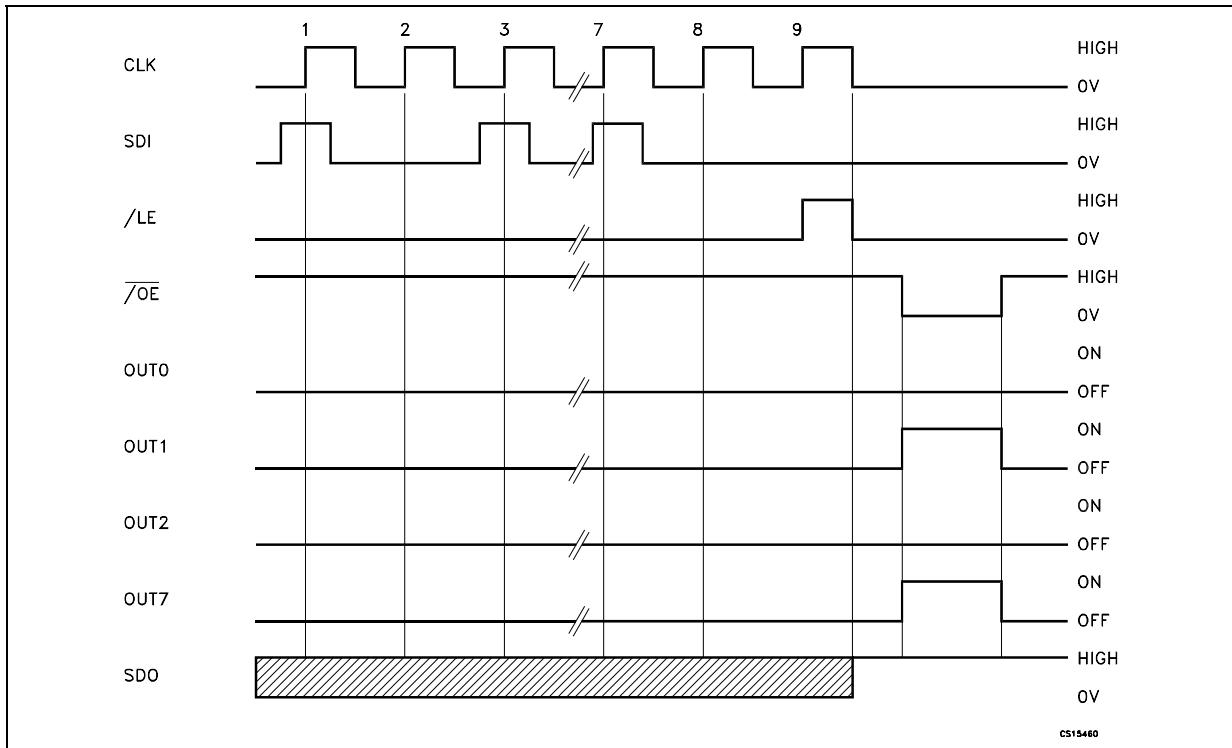
**Figure 2 : /LE Terminal****Figure 3 : CLK, SDI Terminal****Figure 4 : SDO Terminal**

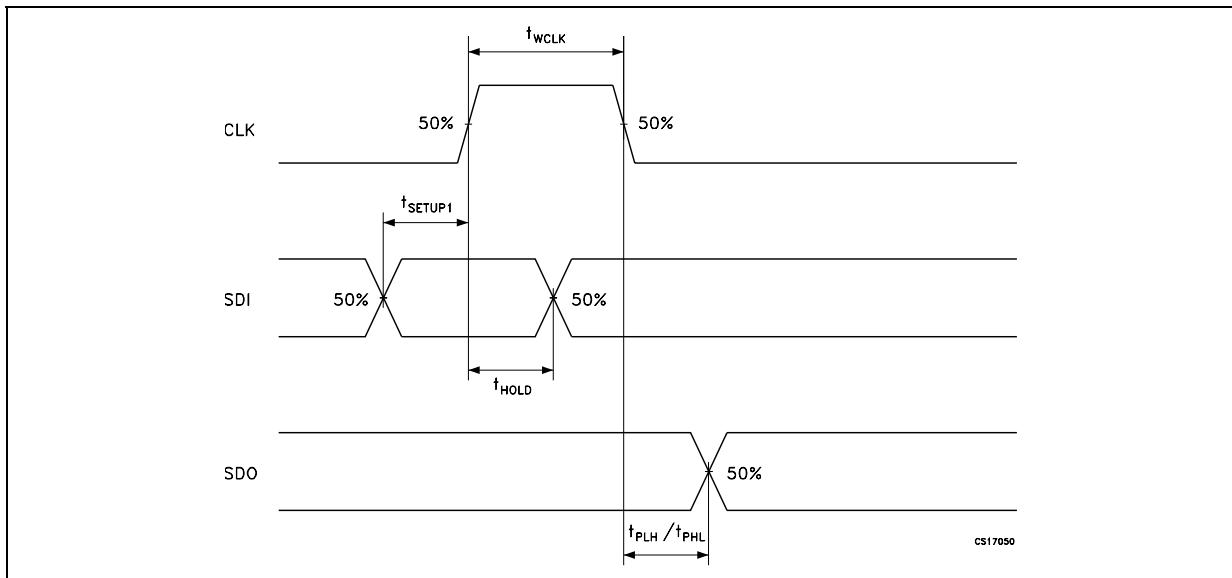
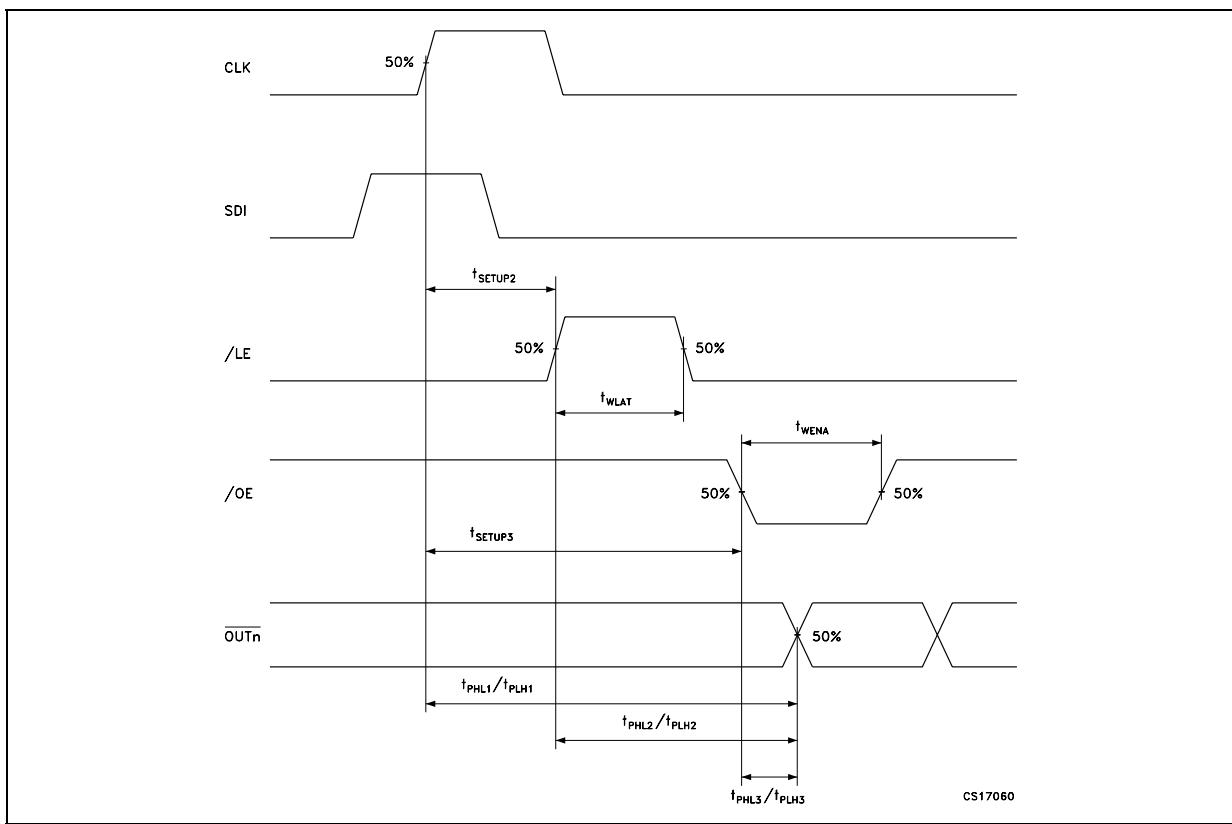
## STP08CL596

### BLOCK DIAGRAM



### TIMING DIAGRAM

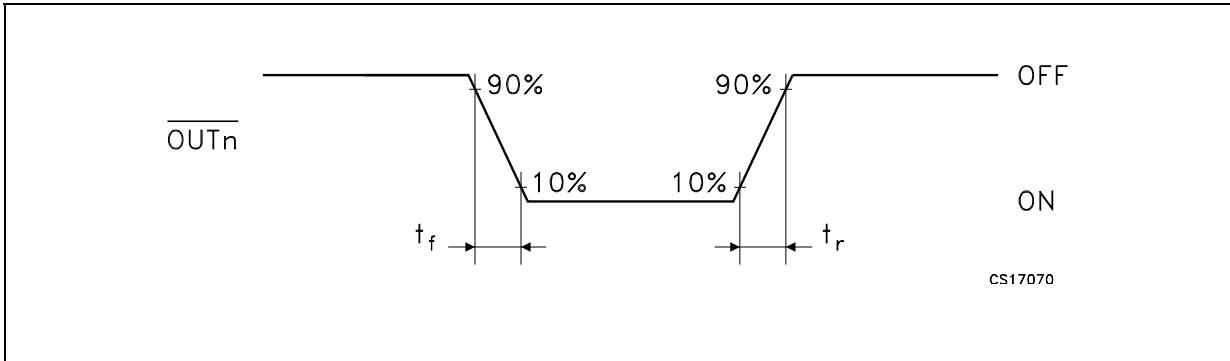


**CLOCK, SERIAL-IN, SERIAL-OUT****CLOCK, SERIAL-IN, LATCH, ENABLE, OUTPUTS**

## STP08CL596

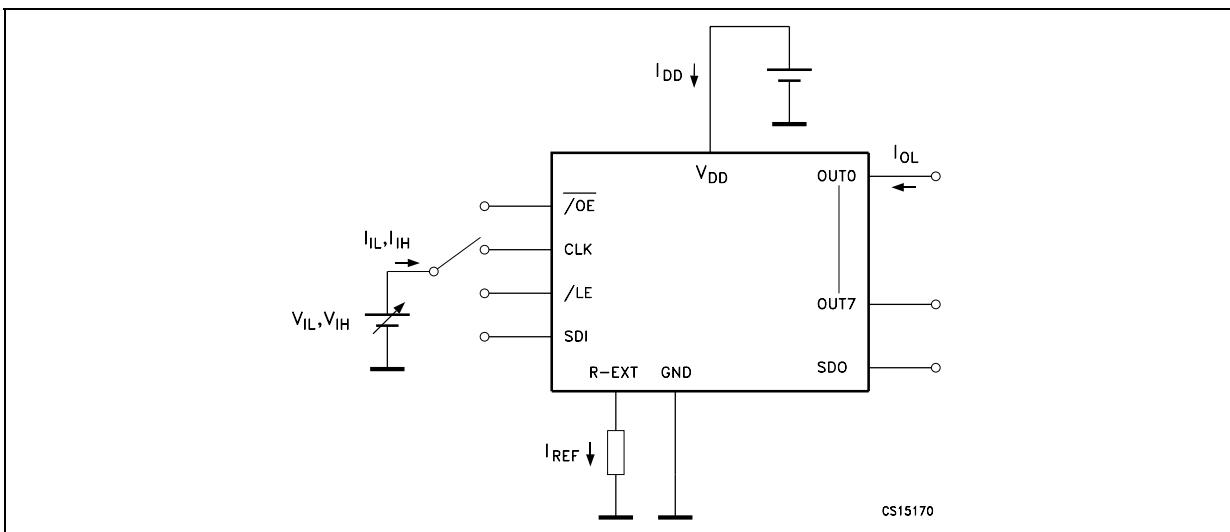
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### OUTPUTS

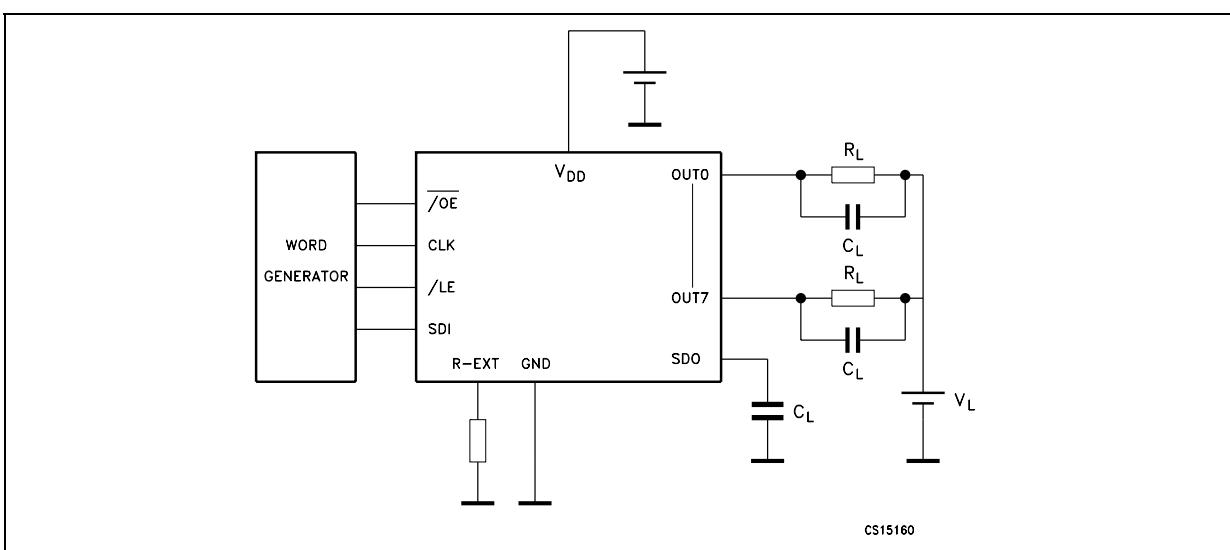


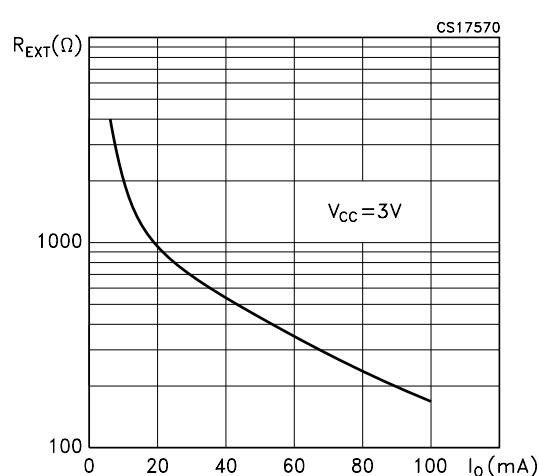
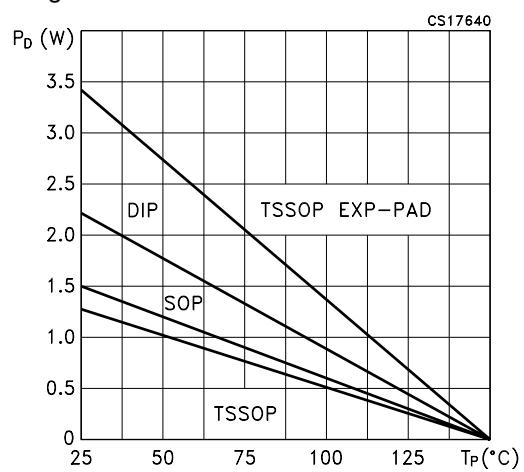
### TEST CIRCUIT

DC Characteristic



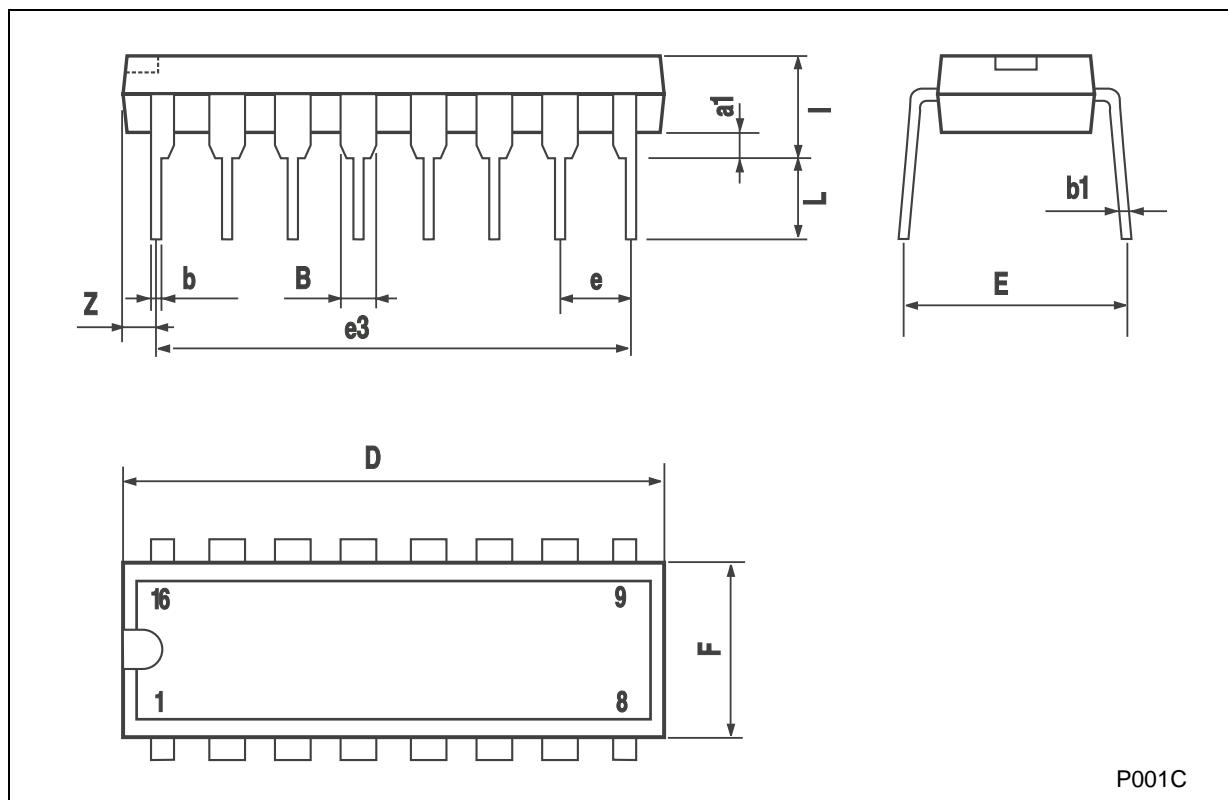
AC Characteristic



**Figure 5 : Output Current-R<sub>EXT</sub> Resistor****Figure 6 : Power Dissipation vs Temperature Package**

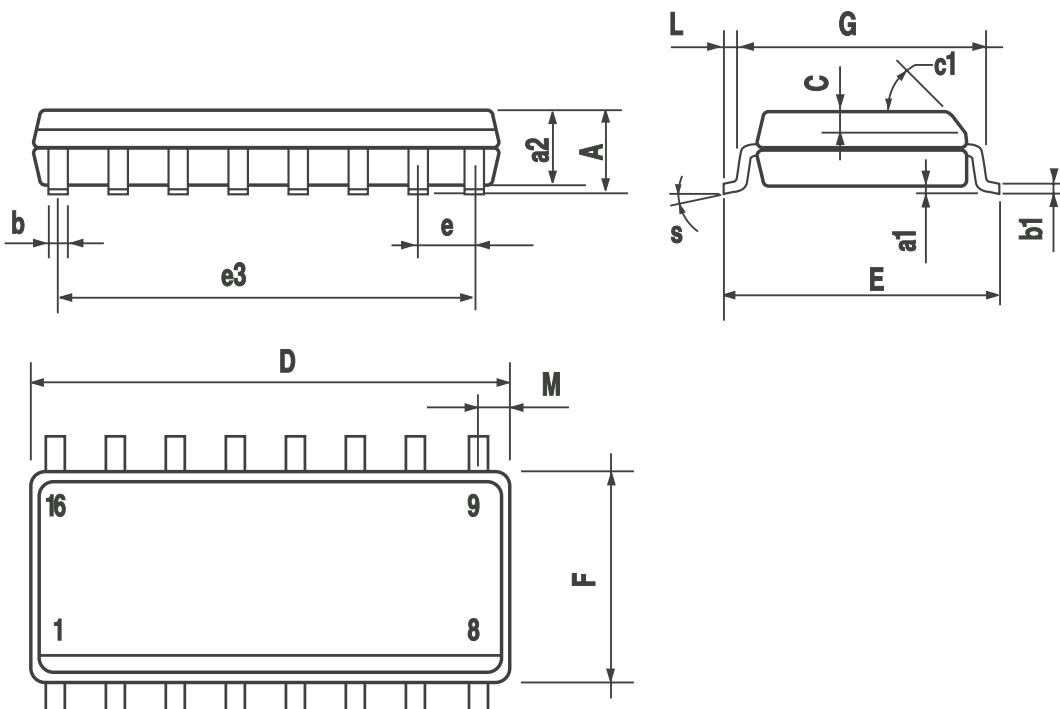
## Plastic DIP-16 (0.25) MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.51			0.020		
B	0.77		1.65	0.030		0.065
b		0.5			0.020	
b1		0.25			0.010	
D			20			0.787
E		8.5			0.335	
e		2.54			0.100	
e3		17.78			0.700	
F			7.1			0.280
I			5.1			0.201
L		3.3			0.130	
Z			1.27			0.050



## SO-16 MECHANICAL DATA

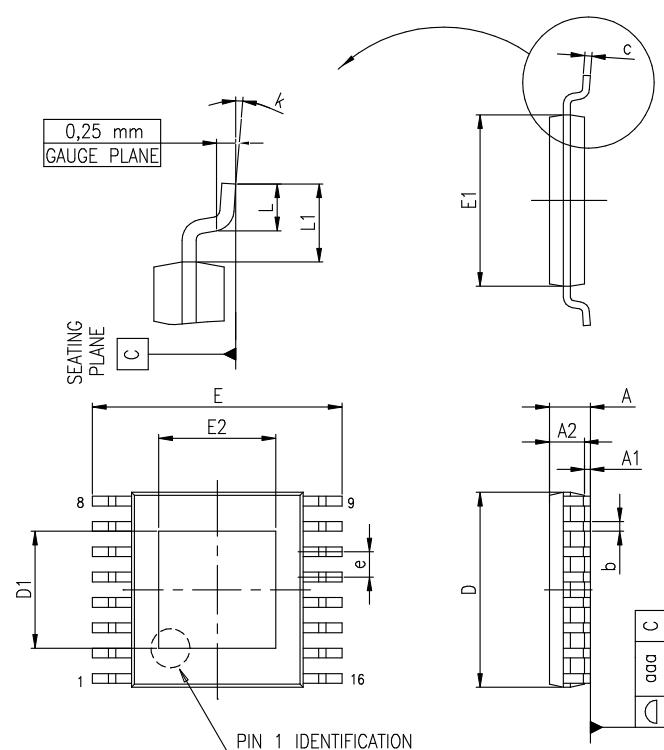
DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.75			0.068
a1	0.1		0.2	0.004		0.008
a2			1.65			0.064
b	0.35		0.46	0.013		0.018
b1	0.19		0.25	0.007		0.010
C		0.5			0.019	
c1		45° (typ.)				
D	9.8		10	0.385		0.393
E	5.8		6.2	0.228		0.244
e		1.27			0.050	
e3		8.89			0.350	
F	3.8		4.0	0.149		0.157
G	4.6		5.3	0.181		0.208
L	0.5		1.27	0.019		0.050
M			0.62			0.024
S	8	° (max.)				



PO13H

## TSSOP16 EXPOSED PAD MECHANICAL DATA

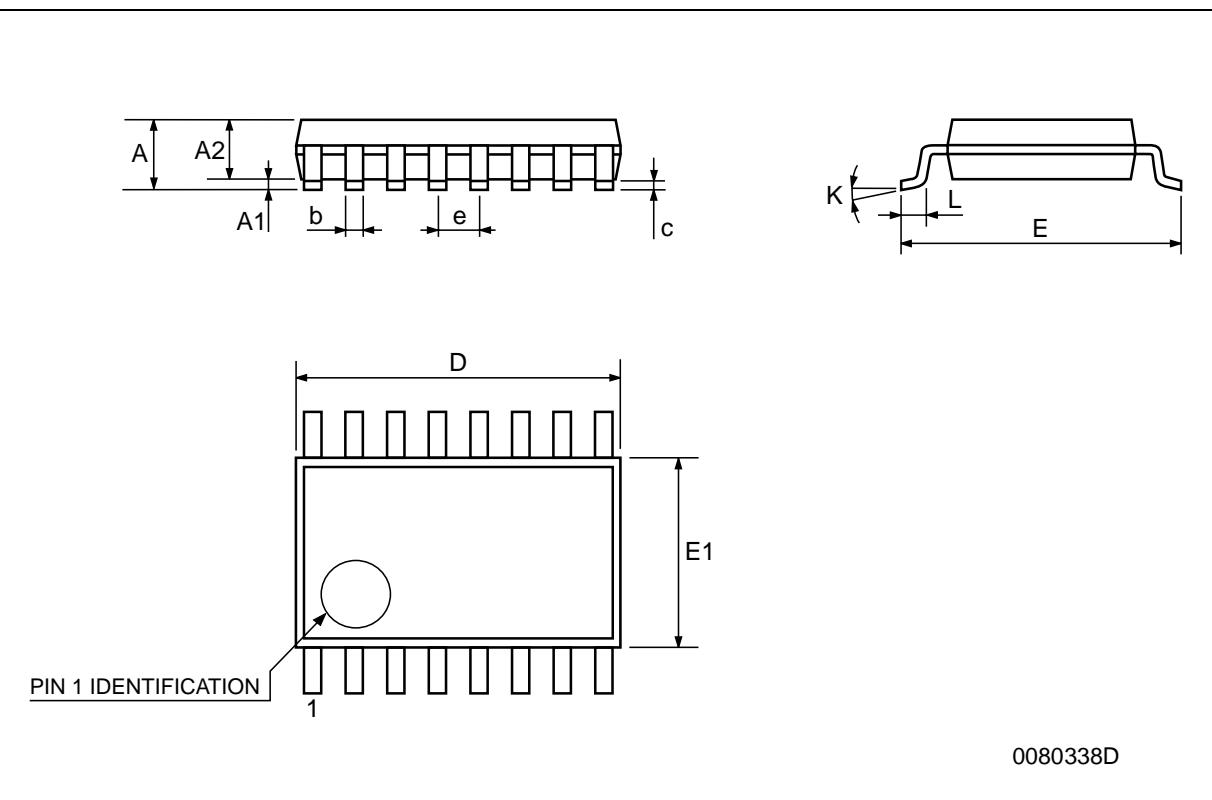
DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.2			0.047
A1			0.15		0.004	0.006
A2	0.8	1	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
c	0.09		0.20	0.004		0.0089
D	4.9	5	5.1	0.193	0.197	0.201
D1	1.7			0.067		
E	6.2	6.4	6.6	0.244	0.252	0.260
E1	4.3	4.4	4.5	0.169	0.173	0.177
E2	1.5			0.059		
e		0.65			0.0256	
K	0°		8°	0°		8°
L	0.45	0.60	0.75	0.018	0.024	0.030



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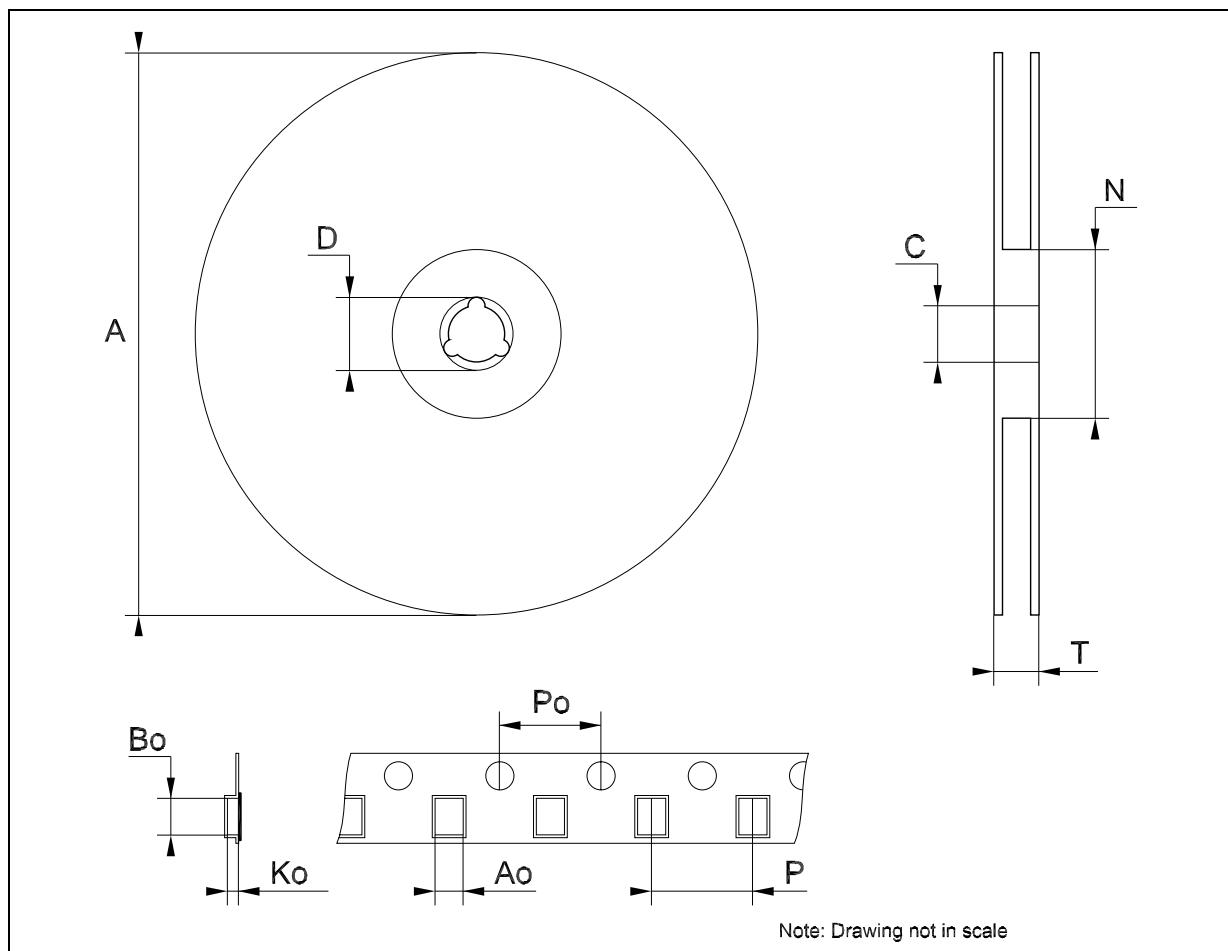
## TSSOP16 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			1.2			0.047
A1	0.05		0.15	0.002	0.004	0.006
A2	0.8	1	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
c	0.09		0.20	0.004		0.0079
D	4.9	5	5.1	0.193	0.197	0.201
E	6.2	6.4	6.6	0.244	0.252	0.260
E1	4.3	4.4	4.48	0.169	0.173	0.176
e		0.65 BSC			0.0256 BSC	
K	0°		8°	0°		8°
L	0.45	0.60	0.75	0.018	0.024	0.030



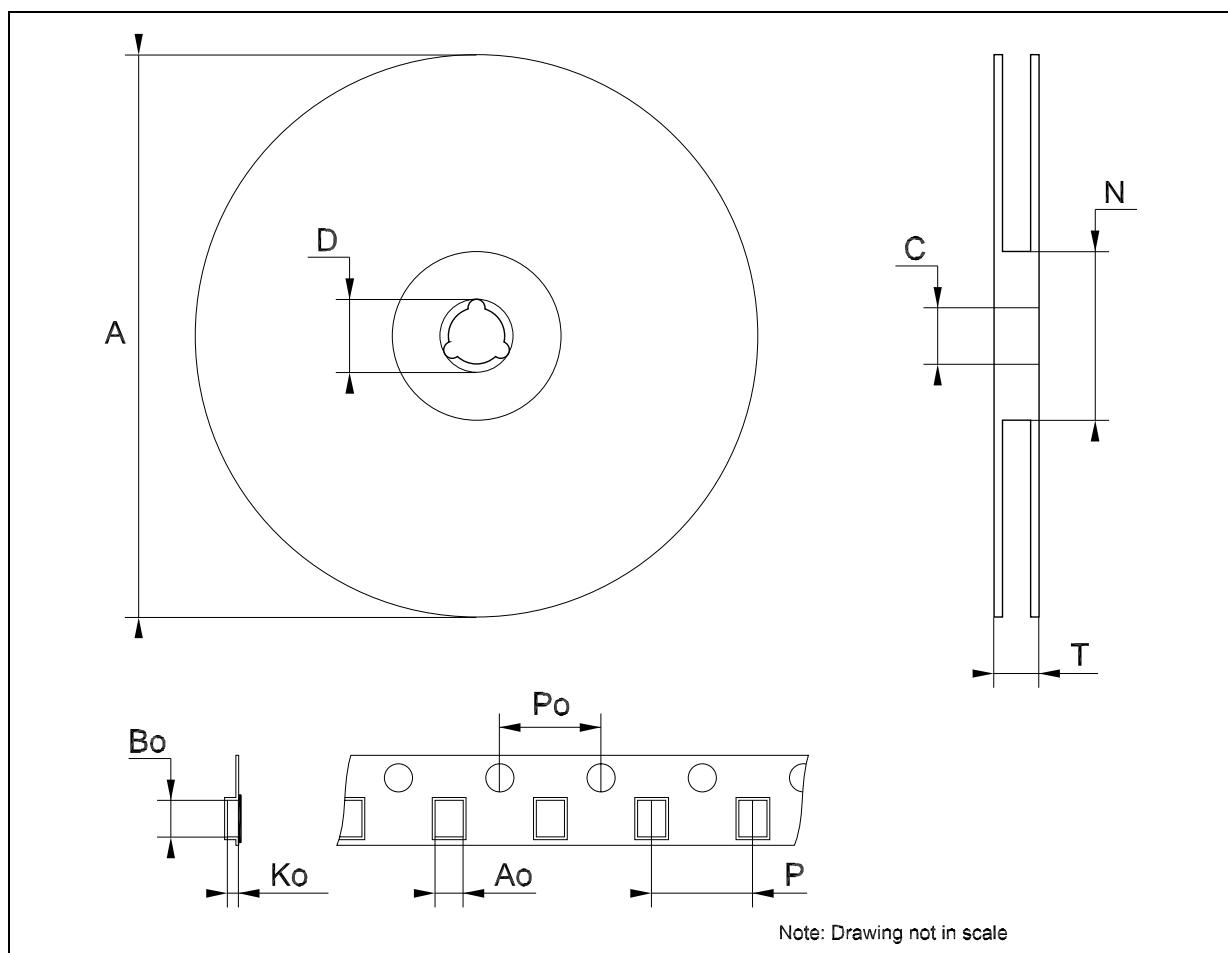
## Tape &amp; Reel SO-16 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			330			12.992
C	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
T			22.4			0.882
Ao	6.45		6.65	0.254		0.262
Bo	10.3		10.5	0.406		0.414
Ko	2.1		2.3	0.082		0.090
Po	3.9		4.1	0.153		0.161
P	7.9		8.1	0.311		0.319



<b>Tape &amp; Reel TSSOP16 MECHANICAL DATA</b>
--

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			330			12.992
C	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
T			22.4			0.882
Ao	6.7		6.9	0.264		0.272
Bo	5.3		5.5	0.209		0.217
Ko	1.6		1.8	0.063		0.071
Po	3.9		4.1	0.153		0.161
P	7.9		8.1	0.311		0.319



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