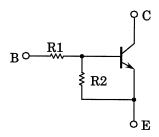
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process)

RN1130F

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

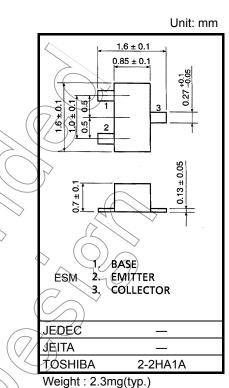
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- Complementary to RN2130F

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

				\[
Characteristic	Symbol	Rating	Unit	
Collector-base voltage	V _{CB} Ø	50	V	
Collector-emitter voltage	VCEQ)) 50	v	/ /
Emitter-base voltage	VEBO	10	∕ v	
Collector current		100	mA	
Collector power dissipation	PC	100	m₩	
Junction temperature	()†j	150	°℃	
Storage temperature range	T _{stg}	-55 to 150	°C	



 Storage temperature range
 T_{stg}
 -55 to 150
 °C

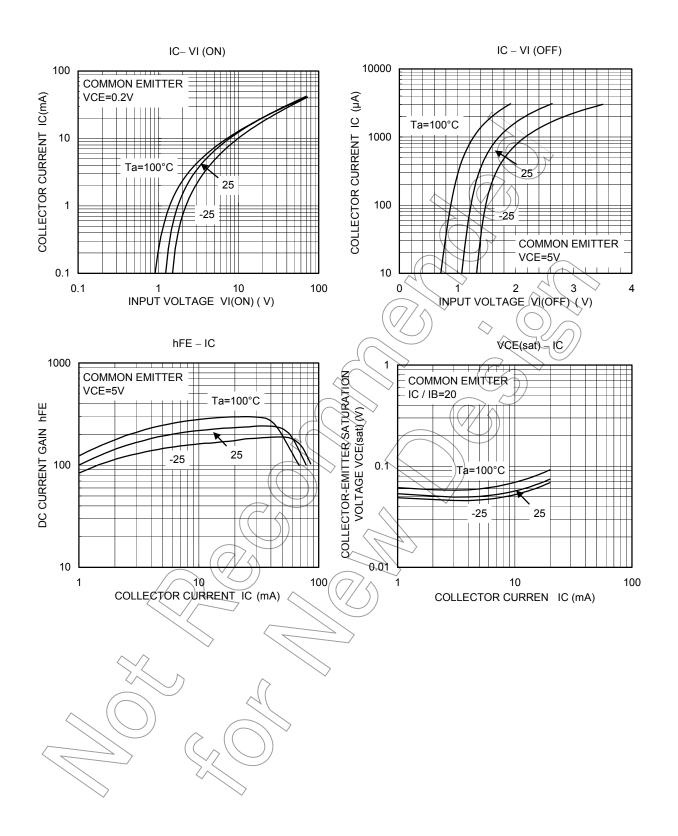
 Note:
 Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum

ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

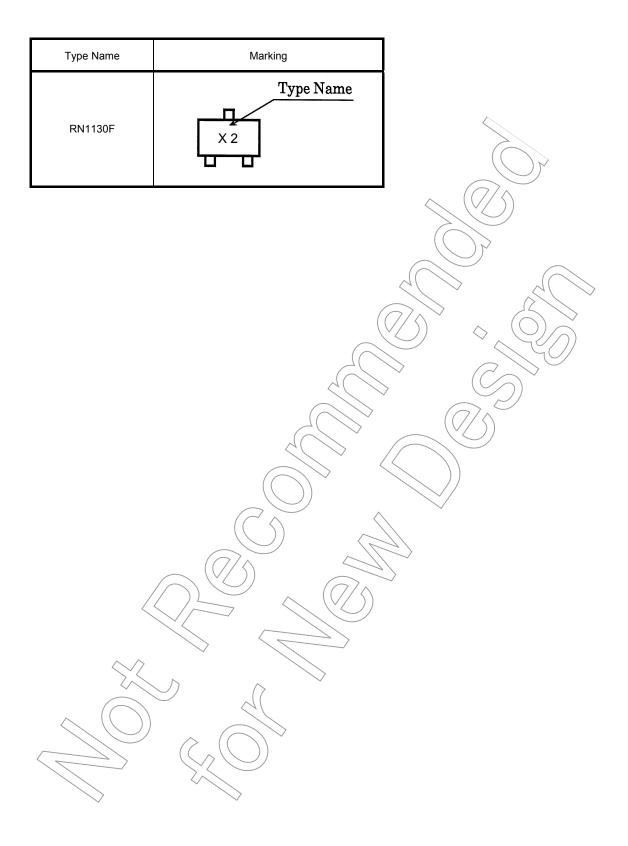
Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	V _{CB} = 50V, I _E = 0	—	—	100	nA
	I _{CEO}	V _{CE} = 50V, I _B = 0	_	_	500	nA
Emitter cut-off current	I _{EBO}	V _{EB} =10V, I _C = 0	38	$\langle A \rangle$	72	μA
DC current gain	h _{FE}	V _{CE} = 5V, I _C = 10mA	100	->	X	
Collector-emitter saturation voltage	V _{CE (sat)}	I _C =5mA, I _B =0.25mA	—	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	V_{CE} = 0.2V, I _C = 5mA	1.7		8.2	V
Input voltage (OFF)	VI(OFF)	V _{CE} = 5V, I _C = 0.1mA	1.0 ($V \neq S$	1.6	V
Transition frequency	fT	V _{CE} = 10V, I _C = 5mA	λ	250		MHz
Collector output capacitance	C _{ob}	V _{CB} = 10V, I _E = 0, f = 1MHz) 3		pF
Input resistor	R1	- (70	100	130	κΩ
Resistance ratio	R1/ R2	- <<	0.8	1.0	1.2	$\langle \rangle$

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