

High Speed Dynamic RAM

Rambus DRAM

X6

Capacity	Type No.	Organization	Data Transfer Rate (ns)		Hit Latency (ns)		Power Supply (V)	Power Dissipation (mW)	No. of Pins	Package Width
			Min	Max	Read	Write				
4.5M Bit	*TC59R0409VK	524,288 x 9	2	5	48	16	5V±10%	1325	32	SVP32 (0.65mm pitch)
18M Bit	**TC59R1809VK	2,097,152 x 9	2	5	40	16	3.3V±0.3V	TBD	32	SVP (0.65mm pitch)
	**TC85RT000VK	Rambus Transceiver	2	5	—	—	5V±0.3V	TBD	42	SVP42 (0.5mm pitch)

Synchronous DRAM

X6

Capacity	Type No.	Organization	Access Time (ns)		Clock Cycle Time (ns)	Power Supply (V)	Power Dissipation (mW)		No. of Pins	Package Width
			tRAC	tCAC			Active	Stand-by		
16M Bit	**TC59S1604FT/FTL-10	4,194,304 x 4 (2Bank)	60	30	10	3.3V±0.3V	504	3.6	44 (42)	TSOP(II): 0.4 (0.8mm pitch)
	**TC59S1604FT/FTL-12		72	36	12		468	3.6		
	**TC59S1608FT/FTL-10	2,097,152 x 8 (2Bank)	60	30	10		504	3.6		
	**TC59S1608FT/FTL-12		72	36	12		468	3.6		