AG603-86

InGaP HBT Gain Block

Product Information

Product Features

- DC – 6000 MHz
- +19.5 dBm P1dB at 900 MHz
- +33.5 dBm OIP3 at 900 MHz
- 18.5 dB Gain at 900 MHz
- Single Voltage Supply
- Green SOT-86 SMT Package
- Internally matched to 50 Ω

Applications

- Mobile Infrastructure
- CATV / DBS
- W-LAN / ISM
- RFID
- Defense / Homeland Security
- Fixed Wireless

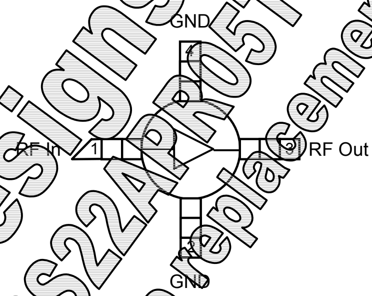
Product Description

The AG603-86 is a general-purpose buffer amplifier that offers high dynamic range in a low-cost surface-mount package. At 900 MHz, the AG603-86 typically provides 18.5 dB gain, +33.5 dBm OIP3, and +19.5 dBm P1dB. The device combines dependable performance with consistent quality to maintain MTTF values exceeding 100 years at mounting temperatures of +85 °C & is housed in a SOT-86 industry-standard SMT lead-free/green/RoHS-compliant package.

The AG603-86 consists of Darlington pair amplifiers using the high reliability InGaP/GaAs HBT process technology and only requires DC-blocking capacitors, a bias resistor, and an inductive RF choke for operation.

The broadband MMIC amplifier can be directly applied to various current and next generation wireless technologies such as GPRS, GSM, CDMA, and W-CDMA. In addition, the AG603-86 will work for other various applications within the DC to 6 GHz frequency range such as CATV and fixed wireless.

Functional Diagram



Function	Pin No.
Input	1
Output/Bias	3
Ground	2, 4

Specifications (1)

Parameter	Units	Min	Typ	Max
Operational Bandwidth	MHz	DC		6000
Test Frequency	MHz		900	
Gain	dB		18.2	
Input Return Loss	dB		20	
Output Return Loss	dB		17	
Output IP3 (2)	dBm		+19.4	
Output IP2	dBm		+33.7	
Output P1dB	dBm		+45	
Noise Figure	dB		3.8	
Test Frequency	MHz		1900	
Gain	dB	14.9	15.9	16.9
Output IP3 (2)	dBm		+19.2	
Output P1dB	dBm		+33.4	
Device Voltage	V		5.16	
Device Current	mA		75	

1. Test conditions: T = 25° C, Supply Voltage = +6 V, R_{bias} = 11.2 Ω, 50 Ω System.
2. 3OIP measured with two tones at an output power of +2 dBm/tone separated by 10 MHz. The suppression on the largest IM3 product is used to calculate the 3OIP using a 2:1 rule.
3. The junction temperature ensures a minimum MTTF rating of 1 million hours of usage.

Typical Performance (1)

Parameter	Units	Typical			
Frequency	MHz	500	900	1900	2140
S21	dB	18.9	18.2	15.9	15.3
S11	dB	-17	-20	-18	-17
S22	dB	-21	-17	-14	-13
Output P1dB	dBm	+19.4	+19.4	+19.2	+19.1
Output IP3	dBm	+34.1	+33.7	+33.4	+32.8
Noise Figure	dB	3.8	3.8	3.9	4.0

Absolute Maximum Rating

Parameter	Rating
Operating Case Temperature	-40 to +85 °C
Storage Temperature	-55 to +125 °C
DC Voltage	+7 V
RF Input Power (continuous)	+10 dBm
Junction Temperature	+250° C

Operation of this device above any of these parameters may cause permanent damage.

Ordering Information

Part No.	Description
AG603-86	InGaP HBT Gain Block (lead-tin SOT-86 Pkg)
AG603-86G	InGaP HBT Gain Block (lead-free/green/RoHS-compliant SOT-86 Pkg)

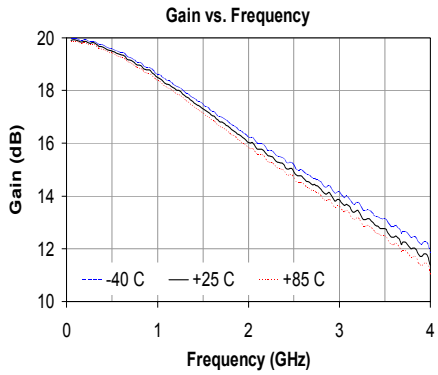
Specifications and information are subject to change without notice

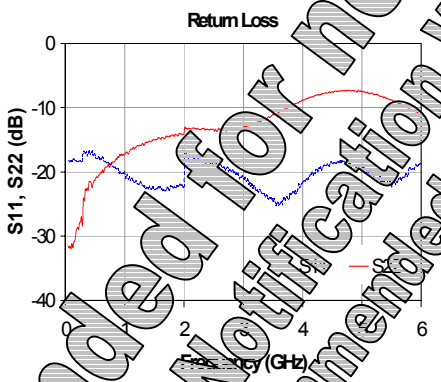
Typical Device RF Performance

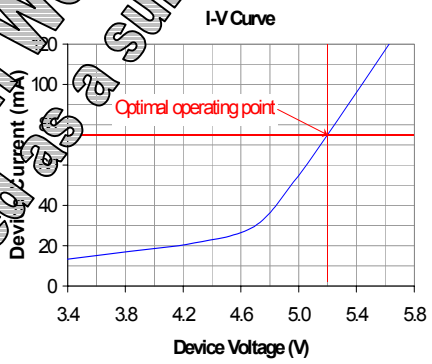
Supply Bias = +6 V, R_{bias} = 11.2 Ω, I_{cc} = 75 mA

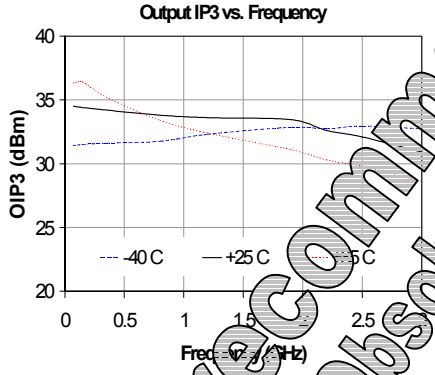
Frequency	MHz	100	500	900	1900	2140	2400	3500	3900
S21	dB	19.3	18.9	18.2	15.9	15.3	14.9	12.7	9.4
S11	dB	-18	-17	-20	-18	-17	-18	-21	-19
S22	dB	-31	-21	-17	-14	-13	-13	-14	-14
Output P1dB	dBm	+19.4	+19.4	+19.4	+19.2	+19.1	19.[illegible]	17.5	14.1
Output IP3	dBm	+34.5	+34.1	+33.7	+33.4	+32.8	32.[illegible]		
Noise Figure	dB	3.8	3.8	3.8	3.9	4.0	4.[illegible]		

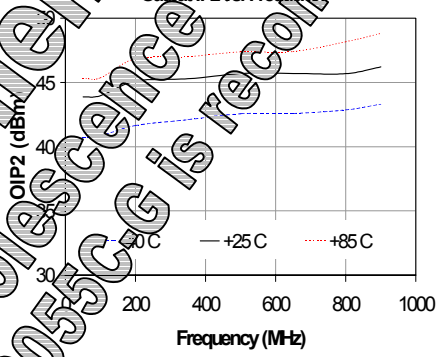
1. Test conditions: T = 25° C, Supply Voltage = +6 V, Device Voltage = 5.16 V, Rbias = 11.2 Ω, Icc = 75 mA typical, 50 Ω System
2. 3OIP measured with two tones at an output power of +2 dBm/tone separated by 10 MHz. The suppression on the largest IM3 product is used to calculate the OIP using a 2:1 rule.
3. Data is shown as device performance only. Actual implementation for the desired frequency band will be determined by external components shown in the application circuit.

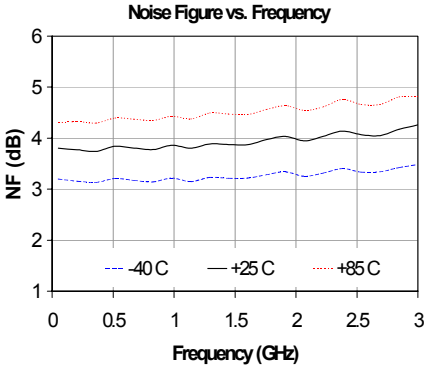


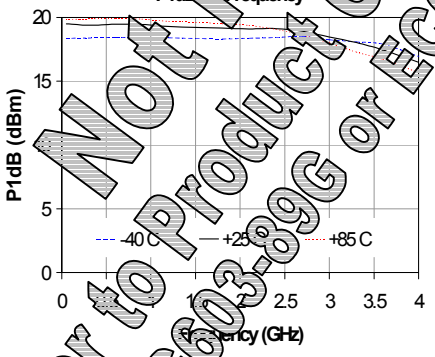


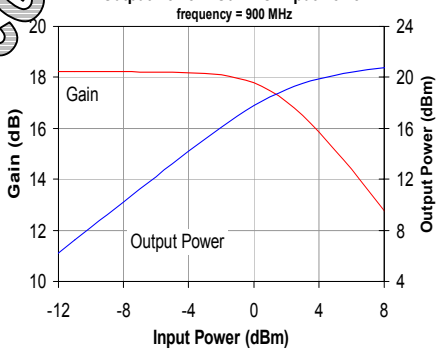


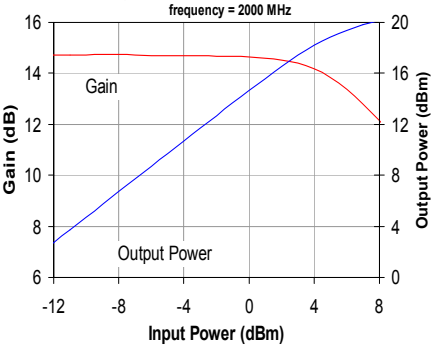






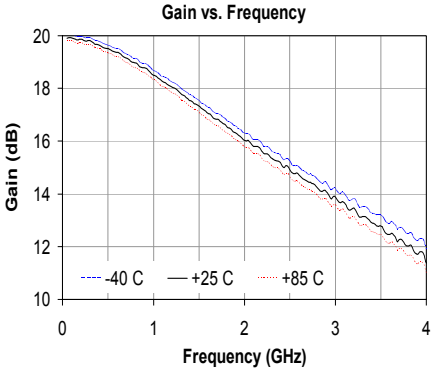


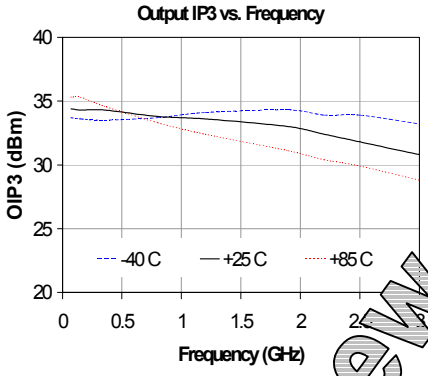


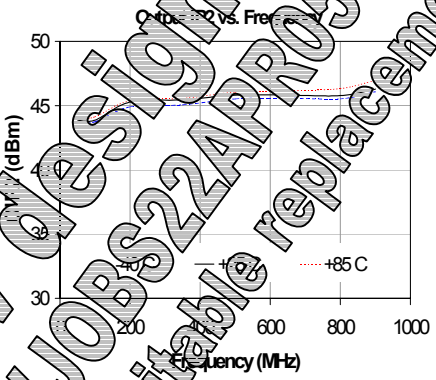


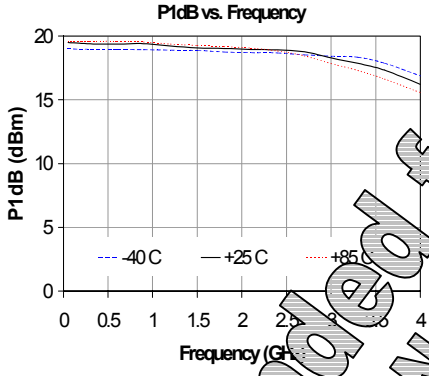
Typical Device RF Performance (cont'd)

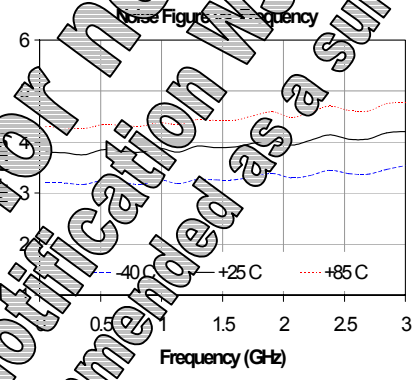
Supply Bias = +8 V, R_{bias} = 38 Ω, I_{cc} = 75 mA



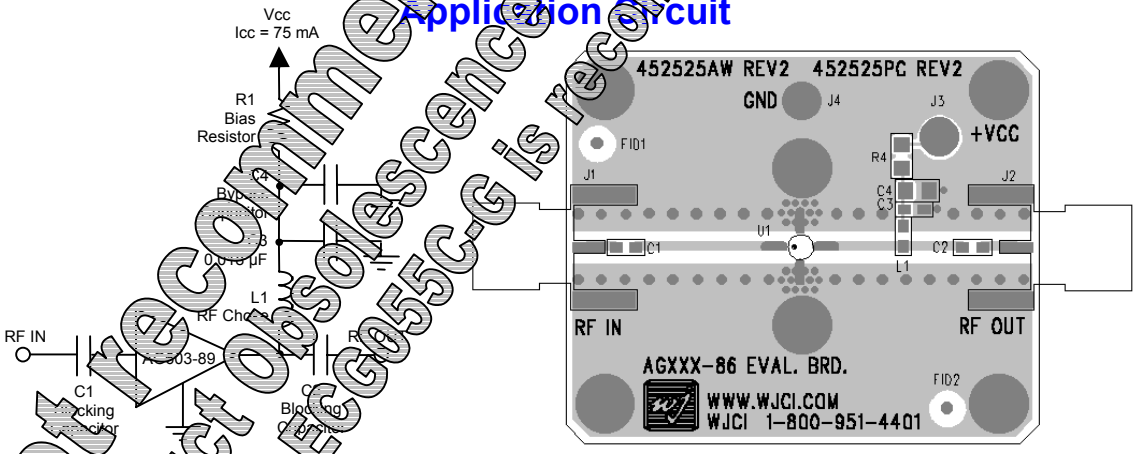








Application Circuit



Recommended Component Values

Reference Designator	Frequency (MHz)						
	50	500	900	1900	2200	2500	3500
L1	270 nH	82 nH	68 nH	27 nH	22 nH	18 nH	15 nH
C1, C2, C4	.018 µF	1000 pF	100 pF	68 pF	68 pF	56 pF	39 pF

1. The proper values for the components are dependent upon the intended frequency of operation.
2. The following values are contained on the evaluation board to achieve optimal broadband performance:

Ref. Des.	Value / Type	Size
L1	39 nH wirewound inductor	0603
C1, C2	56 pF chip capacitor	0603
C3	0.018 µF chip capacitor	0603
C4	Do Not Place	
R1	10.0 Ω 1% tolerance	0805

Recommended Bias Resistor Values

Supply Voltage	R1 value	Size
6 V	11.2 ohms	0805
7 V	24.5 ohms	1210
8 V	38 ohms	1210
9 V	51 ohms	2010
10 V	65 ohms	2010
12 V	91 ohms	2512

The proper value for R1 is dependent upon the supply voltage and allows for bias stability over temperature. WJ recommends a minimum supply bias of +6 V. A 1% tolerance resistor is recommended.

Typical Device Data

S-Parameters (V_{device} = +5.16 V, I_{CC} = 75 mA, T = 25° C, calibrated to device leads)

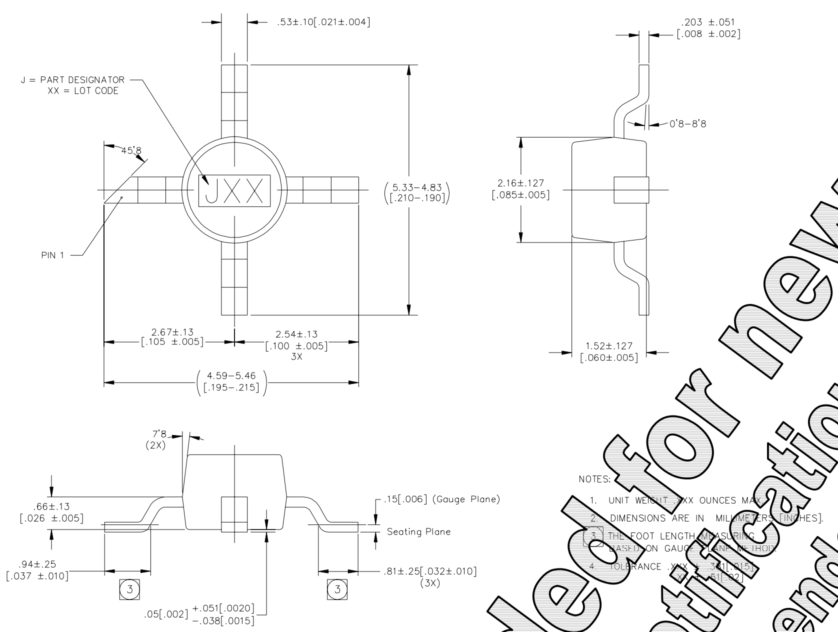
Freq (MHz)	S11 (dB)	S11 (ang)	S21 (dB)	S21 (ang)	S12 (dB)	S12 (ang)	S22 (dB)	S22 (ang)
50	-18.23	174.74	19.89	177.21	-22.60	-0.68	[illegible]	[illegible]
250	-18.28	162.59	19.75	165.56	-22.48	-0.53	[illegible]	[illegible]
500	-17.36	147.85	19.50	151.86	-22.44	-0.61	[illegible]	-148.15
750	-18.86	130.86	19.09	138.79	-22.46	-1.53	[illegible]	[illegible]
1000	-20.20	108.49	18.57	126.44	-22.42	[illegible]	[illegible]	[illegible]
1250	-21.82	84.57	18.05	114.78	-22.30	[illegible]	[illegible]	[illegible]
1500	-22.73	53.36	17.47	103.87	-22.14	[illegible]	[illegible]	[illegible]
1750	-22.84	27.48	16.86	93.52	-21.87	[illegible]	[illegible]	-160.07
2000	-21.25	9.33	16.26	83.93	-21.75	-4.47	[illegible]	-166.54
2250	-18.04	-0.39	15.69	75.43	-21.40	-5.54	[illegible]	-159.08
2500	-18.33	-7.20	15.34	68.57	-20.87	-5.78	-13.[illegible]	-168.98
2750	-19.57	-12.19	14.80	59.90	[illegible]	-9.55	[illegible]	177.37
3000	-21.03	-10.51	14.30	51.45	[illegible]	[illegible]	[illegible]	159.43
3250	-23.01	-10.01	13.83	43.05	[illegible]	[illegible]	[illegible]	143.27
3500	-24.57	13.25	13.33	34.97	[illegible]	-16.08	-11.07	128.02
3750	-24.13	41.90	12.78	26.51	-19.65	-18.64	-10.02	115.52
4000	-21.65	64.98	12.28	18.62	-19.25	-22.08	-8.79	106.49
4250	-19.84	74.82	11.83	[illegible]	-19.[illegible]	-25.[illegible]	-8.01	99.24
4500	-18.66	82.81	11.40	[illegible]	-19.[illegible]	-28.[illegible]	-7.55	94.08
4750	-18.43	89.31	10.99	-3.54	[illegible]	-30.96	-7.34	90.85
5000	-19.43	99.10	10.68	-10.20	[illegible]	-35.34	-7.50	88.62
5250	-21.15	118.11	10.43	-16.78	-17.98	-37.98	-7.93	86.82
5500	-22.30	145.00	10.25	-23.41	-17.55	-40.47	-8.80	85.75
5750	-20.49	175.26	[illegible]	-30.29	-17.[illegible]	-44.86	-9.84	84.21
6000	-18.75	-172.37	[illegible]	[illegible]	[illegible]	-47.67	-11.15	80.32

Device S-parameters are available for download off of the website at: http://www.wj.com

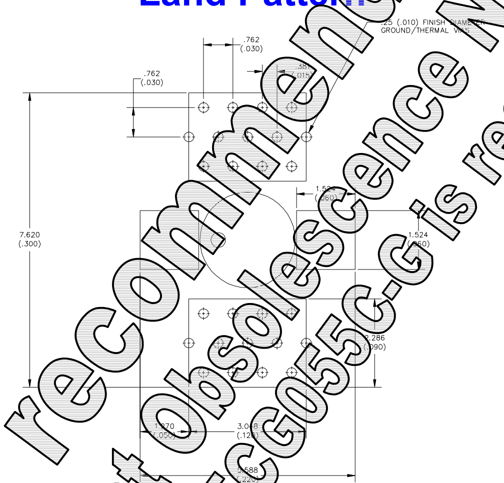
AG603-86 (SOT-86 Package) Mechanical Information

This package may contain lead-bearing materials. The plating material on the leads is SnPb.

Outline Drawing



Land Pattern



Thermal Specifications

Parameter	Rating
Operating Case Temperature	-40 to +85 °C
Thermal Resistance, Rth [(1)]	206 °C/W
Junction Temperature, Tjc [(2)]	165 °C

1. The thermal resistance is referenced from the hottest part of the junction to the ground lead (pin 2 or 4).
2. This corresponds to the typical biasing condition of +5.16V, 75 mA at an 85 °C case temperature. A minimum MTTF of 1 million hours is achieved for junction temperatures below 177 °C.

Product Marking

The component will be marked with a "J" designator followed by a two-digit numeric lot code on the top surface of the package.

Tape and reel specifications for this part are located on the website in the "Application Notes" section.

MSL / ESD Rating

Caution! ESD sensitive device.

ESD Rating: Class 0
Value: Passes at 150 V
Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

ESD Rating: Class II
Value: Passes at 250 V
Test: Charged Device Model (CDM)
Standard: JEDEC Standard JESD22-C101

MSL Rating: Level 1
Standard: JEDEC Standard J-STD-020A

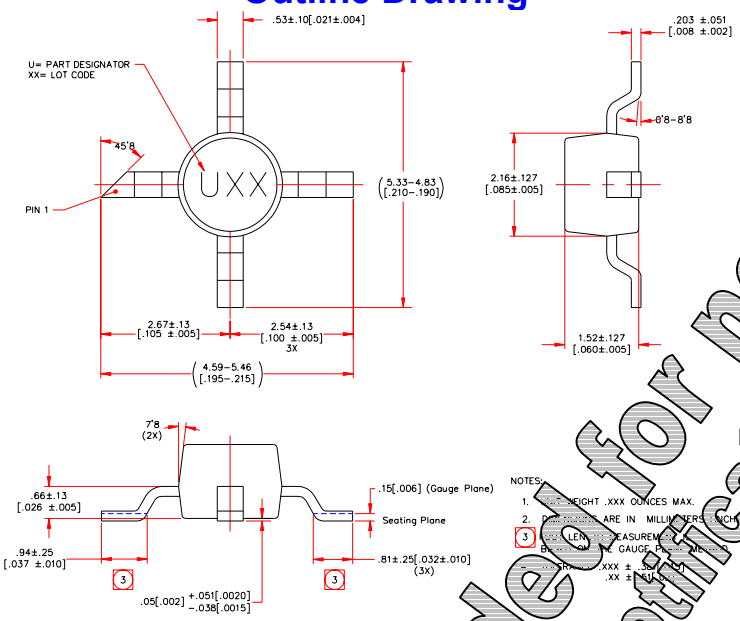
Mounting Config. Notes

1. Ground / thermal vias are critical for the proper performance of this device. Vias should use a .35mm (#80 / .0135") diameter drill and have a final plated thru diameter of .25 mm (.010").
2. Add as much copper as possible to inner and outer layers near the part to ensure optimal thermal performance.
3. Mounting screws can be added near the part to fasten the board to a heatsink. Ensure that the ground / thermal via region contacts the heatsink.
4. Do not put solder mask on the backside of the PC board in the region where the board contacts the heatsink.
5. RF trace width depends upon the PC board material and construction.
6. Use 1 oz. Copper minimum.
7. All dimensions are in millimeters (inches). Angles are in degrees.

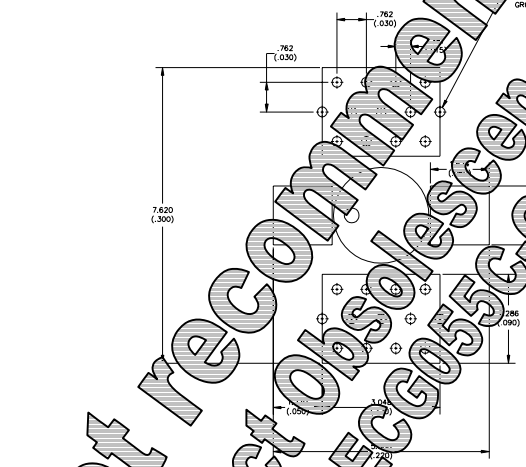
AG603-86G (Green / Lead-free Sot-86 Package) Mechanical Information

This package is lead-free/Green/RoHS-compliant. It is compatible with both lead-free (maximum 260°C reflow temperature) and tin-lead (maximum 245°C reflow temperature) soldering processes. The plating material on the pins is annealed matte tin over copper.

Outline Drawing



Land Pattern



Thermal Specifications

Parameter	Rating
Operating Case Temperature	-40 to +85 °C
Thermal Resistance Rth (1)	206 °C/W
Junction Temperature, Tj (2)	165 °C

1. The thermal resistance is referenced from the hottest part of the junction to the ground lead (pin 2 or 4).
2. This corresponds to the typical biasing condition of +5V, 80 mA at an 85 °C case temperature. A maximum MTTF of 1 million hours is achieved for junction temperatures below 177 °C.

Product Marking

The component will be marked with an "U" designator followed by a two-digit numeric lot code on the top surface of the package.

Tape and reel specifications for this part are located on the website in the "Application Notes" section.

MSL / ESD Rating

Caution! ESD sensitive device.

ESD Rating:	Class 1C
Value:	Passes at 1000 V min.
Test:	Human Body Model (HBM)
Standard:	JEDEC Standard JESD22-A114
ESD Rating:	Class IV
Value:	Passes at 1000 V min.
Test:	Charged Device Model (CDM)
Standard:	JEDEC Standard JESD22-C101
MSL Rating:	Level 3 at +260° C convection reflow
Standard:	JEDEC Standard J-STD-020

Mounting Config. Notes

1. Ground / thermal vias are critical for the proper performance of this device. Vias should use a .35mm (#80 / .0135") diameter drill and have a final plated thru diameter of .25 mm (.010").
2. Add as much copper as possible to inner and outer layers near the part to ensure optimal thermal performance.
3. Mounting screws can be added near the part to fasten the board to a heatsink. Ensure that the ground / thermal via region contacts the heatsink.
4. Do not put solder mask on the backside of the PC board in the region where the board contacts the heatsink.
5. RF trace width depends upon the PC board material and construction.
6. Use 1 oz. Copper minimum.
7. All dimensions are in millimeters (inches). Angles are in degrees.