Pin No.	Name	Type	Description
32	GND_SRC	GND	Ground for SRC clocks
33	VDD_SRC	PWR	3.3V Power supply for SRC clocks
34	VDD_SRC	PWR	3.3V Power supply for SRC clocks
35	SRC6# / CPU2#_ITP	O, DIF	Selectable complementary differential CPU or SRC clock output. ITP_EN = 0 @ CK_PWRGD assertion = SRC6 ITP_EN = 1 @ CK_PWRGD assertion = CPU2
36	SRC6 / CPU2_ITP,	O, DIF	Selectable True differential CPU or SRC clock output. ITP_EN = 0 @ CK_PWRGD assertion = SRC6 ITP_EN = 1 @ CK_PWRGD assertion = CPU2
37	CPU1#	O, DIF	Complement differential CPU clock output
38	CPU1	O, DIF	True differential CPU clock output
39	VDD_CPU	PWR	3.3V Power supply for CPU clocks
40	CPU0#	O, DIF	Complement differential CPU clock output
41	CPU0	O, DIF	True differential CPU clock output
42	GND_CPU	GND	Ground for clocks
43	SCLK	I	SMBus compatible SCLOCK
44	SDATA	I/O	SMBus compatible SDATA
45	CPU_STP#*	I, PU	3.3V-tolerant input for stopping CPU outputs *(internal 100K-ohm pull-up)*
46	PCI/SRC_STP#*	I, PU	3.3V-tolerant input for stopping PCI and SRC outputs *(internal 100K-ohm pull-up)*
47	XOUT	O	25.00MHz Crystal output, *Float XOUT if using only CLKIN (Clock input)*
48	XIN / CLKIN	I	25.00MHz Crystal input or 3.3V, 25MHz Clock Input

EProClock® Programmable Technology

EProClock® is the world's first non-volatile programmable clock. The EProClock® technology allows board designer to promptly achieve optimum compliance and clock signal integrity; historically, attainable typically through device and/or board redesigns.

EProClock® technology can be configured through SMBus or hard coded.

Features:

- > 4000 bits of configuration
- Can be configured through SMBus or hard coded
- Custom frequency sets
- Differential slew control on true or compliment or both
- Differential duty cycle control on true or compliment or both
- Differential amplitude control
- Differential and single-ended slew rate control
- Program Internal or External series resistor on single-ended clocks
- Program different spread profiles
- Program different spread modulation rate

Frequency Select Pin (FS)

FSD_SATA	FSC	FSB	FSA	CPU	SRC	SATA75/SRC0	PCI
0	0	0	0	100.00	100.00	100.00	33.33
0	0	0	1	100.00	100.00	100.00	33.33
0	0	1	0	83.33	100.00	100.00	33.33
0	0	1	1	83.33	100.00	100.00	33.33
0	1	0	0	133.33	100.00	100.00	33.33
0	1	0	1	133.33	100.00	100.00	33.33
0	1	1	0	166.67	100.00	100.00	33.33
0	1	1	1	166.67	100.00	100.00	33.33
1	0	0	0	100.00	100.00	75.00	33.33
1	0	0	1	100.00	100.00	75.00	33.33