SDLS047 - DECEMBER 1983 - REVISED MARCH 1988

- Operation from Very Slow Edges
- Improved Line-Receiving Characteristics
- High Noise Immunity

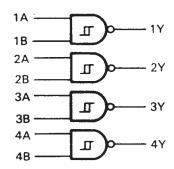
#### description

Each circuit functions as a 2-input NAND gate, but because of the Schmitt action, it has different input threshold levels for positive  $(V_{T+})$  and for negative going  $(V_{T-})$  signals.

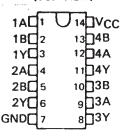
These circuits are temperature-compensated and can be triggered from the slowest of input ramps and still give clear, jitter-free output signals.

The SN54132, SN54LS132, and SN54S132 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74132, SN74LS132, and SN74S132 are characterized for operation from 0°C to 70°C.

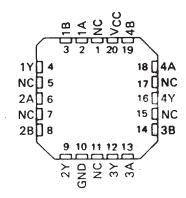
#### logic diagram (positive logic)



#### SN54132, SN54LS132, SN54S132 . . . J OR W PACKAGE SN74132 . . . N PACKAGE SN74LS132, SN74S132 . . . D OR N PACKAGE (TOP VIEW)

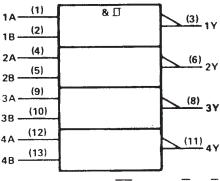


# SN54LS132, SN54S132 . . . FK PACKAGE (TOP VIEW)



NC-No internal connection

#### logic symbol†



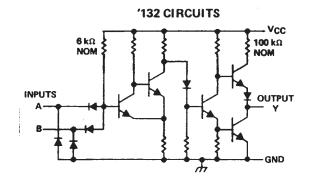
positive logic:  $Y = \overline{AB}$  or  $Y = \overline{A} + \overline{B}$ 

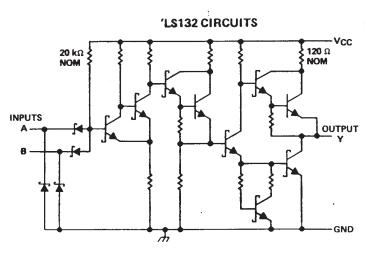
Pin numbers shown are for D, J, N, and W packages.

<sup>&</sup>lt;sup>†</sup>This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

SDLS047 - DECEMBER 1983 - REVISED MARCH 1988

#### schematics





# **'S132 CIRCUITS** -vcc 2.8 kΩ NOM **50** Ω NOM **INPUTS** OUTPUT **GND**

Resistor values shown are nominal.

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note	(1)	7 V
Input voltage: '132, 'S132		5.5 V
Operating free-air temperature	: SN54'	55°C to 125°C
	SN74'	$\dots$ 0°C to 70°C
Storage temperature range		$-65^{\circ}$ C to $150^{\circ}$ C

NOTE 1: Voltages values are with respect to network ground terminal.



#### recommended operating conditions

			SN54132			SN74132			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	٧	
Іон	High-level output current			- 0.8			- 0.8	mA	
IOL	Low-level output current			16			16	mA	
TA	Operating free-air temperature	- 55		125	0		70	°C	

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDIT	rions†	MIN	TYP‡	MAX	UNIT
V <sub>T+</sub>	V <sub>CC</sub> = 5 V			1.5	1.7	2	V
v <sub>T-</sub>	V <sub>CC</sub> = 5 V			0.6	0.9	1.1	V
V <sub>hys</sub> (V <sub>T+</sub> -V <sub>T-</sub> )	V <sub>CC</sub> = 5 V			0.4	0.8		V
ViK	V <sub>CC</sub> = MIN,	I <sub>I</sub> = - 12 mA				- 1.5	V
VOH	V <sub>CC</sub> = MIN,	V <sub>1</sub> = 0.6 V,	t <sub>OH</sub> = - 0.8 mA	2.4	3.4		V
VOL	V <sub>CC</sub> = MIN,	V <sub>1</sub> = 2 V,	IOL = 16 mA		0.2	0.4	V
I <sub>T+</sub>	V <sub>CC</sub> = 5 V,	V1 = VT+			- 0.43		mA
1 <sub>T</sub> _	V <sub>CC</sub> = 5 V,	Λ1 = Λ <sup>L</sup>			- 0.56		mA
l <sub>1</sub>	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 5.5 V				1	mA
ΊΗ	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 2.4 V			-	40	μА
li L	V <sub>CC</sub> = MAX,	V <sub>IL</sub> = 0.4 V			- 0.8	- 1.2	mA
los§	V <sub>CC</sub> = MAX			- 18	•	- 55	mA
ГССН	V <sub>CC</sub> = MAX				15	24	mA
ICCL	V <sub>CC</sub> = MAX				26	40	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

# switching characteristics, $V_{CC}$ = 5 V, $T_A$ = 25°C (see figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN	TYP	MAX	UNIT
<sup>t</sup> PLH	Any		$R_1 = 400 \Omega_s$	C <sub>1</sub> = 15 pF		15	22	ns
<sup>t</sup> PHL	Atty		11 - 400 38,			15	22	ns

<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ . § Not more than one output should be shorted at a time.

# SN54LS132, SN74LS132 QUADRUPLE 2-INPUT POSITIVE-NAND SCHMITT TRIGGERS

SDLS047 - DECEMBER 1983 - REVISED MARCH 1988

#### recommended operating conditions

		S	SN54LS132			SN74LS132			
		MIN	NOM	MAX	MIN	MOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V	
ЮН	High-level output current			- 0.4			-0.4	mA	
IOL	Low-level output current		***	4			8	mA	
TA	Operating free-air temperature	55		125	0		70	°c	

#### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDI	riouet	S	N54LS1	32	SI	UNIT		
PARAMETER		1EST CONDI	TIONS	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
V <sub>T+</sub>	V <sub>CC</sub> = 5 V			1.4	1.6	1.9	1.4	1.6	1.9	V
∨ <sub>T−</sub>	V <sub>CC</sub> = 5 V			0.5	8.0	1	0.5	8.0	1	V
V <sub>hγs</sub> (V <sub>T +</sub> -V <sub>T -</sub> )	V <sub>CC</sub> = 5 V			0.4	0.8		0.4	8.0		V
ViK	V <sub>CC</sub> = MIN,	I <sub>I</sub> = - 18 mA				- 1.5			- 1.5	V
Voн	V <sub>CC</sub> = MIN,	V <sub>1</sub> = 0.5 V,	IOH = - 0.4 mA	2.5	3.4		2.7	3.4		٧
VOL	VCC = MIN,	V <sub>I</sub> = 1.9 V	IOL = 4 mA		0.25	0.4	I	0.25	0.4	V
VOL	A C.C 101114'	V1 - 1.5 V	IOL = 8 mA					0.35	0.5	]
1 <sub>T+</sub>	V <sub>CC</sub> = 5 V,	V <sub>I</sub> = V <sub>T+</sub>		_	- 0.14		-	- 0.14		mA
IT-	V <sub>CC</sub> = 5 V,	V1 = VT_		-	- 0.18		-	- 0.18		mA
l <sub>l</sub>	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 7 V				0.1			0.1	mA
ЧН	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 2.7 V				20			20	μА
11L	V <sub>CC</sub> = MAX,	V <sub>IL</sub> = 0.4 V				- 0.4			- 0.4	mA
os §	V <sub>CC</sub> = MAX	,	· · · · · · · · · · · · · · · · · · ·	- 20		- 100	- 20		- 100	mA
ГССН	V <sub>CC</sub> = MAX				5.9	11		5.9	11	mA
<sup>1</sup> CCL	V <sub>CC</sub> = MAX				8.2	14		8.2	14	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions,

#### switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST COM	MIN	TYP	MAX	UNIT	
<sup>t</sup> PLH	Anv	<b>V</b>	$R_1 = 2 k\Omega$ ,	C <sub>1</sub> = 15 pF		15	22	ns
<sup>t</sup> PHL_	, ,,,,	'	11, 2, 2, 2,	OF = 12 bi-		15	22	ns

<sup>‡</sup> All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25° C.

<sup>§</sup> Not more than one output should be shorted at a time, and duration of the short-circuit should not exceed one second

# SN54S132, SN74S132 QUADRUPLE 2-INPUT POSITIVE-NAND SCHMITT TRIGGERS

SDLS047 - DECEMBER 1983 - REVISED MARCH 1988

#### recommended operating conditions

			SN54S132		SN74S132			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNII
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
Іон	High-level output current			<b>– 1</b>			<b>– 1</b>	mA
IOL	Low-level output current			20			20	mA
TA	Operating free-air temperature	- 55		125	0		70	°C

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDIT	uovet		SN54S1	32	:	SN74S1	32	UNIT
PARAMETER		TEST CONDIT	ION2.	MIN	TYP‡	MAX	MIN	TYP‡	MAX	ONT
V <sub>T+</sub>	V <sub>CC</sub> = 5 V			1.6	1.77	1.9	1.6	1.77	1.9	٧
V <sub>T</sub> _	V <sub>CC</sub> = 5 V			1.1	1.22	1.4	1.1	1.22	1.4	٧
V <sub>hys</sub> (V <sub>T +</sub> -V <sub>T -</sub> )	V <sub>CC</sub> = 5 V			0.2	0.55		0.2	0.55		٧
VIK	V <sub>CC</sub> = MIN,	I <sub>1</sub> = - 18 mA				- 1.2			- 1.2	V
Voн	V <sub>CC</sub> = MIN,	V <sub>1</sub> = 1.1 V,	IOH = - 1 mA	2.5	3.4		2.7	3.4		٧
VOL	V <sub>CC</sub> = MIN,	V1 = 1.9 V,	IOL = 20 mA			0.5			0.5	V
I <sub>T+</sub>	V <sub>CC</sub> = 5 V,	V1 = VT+			- 0.9			- 0.9		mA
1T_	V <sub>CC</sub> = 5 V,	VI = VT_			- 1.1			<b>- 1.1</b>		mA
lį	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 5.5 V				1			1	mA
ЧН	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 2.7 V				50			50	μA
115	V <sub>CC</sub> = MAX,	V <sub>1L</sub> = 0.5 V				- 2			- 2	mΑ
los§	V <sub>CC</sub> = MAX			<b>- 40</b>		- 100	- 40		<b>– 100</b>	mΑ
ССН	V <sub>CC</sub> = MAX				28	44		28	44	mA
ICCL	V <sub>CC</sub> = MAX				44	68		44	68	mA

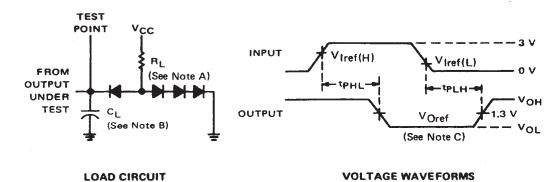
 $<sup>^\</sup>dagger$  For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

## switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN	TYP	MAX	UNIT
t <sub>PLH</sub>	A or B	V	$R_1 = 280 \Omega_s$	C <sub>1</sub> = 15 pF		7	10.5	ns
tPHL	70,0	'	11 - 200 14,	O[ - 13 br		8.5	13	nis

<sup>‡</sup> All typical values are at  $V_{CC}$  = 5 V,  $T_A$  = 25°C. § Not more than one output should be shorted at a time, and duration of the short-circuit should not exceed one second.

#### PARAMETER MEASUREMENT INFORMATION



NOTES: A. All diodes are 1N3064 or equivalent.

B. C<sub>L</sub> includes probe and jig capacitance.

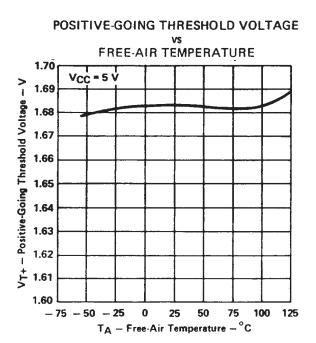
C. Generator characteristics and reference voltages are:

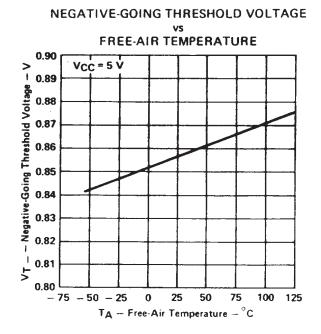
	G	enerator C	haracteris	tics	Reference Voltages					
	Zout	PRR	tr	tf	VI ref(H)	VI ref(L)	VO ref			
SN54'/SN74'	50	1 MHz	10 ns	10 ns	1.7 V	0.9 V	1.5 V			
SN54LS'/SN74LS'	50	1 MHz	15 ns	6 ns	1.6 V	0.8 V	1.3 V			
'S132	50	1 MHz	2.5 ns	2.5 ns	1.8 V	1.2 V	1.5 V			

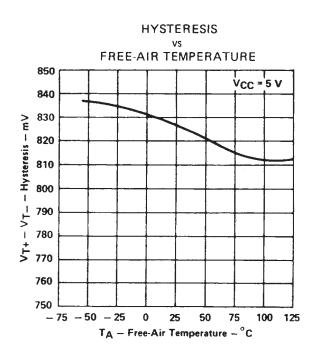
FIGURE 1

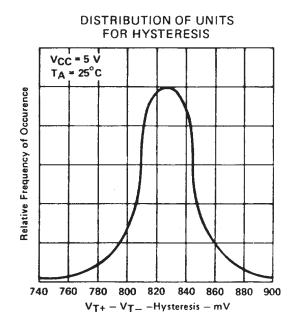


#### **TYPICAL CHARACTERISTICS OF '132 CIRCUITS**

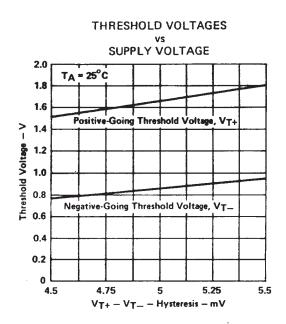


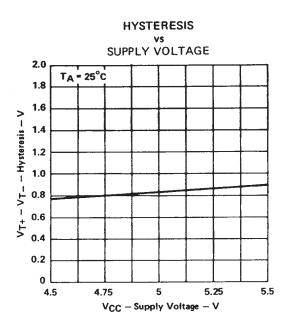


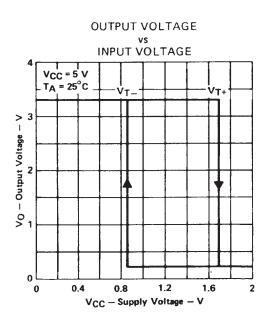




#### TYPICAL CHARACTERISTICS OF '132 CIRCUITS





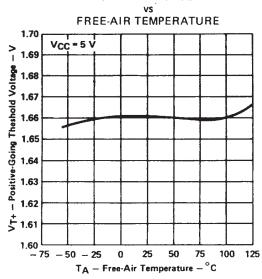


<sup>&</sup>lt;sup>†</sup> Data for temperatures below 0° C and 70° C and supply below 4.75 V and above 5.25 V are applicable for SN54132 only.

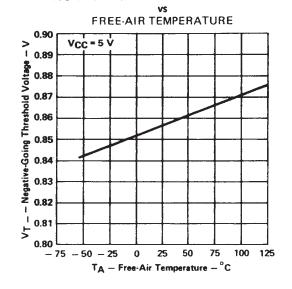


#### TYPICAL CHARACTERISTICS OF 'LS132 CIRCUITS

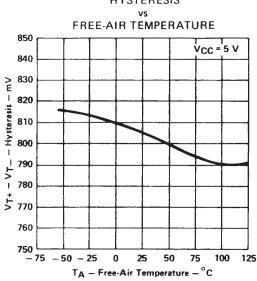
#### POSITIVE-GOING THRESHOLD VOLTAGE



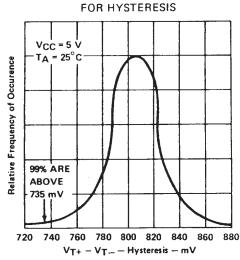
#### **NEGATIVE-GOING THRESHOLD VOLTAGE**



#### **HYSTERESIS**

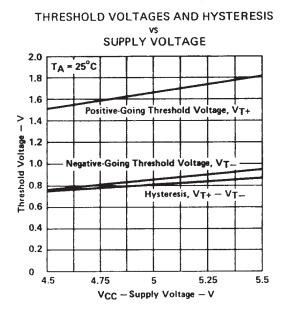


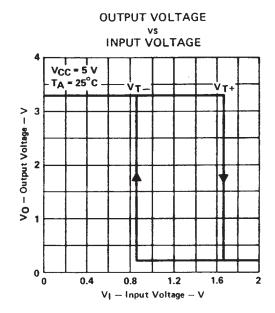
# DISTRIBUTION OF UNITS



Data for temperatures below 0°C and above 70°C and supply voltages below 4.75 V and above 5.25 V are applicable for SN54LS132 only.

#### TYPICAL CHARACTERISTICS OF 'LS132 CIRCUITS

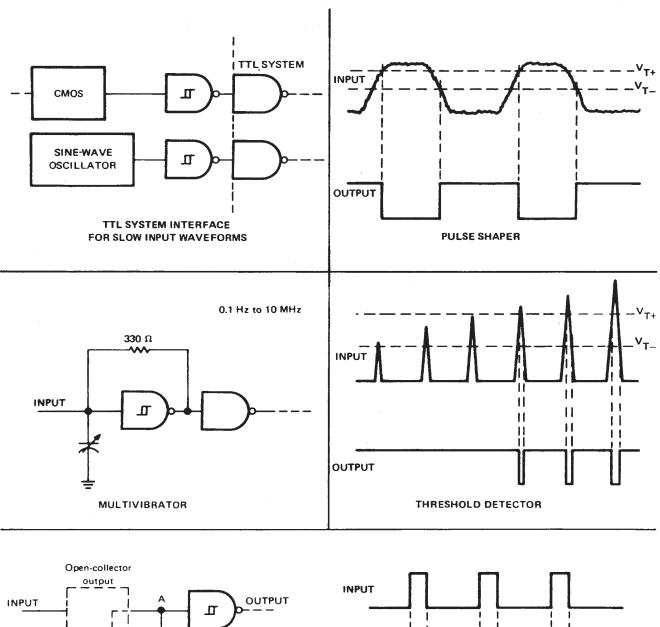


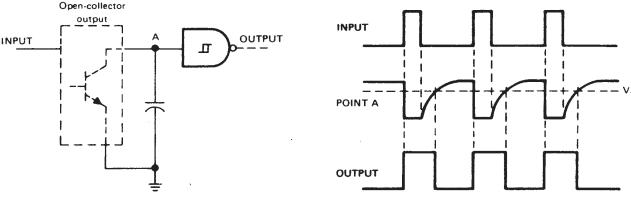


<sup>†</sup> Data for temperatures below 0°C and above 70°C and supply voltages below 4.75 V and above 5.25 V are applicable for SN54LS132 only.



#### TYPICAL APPLICATION DATA











7-Jun-2010

#### **PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
7600401CA	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7600401DA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7600401DA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/31303BCA	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/31303BCA	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SN54132J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI	Replaced by SN54LS132J
SN54132J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI	Replaced by SN54LS132J
SN54LS132J	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SN54LS132J	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SN54S132J	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SN54S132J	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SN74132N	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Samples Not Available
SN74132N	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Samples Not Available
SN74132N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Samples Not Available
SN74132N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Samples Not Available
SN74LS132D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples



www.ti.com 7-Jun-2010

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
SN74LS132DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132DRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI	Samples Not Availabl
SN74LS132J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI	Samples Not Availab
SN74LS132N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributo or Sales Office
SN74LS132N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributo or Sales Office
SN74LS132N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Samples Not Availab
SN74LS132N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Samples Not Availab
SN74LS132NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributo or Sales Office
SN74LS132NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributo or Sales Office
SN74LS132NSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132NSR	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132NSRE4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132NSRE4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132NSRG4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS132NSRG4	ACTIVE	SO	NS	14	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74S132N	NRND	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Samples Not Availab
SN74S132N	NRND	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Samples Not Availab
SN74S132N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Samples Not Availab





www.ti.com 7-Jun-2010

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
SN74S132N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI	Samples Not Available
SN74S132NE4	NRND	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Samples Not Available
SN74S132NE4	NRND	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Samples Not Available
SNJ54132J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI	Replaced by SNJ54LS132J
SNJ54132J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI	Replaced by SNJ54LS132J
SNJ54LS132FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54LS132FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54LS132J	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS132J	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS132W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS132W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S132FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54S132FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54S132J	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S132J	ACTIVE	CDIP	J	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S132W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S132W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type	Purchase Samples

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.



#### PACKAGE OPTION ADDENDUM

7-Jun-2010

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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#### OTHER QUALIFIED VERSIONS OF SN54132, SN54LS132, SN54S132, SN74132, SN74LS132, SN74S132:

Catalog: SN74132, SN74LS132, SN74S132

• Military: SN54132, SN54LS132, SN54S132

NOTE: Qualified Version Definitions:

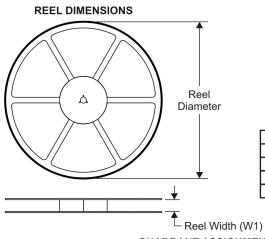
Catalog - TI's standard catalog product

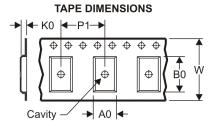
Military - QML certified for Military and Defense Applications

PACKAGE MATERIALS INFORMATION

www.ti.com 29-Jul-2009

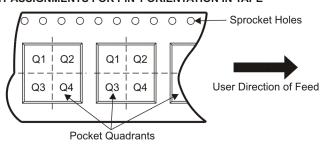
#### TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

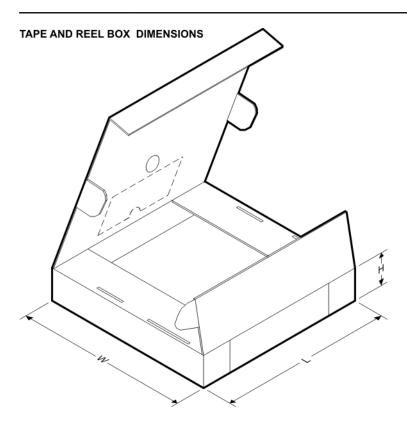


#### \*All dimensions are nominal

	Device	Package Type	Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
ı	SN74LS132DR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1
ı	SN74LS132NSR	SO	NS	14	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1

**PACKAGE MATERIALS INFORMATION** 

www.ti.com 29-Jul-2009



#### \*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LS132DR	SOIC	D	14	2500	346.0	346.0	33.0
SN74LS132NSR	SO	NS	14	2000	346.0	346.0	33.0

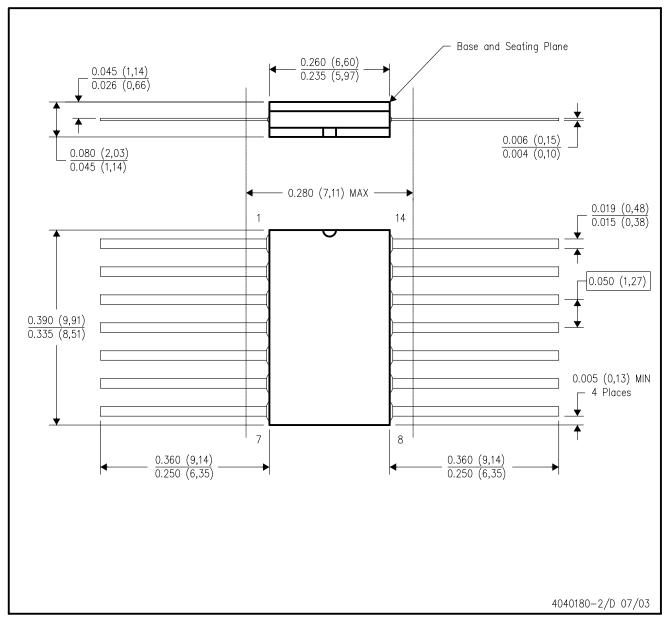
## 14 LEADS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

# W (R-GDFP-F14)

# CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F14 and JEDEC MO-092AB



#### FK (S-CQCC-N\*\*)

#### **28 TERMINAL SHOWN**

#### **LEADLESS CERAMIC CHIP CARRIER**



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. The terminals are gold plated.
- E. Falls within JEDEC MS-004



# N (R-PDIP-T\*\*)

## PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN

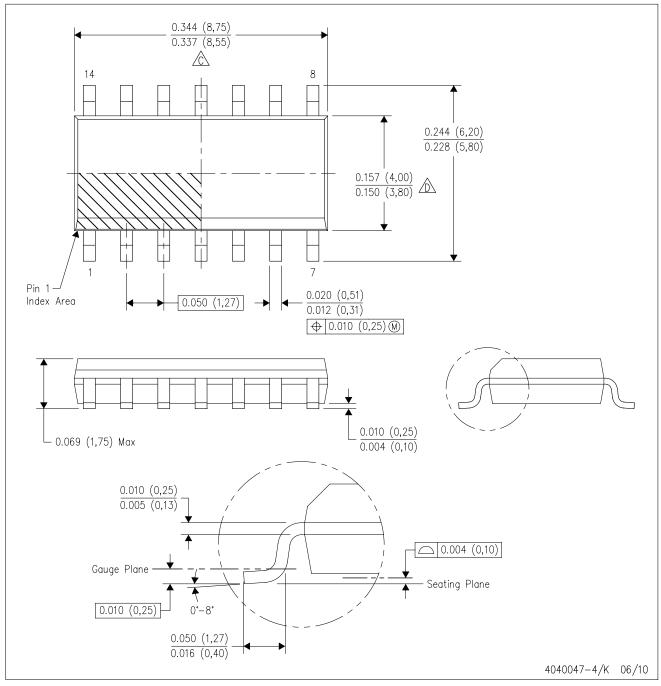


- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



# D (R-PDSO-G14)

#### PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AB.



#### **MECHANICAL DATA**

# NS (R-PDSO-G\*\*)

# 14-PINS SHOWN

#### PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



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