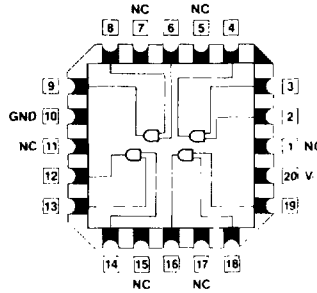


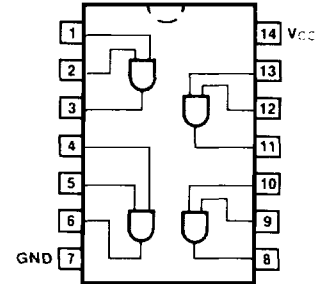
54F/74F08

Quad 2-Input AND Gate

Connection Diagrams



**Pin Assignment
for LCC and PCC**



**Pin Assignment
for DIP and SOIC**

Ordering Code: See Section 5

Input Loading/Fan-Out: See Section 3 for U.L. definitions

Pin Names	Description	54F/74F(U.L.) HIGH/LOW
	Inputs	0.5/0.375
	Outputs	25/12.5

DC Characteristics over Operating Temperature Range (unless otherwise specified)

Symbol	Parameter	54F/74F			Units	Conditions	
		Min	Typ	Max		V _{IN} = Open	V _{CC} = Max
I _{CCH}	Power Supply Current		5.5	8.3	mA	V _{IN} = Open	V _{CC} = Max
I _{CCL}			8.6	12.9		V _{IN} = Gnd	

AC Characteristics: See Section 3 for waveforms and load configurations

Symbol	Parameter	54F/74F			54F		74F		Units	Fig. No.
		T _A = +25°C V _{CC} = +5.0 V C _L = 50 pF			T _A , V _{CC} = Mil C _L = 50 pF		T _A , V _{CC} = Com C _L = 50 pF			
		Min	Typ	Max	Min	Max	Min	Max		
t _{PLH}	Propagation Delay	3.0	4.2	5.6	2.5	7.5	3.0	6.6	ns	3-1 3-4
t _{PHL}		2.5	4.0	5.3	2.0	7.5	2.5	6.3		