

54F/74F978 Octal Flip-Flop with Serial Scanner

General Description

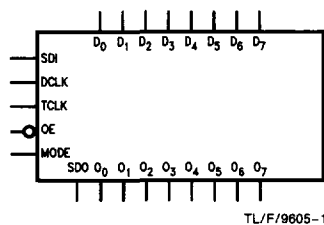
The 'F978 is a high speed low power octal flip-flop with a buffered Data Clock (DCLK), Test Clock (TCLK), and a buffered Output Enable (OEN). Serial diagnostics are performed with on-board multiplexers.

- Inputs and outputs on opposite sides of package allowing easy interface with microprocessors
- RAM write-back for writable control store
- Useful as input or output port for microprocessors
- Cascadable for wide control words as used in microprogramming

Features

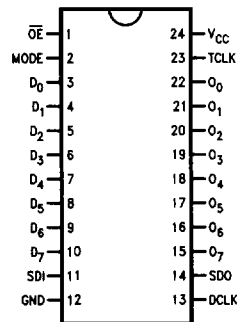
- Edge-triggered D-type registers
- On-line and off-line system diagnostics with independent test clock

Logic Symbol



Connection Diagrams

Pin Assignment
for 24-Pin DIP/SOIC



Pin Assignment
for 28-Pin LCC/PLCC

