

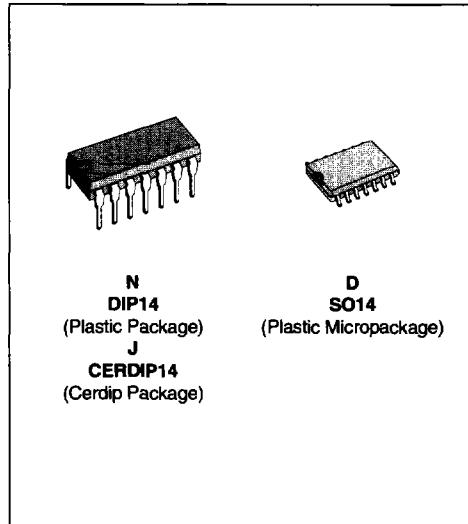
LOW POWER QUAD VOLTAGE COMPARATORS

- WIDE SINGLE SUPPLY VOLTAGE RANGE OR DUAL SUPPLIES FOR ALL DEVICES : +2V TO +36V OR $\pm 1V$ TO $\pm 18V$
- VERY LOW SUPPLY CURRENT (1.1mA) INDEPENDENT OF SUPPLY VOLTAGE (1.4mW/comparator at +5V)
- LOW INPUT BIAS CURRENT : 25nA TYP
- LOW INPUT OFFSET CURRENT : $\pm 5nA$ TYP
- LOW INPUT OFFSET VOLTAGE : $\pm 1mV$ TYP
- INPUT COMMON-MODE VOLTAGE RANGE INCLUDES GROUND
- LOW OUTPUT SATURATION VOLTAGE : 250mV TYP. ($I_o = 4mA$)
- DIFFERENTIAL INPUT VOLTAGE RANGE EQUAL TO THE SUPPLY VOLTAGE
- TTL, DTL, ECL, MOS, CMOS COMPATIBLE OUTPUTS

DESCRIPTION

These devices consist of four independent precision voltage comparators with an offset voltage specifications as low as 2mV max for LM339A, LM239A and LM139A. All these comparators were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible.

These comparators also have a unique characteristic in that the input common-mode voltage range includes ground even though operated from a single power supply voltage.



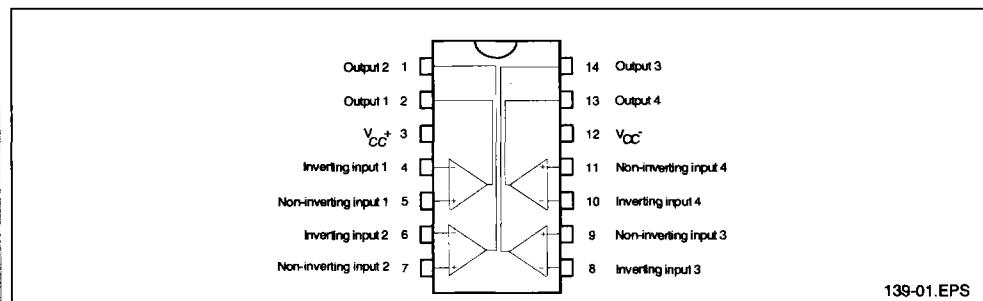
ORDER CODES

Part Number	Temperature Range	Package		
		N	J	D
LM139/A	-55, +125°C	•	•	•
LM239/A	-40, +105°C	•	•	•
LM339/A	0, +70°C	•	•	•
LM2901	-40, +105°C	•	•	•

Examples : LM139AN, LM2901D

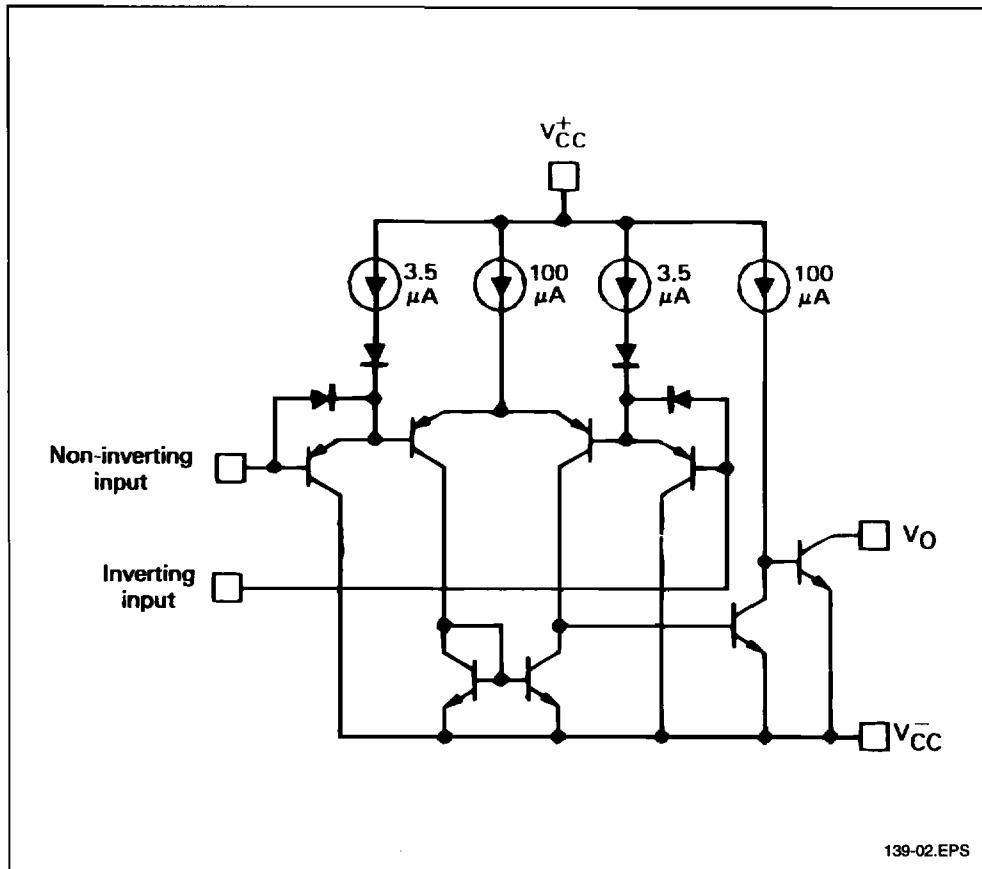
139-01.TBL

PIN CONNECTIONS (top view)



139-01.EPS

SCHEMATIC DIAGRAM (1/4 LM139)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	LM139,A LM239,A	LM339,A	LM2901	Unit
V_{CC}	Supply Voltage	± 18 to 36	± 18 to 36	± 18 to 36	V
V_{id}	Differential Input Voltage	± 36	± 36	± 36	V
V_i	Input Voltage	-0.3 to +36	-0.3 to +36	-0.3 to +36	V
	Output Short-circuit to Ground - (note 1)		Infinite		
P_{tot}	Power Dissipation	570	570	570	mW
T_{oper}	Operating Free-air Temperature Range LM239,A	-55, +125 -40, +105	0, +70	-40, +105	°C
T_{stg}	Storage Temperature Range	-65, +150	-65, +150	-65, +150	°C

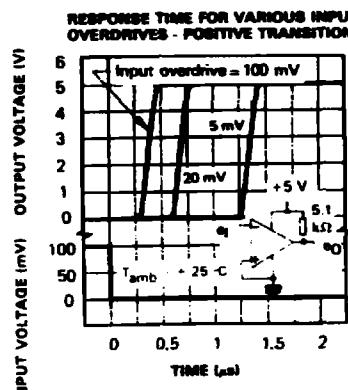
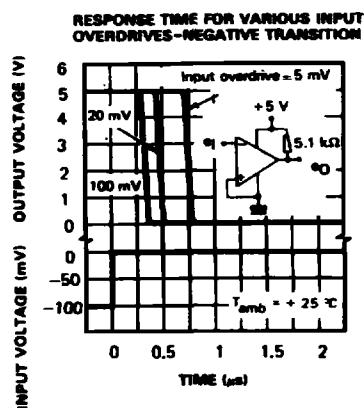
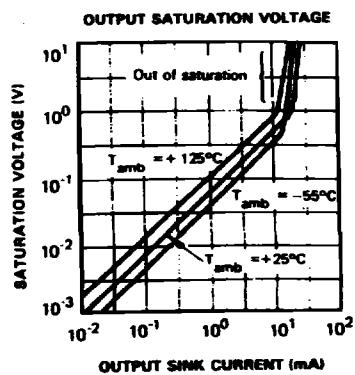
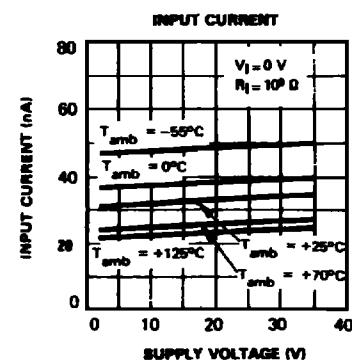
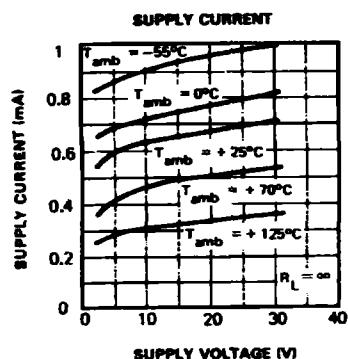
Notes : 1. Short-circuit from the output to V_{CC}^+ can cause excessive heating and eventual destruction. The maximum output current is approximately 20mA, independent of the magnitude of V_{CC}^+ .

ELECTRICAL CHARACTERISTICSV_{CC}⁺ = +5V, V_{CC}⁻ = GND, T_{amb} = 25°C (unless otherwise specified)

Symbol	Parameter	LM139A - LM239A LM339A			LM139 - LM239 LM339 - LM2901			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
V _{IO}	Input Offset Voltage – (note 2) T _{amb} = +25°C LM2901 T _{min.} ≤ T _{amb} ≤ T _{max.} LM2901		1	2		1	5 7 9 15	mV
				4				
I _{IO}	Input Offset Current T _{amb} = +25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		3	25 100		5	50 150	nA
I _B	Input Bias Current (I _{i⁺} or I _{i⁻}) - (note 3) T _{amb} = +25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		25	100 300		25	250 400	nA
A _{VD}	Large Signal Voltage Gain (V _{CC} = 15V, R _L = 15kΩ, V _O = 1 to 11V) LM2901	50	200		50 25	200		V/mV
I _{CC}	Supply Current (all comparators) V _{CC} = +5V, no load V _{CC} = +30V, no load		1.1 1.3	2 2.5		1.1 1.3	2 2.5	mA
V _{ICM}	Input Common Mode Voltage Range - (note 4) (V _{CC} = 30V) T _{amb} = +25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	0 0		V _{CC} ⁺ -1.5 V _{CC} ⁺ -2	0 0		V _{CC} ⁺ -1.5 V _{CC} ⁺ -2	V
V _{ID}	Differential Input Voltage - (note 6)			V _{CC} ⁺			V _{CC} ⁺	V
V _{OL}	Low Level Output Voltage (V _{ID} = -1V, I _{SINK} = 4mA) T _{amb} = +25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		250	400 700		250	400 700	mV
I _{OH}	High Level Output Current (V _{ID} = 1V) (V _{CC} = V _O = 30V) T _{amb} = +25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		0.1	1		0.1	1	nA μA
I _{SINK}	Output Sink Current (V _{ID} = -1V, V _O = 1.5V)	6	16		6	16		mA
t _{RE}	Response Time - (note 5) (R _L = 5.1kΩ connected to V _{CC} ⁺ , V _(ref) = +1.4V)		1.3			1.3		μs
t _{REL}	Large Signal Response Time (R _L = 5.1kΩ connected to V _{CC} ⁺ , e _t = TTL, V _(ref) = +1.4V)		300			300		ns

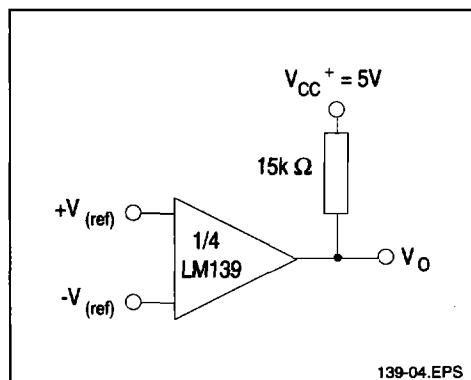
- Notes :**
- At output switch point, V_O ≈ 1.4V, R_S = 0 with V_{CC}⁺ from 5V to 30V, and over the full input common-mode range (0V to V_{CC}⁻ - 1.5V).
 - The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output, so no loading charge exists on the reference of input lines.
 - The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V. The upper end of the common-mode voltage range is V_{CC}⁺ - 1.5V, but either or both inputs can go to +30V without damage.
 - The response time specified is for a 100mV input step with 5mV overdrive. For larger overdrive signals 300ns can be obtained.
 - Positive excursions of input voltage may exceed the power supply level. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than -0.3V (or 0.3V below the negative power supply, if used).

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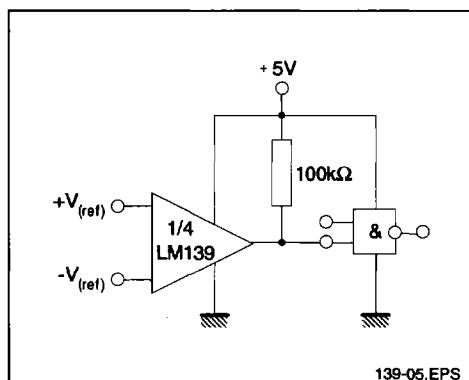


TYPICAL APPLICATIONS

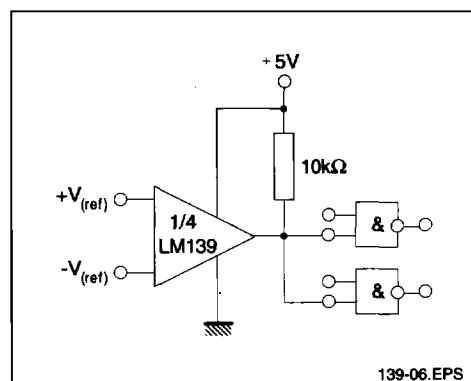
BASIC COMPARATOR



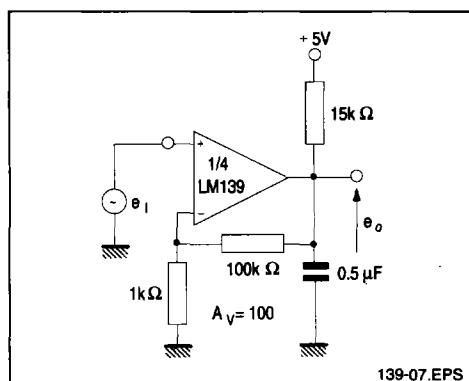
DRIVING CMOS



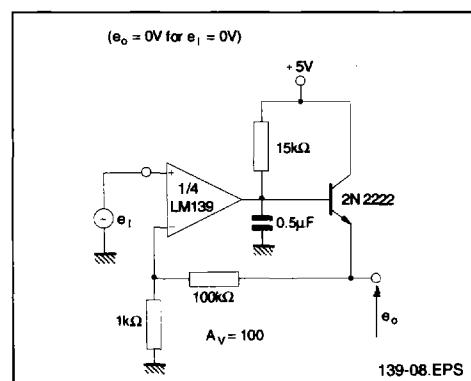
DRIVING TTL



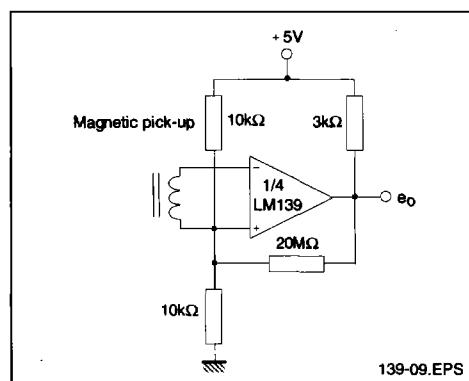
LOW FREQUENCY OP AMP



LOW FREQUENCY OP AMP

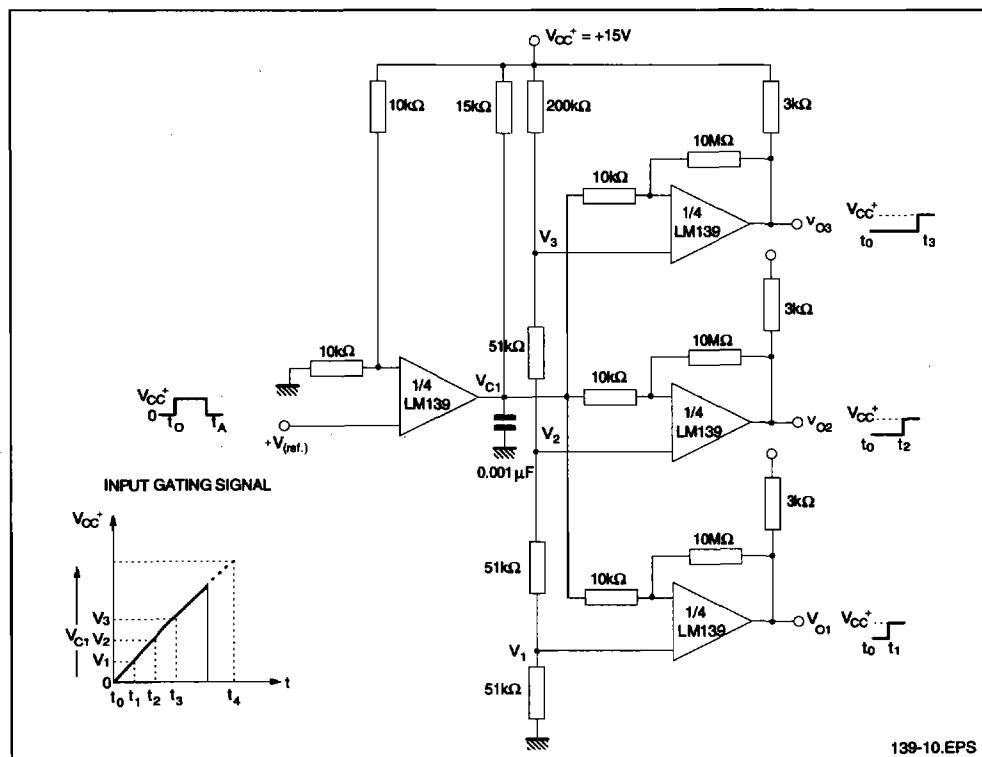


TRANSDUCER AMPLIFIER

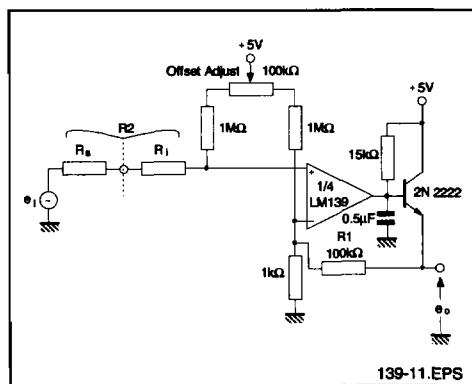


TYPICAL APPLICATIONS (continued)

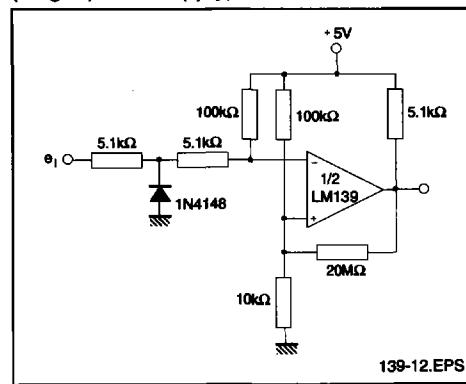
TIME DELAY GENERATOR

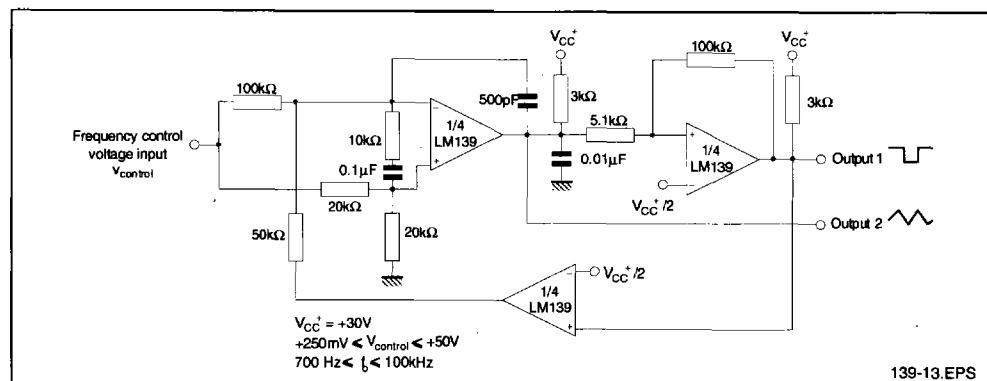
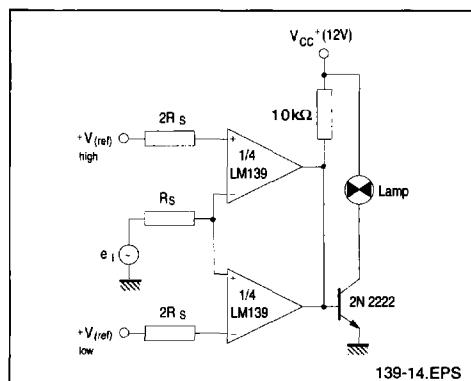
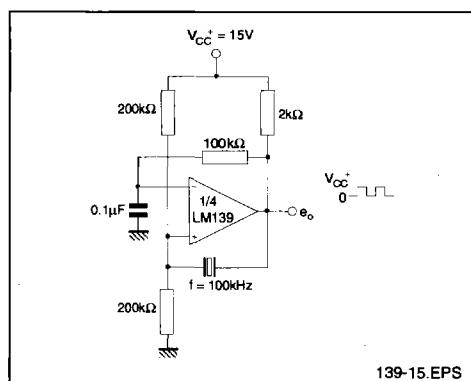
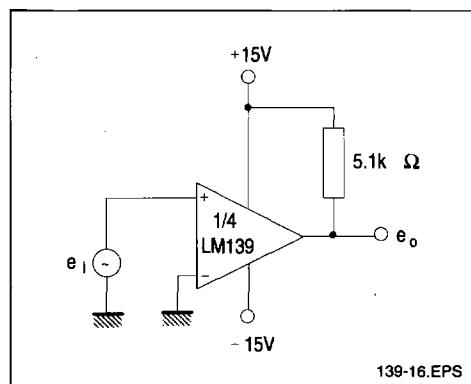


LOW FREQUENCY OP AMP WITH OFFSET ADJUST



ZERO CROSSING DETECTOR (single power supply)



TYPICAL APPLICATIONS (continued)**TWO-DECADE HIGH-FREQUENCY VCO****LIMIT COMPARATOR****CRYSTAL CONTROLLED OSCILLATOR****SPLIT-SUPPLY APPLICATIONS****ZERO CROSSING DETECTOR****COMPARATOR WITH A NEGATIVE REFERENCE**