

MBRD620CT – MBRD6100CT

6.0A SURFACE MOUNT DUAL SCHOTTKY BARRIER RECTIFIER

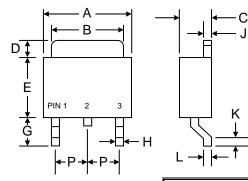


Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Surge Current Capability
- Low Power Loss, High Efficiency
- Ideally Suited for Automatic Assembly
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications

Mechanical Data

- Case: DPAK/TO-252, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.3 grams (approx.)
- Mounting Position: Any
- Marking: Device Code, See Page 3
- Lead Free: For RoHS / Lead Free Version, Add "-LF" Suffix to Part Number, See Page 4



PIN 10

Dim Min Max A 6.05 6.70 B 5.05 5.55 C 2.10 2.50 D 1.05 1.25 E 5.48 6.20 G 2.55 3.40	DPAK/TO-252						
B 5.05 5.55 C 2.10 2.50 D 1.05 1.25 E 5.48 6.20	(im	Di				
C 2.10 2.50 D 1.05 1.25 E 5.48 6.20)	A	A				
D 1.05 1.25 E 5.48 6.20	;	8	В				
E 5.48 6.20)	0	C				
	;	D	D				
G 2.55 3.40)	Ε	E				
G 2.00 3.40)	G	G				
H 0.55 0.90)	Н	н				
J 0.40 0.60)	J	J				
K 0.95 1.60)	K	ĸ				
L 0.45 0.55	;		L				
P 2.30 Typical	2.30 Typical						
All Dimensions in mm							

Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

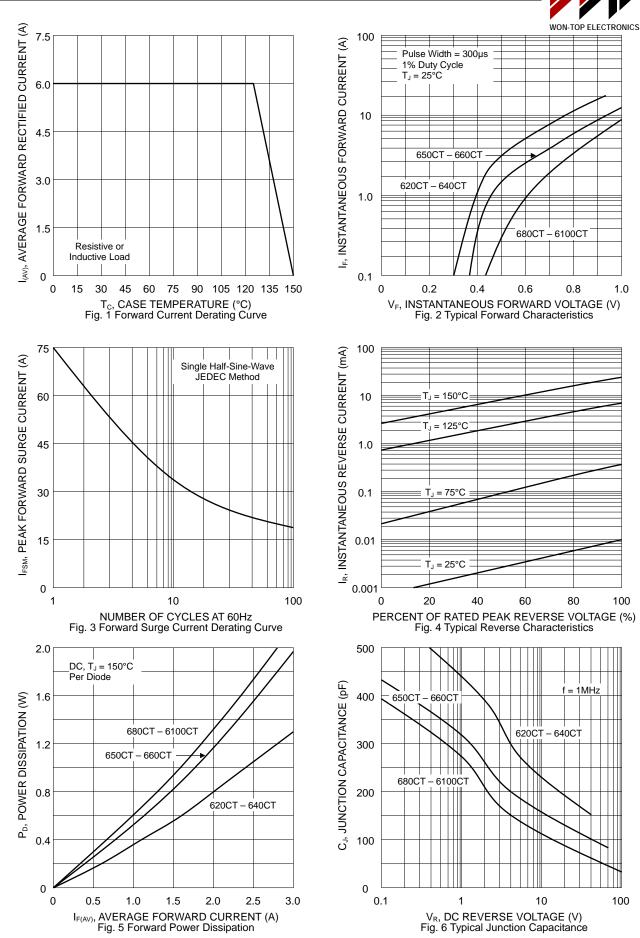
Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	MBRD 620CT	MBRD 630CT	MBRD 640CT	MBRD 650CT	MBRD 660CT	MBRD 680CT	MBRD 6100CT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	20	30	40	50	60	80	100	V
RMS Reverse Voltage	VR(RMS)	14	21	28	35	42	56	70	V
Average Rectified Output CurrentTotal Device $@T_c = 125^{\circ}C$ Per Diode	lo	6.0 3.0					А		
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM				75				A
Forward Voltage per diode $@I_F = 3.0A$	Vfm	0.55 0.75 0.85			85	V			
Peak Reverse Current $@T_J = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_J = 100^{\circ}C$	Irm	0.2 15			mA				
Typical Junction Capacitance (Note 1)	Сл		300		20	00	1:	50	pF
Thermal Resistance, Junction to Ambient (Note 2) Thermal Resistance, Junction to Case (Note 2)	R JA R JC				80 6.0		·		°C/W
Operating and Storage Temperature Range	TJ, TSTG			-	55 to +15	0			°C

Note: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.

2. Mounted on FR-4 PC board with minimum recommended pad layout per diode.

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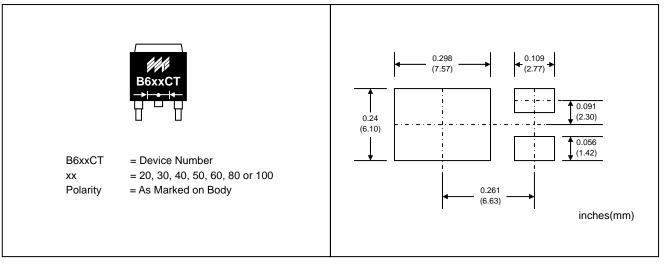


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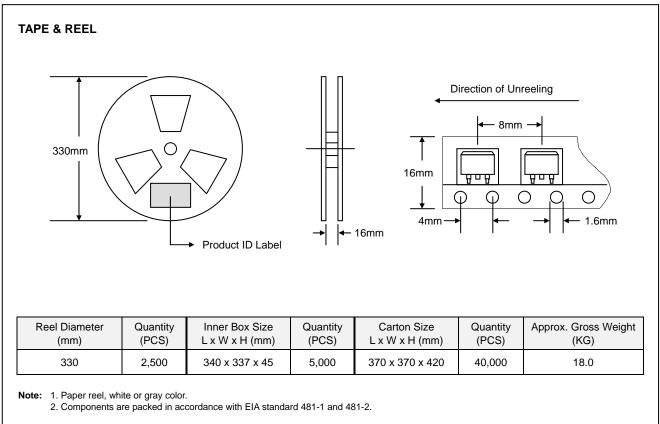


MARKING INFORMATION

RECOMMENDED FOOTPRINT



PACKAGING INFORMATION





Product No.	Package Type	Shipping Quantity				
MBRD620CT-T3	DPAK	2500/Tape & Reel				
MBRD630CT-T3	DPAK	2500/Tape & Reel				
MBRD640CT-T3	DPAK	2500/Tape & Reel				
MBRD650CT-T3	DPAK	2500/Tape & Reel				
MBRD660CT-T3	DPAK	2500/Tape & Reel				
MBRD680CT-T3	DPAK	2500/Tape & Reel				
MBRD6100CT-T3	DPAK	2500/Tape & Reel				

ORDERING INFORMATION

1. Shipping quantity given is for minimum packing quantity only. For minimum order quantity, please consult the Sales Department.

 To order RoHS / Lead Free version (with Lead Free finish), add "-LF" suffix to part number above. For example, MBRD620CT-T3-LF.

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