



National
Semiconductor™

Consumer Series

Discrete POWER & Signal
Technologies

Device No.	Case Style	V _{CE0} (V) Min	V _{CEO} (V) Min	V _{EBO} (V) Min	I _{CEO} (nA) Max	V _{CB} (V) Max	h _{FE} Min	I _C (mA) Max	V _{CE(SAT)} (V) Max	V _{BE(SAT)} (V) Min	I _C (mA) Max	C _{ob} (pF) Max	f _T (MHz) Min	I _C (mA) Max	NF (dB) Max	Test Conditions	Process No.
CS8050C	TO-92 (92)	40	25	6	100	35	45	5	0.5	1.2	800	9	100	50			37
CS8050D	TO-92 (92)	40	25	6	100	35	85	300	0.5	1.2	800	9	100	50			37
CS8550C	TO-92 (92)	40	25	6	100	35	45	5	0.5	1.2	800	15	100	50			77
CS8550D	TO-92 (92)	40	25	6	100	35	85	300	0.5	1.2	800	15	100	50			77
CS9011	TO-92 (92)	40	30	5	100	30	132	198	0.3	0.75	10	3.5	150	1	4	(Note 2)	23
CS9012D	TO-92 (92)	40	25	5	100	25	64	91	0.6	1.2	300						68
CS9012F	TO-92 (92)	40	25	5	100	25	96	135	0.6	1.2	300						68
CS9012G	TO-92 (92)	40	25	5	100	25	118	166	0.6	1.2	300						68
CS9013D	TO-92 (92)	40	25	5	100	25	64	91	0.6	1.2	300						10
CS9013E	TO-92 (92)	40	25	5	100	25	78	112	0.6	1.2	300						10
CS9013G	TO-92 (92)	40	25	5	100	25	118	166	0.6	1.2	300						10
CS9013H	TO-92 (92)	40	25	5	100	25	144	202	0.6	1.2	300						10
CS9013J	TO-92 (92)	40	25	5	100	25		50	0.6	1.2	300						10