# SN54ALS29823, SN74ALS29823 9-BIT BUS-INTERFACE FLIP-FLOPS WITH 3-STATE OUTPUTS

SDAS146B - JANUARY 1986 - REVISED JANUARY 1995

- Functionally Equivalent to AMD's AM29823
- Provide Extra Data Width Necessary for Wider Address/Data Paths or Buses With Parity
- Outputs Have Undershoot-Protection Circuitry
- Power-Up High-Impedance State
- Buffered Control Inputs Reduce dc Loading Effects
- Package Options Include Plastic Small-Outline (DW) Packages and Standard Plastic (NT) and Ceramic (JT) 300-mil DIPs

#### SN54ALS29823...JT PACKAGE SN74ALS29823...DW OR NT PACKAGE (TOP VIEW)

ŌĒ <b>[</b> 1	U 24	v <sub>cc</sub>
1D 🛮 2	23	] 1Q
2D 🛮 3	22	2Q
3D 🛮 4	21	] 3Q
4D 🛮 5	20	] 4Q
5D <b>[]</b> 6	19	] 5Q
6D <b>[]</b> 7	18	] 6Q
7D 🛛 8	17	]7Q
8D <b>[]</b> 9	16	] 8Q
9D <b>[]</b> 10	15	] 9Q
CLR [] 11	14	CLKEN
GND 12	13	CLK

### description

These 9-bit flip-flops feature 3-state outputs designed specifically for driving highly capacitive

or relatively low-impedance loads. They are particularly suitable for implementing wider buffer registers, I/O ports, bidirectional bus drivers, parity bus interfacing, and working registers.

With the clock-enable (CLKEN) input low, the nine D-type edge-triggered flip-flops enter data on the low-to-high transitions of the clock (CLK) input. Taking CLKEN high disables the clock buffer, latching the outputs. The 'ALS29823 have noninverting data (D) inputs. Taking the clear (CLR) input low causes the nine Q outputs to go low independently of the clock.

A buffered output-enable ( $\overline{OE}$ ) input places the nine outputs in either a normal logic state (high or low logic levels) or a high-impedance state. The outputs also are in the high-impedance state during power-up and power-down conditions. The outputs remain in the high-impedance state while the device is powered down. In the high-impedance state, the outputs neither load nor drive the bus lines significantly. The high-impedance state and increased drive provide the capability to drive bus lines without interface or pullup components.

OE does not affect the internal operation of the flip-flops. Old data can be retained or new data can be entered while the outputs are in the high-impedance state.

The SN54ALS29823 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS29823 is characterized for operation from 0°C to 70°C.

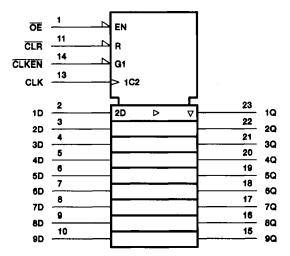
# FUNCTION TABLE (each flip-flop)

				/	
			OUTPUT		
ŌE	CLR	CLKEN	CLK	D	a
L	L	Х	Х	Х	L
L	н	L	1	н	н
L	Н	L	1	L	L
L	Н	Н	X	X	Q <sub>0</sub>
Н	X	X	×	X	z

TEXAS INSTRUMENTS

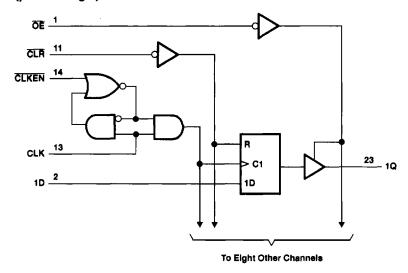
SDAS146B - JANUARY 1986 - REVISED JANUARY 1995

### logic symbol†



<sup>&</sup>lt;sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

### logic diagram (positive logic)



## SN54ALS29823, SN74ALS29823 9-BIT BUS-INTERFACE FLIP-FLOPS WITH 3-STATE OUTPUTS

SDAS146B - JANUARY 1986 - REVISED JANUARY 1995

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V <sub>CC</sub>	7 V
Input voltage, V <sub>1</sub>	
Voltage applied to a disabled high-impedance output	
Operating free-air temperature range, TA: SN54ALS29823	-55°C to 125°C
Storage temperature range	-65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### recommended operating conditions

				SN54ALS29823		UNIT	
				MIN	NOM	MAX	UNII
Vcc	Supply voltage			4.5	5	5.5	٧
VIH	High-level input voltage			2			٧
VIL	Low-level input voltage					0.8	٧
ГОН	High-level output current					-18	mA
loL	Low-level output current					32	mA
Γ.	Putes duration		ČLR low	7			ns
tw	, Pulse duration	Ī	CLK high or low	8			118
			CLR inactive	7			
tsu	Setup time before CLK↑		Data	4			ns
			CLKEN high or low	8			
٠.	Haldkins after OLVT		CLKEN	2			ns
th	Hold time after CLKT		Data	4			ns
TA	Operating free-air temperature			-55	25	125	°C

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED		TEGT COMPITIONS		4AL\$29	823	LINUT
PARAMETER	TEST CO	TEST CONDITIONS			MAX	UNIT
VIK	V <sub>CC</sub> = 4.5 V,	lj = -18 mA			-1.2	٧
Vou	V 45V	IOH = -12 mA	2.4	3.3		V
VOH	V <sub>CC</sub> = 4.5 V	IOH = -18 mA	2			<b>┐ '</b>
VOL	V <sub>CC</sub> = 4.5 V,	IOL = 32 mA		0.25	0.5	٧
lozh	V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 2.4 V			50	μА
IOZL	V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 0.4 V			-50	μA
lj.	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> ≈ 5.5 V			0.1	mA
IIH	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> ≈ 2.7 V			20	μА
ايل	V <sub>CC</sub> = 5.5 V,	V <sub>I</sub> = 0.4 V			-0.5	mA
los§	V <sub>CC</sub> = 5.5 V, '	V <sub>O</sub> = 0	-75		-250	mA
		Outputs high			90	
Icc	V <sub>CC</sub> = 5.5 V	Outputs low			105	mA
		Outputs open			115	

<sup>‡</sup> All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C.

<sup>\$</sup> Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.



# SN54ALS29823, SN74ALS29823 9-BIT BUS-INTERFACE FLIP-FLOPS WITH 3-STATE OUTPUTS

SDAS146B - JANUARY 1986 - REVISED JANUARY 1995

### switching characteristics (see Figure 1)

	FROM	то		VCC = MIN to MAXT, TA = MIN to MAXT				
PARAMETER	(INPUT)	(OUTPUT)	TEST CONDITIONS	SN64AL	.829823	UNIT		
				MIN	MAX			
tPLH .	CLK	Am. O	0. 50.5	2	11.5	ns		
tPHL	OLK	Any Q	C <sub>L</sub> = 50 pF	2	11.5	113		
tPLH	014	CLK Any O C = 300 pF		2	21			
t <sub>PHL</sub>	OLK	Any Q	C <sub>L</sub> = 300 pF	2	21	ns		
<sup>t</sup> PHL	CLA	Any Q	C <sub>L</sub> = 50 pF	1	17.5	ns		
<sup>t</sup> PZH	7.F	4	0 50 5	1.	17			
<sup>t</sup> PZL	ŌĒ	Any Q	C <sub>L</sub> = 50 pF	1	17	ns		
<sup>t</sup> PZH	ŌĒ	A O	0 200 -	1	25	ns		
t <sub>PZL</sub>	OE	Any Q C <sub>L</sub> = 300 pF		Any Cr CL = 300 pr		1	29.5	. 118
tPHZ			0 50-5	1	16			
tPLZ	ŌĒ	Any Q	C <sub>L</sub> = 50 pF	1	14	ns		
tPHZ	ŌĒ	AmicO	0. 5-5	1	12			
<sup>‡</sup> PLZ	]	Any Q	C <sub>L</sub> = 5 pF	1	11	ns		

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, V <sub>CC</sub>			. 7	٧
Input voltage, V <sub>1</sub>			5.5	٧
Voltage applied to a disabled 3-state output			5.5	٧
Operating free-air temperature range, TA: SN74ALS29823	0°0	C to	70°	'n
Storage temperature range				

<sup>\$</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### recommended operating conditions

			SN74AL829823		UNIT		
			MIN	NOM	MAX	UNII	
Vcc	Supply voltage		4.75	5	5.25	V	
VIH	High-level input voltage		2			V	
VIL	Low-level input voltage				0.8	V	
ᅙ	High-level output current			-	-24	mA	
lOL	Low-level output current				48	mA	
	t <sub>w</sub> Pulse duration	CLR low	5				
·w		CLK high or low	5			ns	
		CLR inactive	5				
tsu	Setup time before CLKT	Data	2			ns	
		CLKEN high or low	6				
1	th Hold time after CLK↑	CLKEN	0				
ฑ		Data	2			ns	
TA	Operating free-air temperature		0	25	70	့	

# SN54ALS29823, SN74ALS29823 9-BIT BUS-INTERFACE FLIP-FLOPS WITH 3-STATE OUTPUTS SDAS146B - JANUARY 1986 - REVISED JANUARY 1995

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETER			SN7	4ALS29	823	
PARAMETER	TEST CO	TEST CONDITIONS		TYPT	MAX	UNIT
VIK	V <sub>CC</sub> = 4.75 V,	lj = -18 mA			-1.2	٧
Mari	Von 475 V	I <sub>OH</sub> = −15 mA	2.4	3.3		V
VOH	VOH VCC = 4.75 V	IOH = ~24 mA	2	3.1		٧
VOL	V <sub>CC</sub> = 4.75 V,	I <sub>OL</sub> = 48 mA		0.35	0.5	٧
lozh	V <sub>CC</sub> = 5.25 V,	V <sub>O</sub> = 2.4 V			20	μА
lozl	V <sub>CC</sub> = 5.25 V,	V <sub>O</sub> = 0.4 V			-20	μА
lį	V <sub>CC</sub> = 5.25 V,	V <sub>I</sub> = 5.5 V			0.1	mA
ЧH	$V_{CC} = 5.25 \text{ V},$	V <sub>I</sub> = 2.7 V			20	ıΑ
lil.	$V_{CC} = 5.25 \text{ V},$	V <sub>1</sub> = 0.4 V			-0.2	mA
los‡	V <sub>CC</sub> = 5.25 V,	V <sub>O</sub> <b>=</b> 0	-75		-250	mA
Icc	V <sub>CC</sub> = 5.25 V,	Outputs open		80	115	mA

<sup>†</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25 ^{\circ}\text{C}$ .

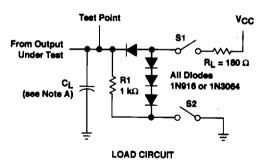
# switching characteristics (see Figure 1)

	FROM	то		VCC = MIN to MAX\$, TA = MIN to MAX\$		
PARAMETER	(INPUT)	(OUTPUT)	TEST CONDITIONS	SN74AL	S29823	UNIT
			1	MIN	MAX	1
tPLH	CLK	4	0 50-5	2	10	
tPHL	OLK	Any Q	Cլ = 50 pF	2	10	ns
<sup>†</sup> PLH	CLK		2 22 5		16	
tpHL_	OLK	Any Q	C <sub>L</sub> = 300 pF		16	ns
†PHL	CLR	Any Q	Cլ = 50 pF		12	ns
tPZH		A 0	0 50-5		14	
tPZL_		Any Q	C <sub>L</sub> = 50 pF		14	ns
<sup>t</sup> PZH	<u> </u>	A	0. 000-5		20	ns
tPZL .	OE	Any Q	C <sub>L</sub> = 300 pF		23	115
tPHZ	ŌĒ		0 50.5		14	ns
<sup>t</sup> PLZ	OE	Any Q	C <sub>L</sub> = 50 pF	12		115
t <sub>PHZ</sub>	<sup>t</sup> PHZ <b>⊙</b> E		Ct = 5 pF		9	
tPLZ		Any Q	OL = 5 pr		. 9	ns

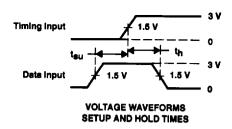
For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

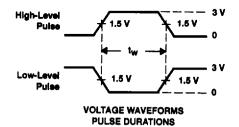
<sup>‡</sup> Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

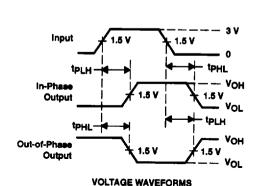
# PARAMETER MEASUREMENT INFORMATION

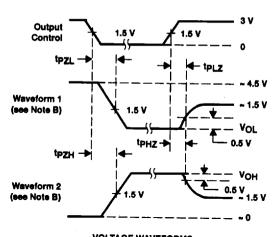


SWITCH	SWITCH POSITION TABLE					
TEST	81	82				
tPLH	Closed	Closed				
tPHL	Closed	Closed				
tPZH	Open	Closed				
tPZL t	Closed	Open				
tPHZ	Closed	Closed				
tPLZ	Closed	Closed				









PROPAGATION DELAY TIMES

NOTES: A. C<sub>L</sub> includes probe and jig capacitance.

VOLTAGE WAVEFORMS ENABLE AND DISABLE TIMES, 3-STATE OUTPUTS

B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.

C. All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z<sub>O</sub> = 50 Ω, t<sub>f</sub> ≤ 2.5 ns, t<sub>f</sub> ≤ 2.5 ns.

Figure 1. Load Circuit and Voltage Waveforms

