

Obsolete – Part Discontinued

Description

high-speed applications.

Pin Configuration

A0

Bo

Co [

A1 [

B1

C1

A2 [B2

C₂

Аз [

B3 [

C3

AEN

GND



PI5C3390/PI5C32390

16-to-8 Multiplexer/Demultiplexer Bus Switch

Diodes' PI5C3390 and PI5C32390 are 16-to-8 multiplexer/demultiplexer bus switches with a low On-Resistance allowing inputs to

be connected directly to outputs. The two enable inputs connect

each of eight I/O to the common I/O pin. This multiplexer func-

tion can be used to select and route logic signals to form crossbar switches, isolate bus capacitance, or provide a zero delay switch

connection. The bus switch creates no additional ground bounce

noise or additional propagation delay. The PI5C32390 is designed

with an internal 25-ohm resistor reducing noise reflection in

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16

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] Vcc

A7

B7

] C7

A6

B6

C6

A5

] B5

C5

7 A4

B4

C4

BEN

Ο 1

2

3

4

5

6

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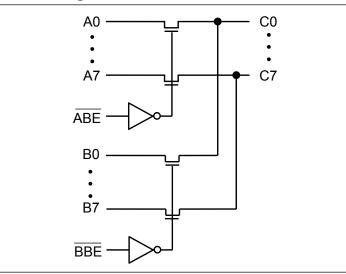
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Features

- → Near-Zero propagation delay
- → Low noise, 25Ω version (PI5C32390)
- → 5Ω switches connect inputs to outputs (PI5C3390)
- → Direct bus connection when switches are ON
- → Ultra-low quiescent power (0.2 μ A typical)
 - Ideally suited for notebook applications
- → Packaging (Pb-free & Green):
 - □ 28-pin QSOP (Q)

Block Diagram



Truth Table⁽¹⁾

Function	AEN	BEN	A Sw	B Sw
Disconnect	Н	Н	Off	Off
A to C	L	Н	On	Off
B to C	Н	L	Off	On
A, B to C	L	L	On	On

Pin Description

Pin Name	I/O	Description
$\overline{\text{AEN}}, \overline{\text{BEN}}$	Ι	Bus Output Enable (Active LOW)
A0 - A7	I/O	Bus A
B0 - B7	I/O	Bus B
C0 - C7	I/O	Bus C

Note: 1. H = High Voltage Level, L = Low Voltage Level

PI5C3390/PI5C32390





Absolute Maximum Ratings

Storage Temperature65°C to +150°C
Ambient Temperature with Power Applied–40°C to +85°C
Supply Voltage to Ground Potential (Inputs&Vcc Only) –0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs&D/O Only)-0.5V to 7.0V
DC Input Voltage0.5V to +7.0V
DC Output Current120mA
Power Dissipation

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, $V_{CC} = 5V \pm 5\%$)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH	I Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW	Level	-0.5		0.8	V
I _{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$		-1		1	μΑ
I	Input LOW Current	V _{CC} = Max., V _{IN} = GND		-1		1	μΑ
I _{OZ}	High Impedance Output Current	$0 \le A, B \le V_{CC}$		-1		1	μΑ
V _{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$			-0.7	-1.2	V
I _{OS}	Short Circuit Current ⁽³⁾	A (B) = 0V, B (A) = V_{CC}		100			mA
V _H	Input Hysteresis at Control Pins				150		mV
		$V_{CC} = Min., V_{IN} = 0.0V,$	PI5C3390	20	5	7	
D	R _{ON} Switch On Resistance ⁽⁴⁾	I _{ON} = 48 mA	PI5C32390	20	28	40	Ω
KON		$V_{\rm CC} = Min., V_{\rm IN} = 2.4V,$	PI5C3390	20	10	15	22
		$I_{ON} = 15 \text{ mA}$	PI5C32390	20	35	48	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at Vcc = 5.0V, TA = 25°C ambient and maximum loading.

3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance $(T_A = 25^{\circ}C, f = 1 \text{ MHz})$

Parameters ⁽¹⁾	Description	Test Conditions	Тур.	Max.	Units
C _{IN}	Input Capacitance			6	pF
C _{OFF}	A/B Capacitance, Switch Off	$V_{\rm IN} = 0V$		6	pF
C _{ON}	A/B Capacitance, Switch On			16	pF

Notes:

1. This parameter is determined by device characterization but is not production tested.





Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V_{CC} = Max., V_{IN} = GND or V_{CC}		0.2	30	μΑ
ΔI_{CC}	Supply Current @ TTL HIGH	$V_{CC} = Max., V_{IN} = 3.4V^{(3)}$			3.5	mA
ICCD	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max. A and B Pins Open $\overline{\text{BE}}$ = GND Control Input Toggling 50% Duty Cycle			0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.

2. Typical values are at Vcc = 5.0V, +25°C ambient.

3. Per TTL driven input (VIN = 3.4V, control inputs only); A and B pins do not contribute to Icc.

4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

PI5C3390						
Parameters	Description	Test Conditions	Min.	Тур.	Max.	Units
t _{PLH} t _{PHL}	Propagation Delay ^(2, 3) A, B to/from C			0.25		
t _{PZH} t _{PZL}	Bus Enable Time AEN/BEN to A, B, C	$C_{L} = 50 \text{ pF}$ $R_{L} = 500\Omega$	1.5		6.5	ns
t _{PHZ} t _{PLZ}	$\frac{\text{Bus Disable Time}}{\text{AEN}/\text{BEN}}$ to A, B, C		1.5		5.5	

PI5C32390						
Parameters	Description	Test Conditions	Min.	Тур.	Max.	Units
t _{PLH} t _{PHL}	Propagation Delay ^(2, 3) A, B to/from C			1.25		
t _{PZH} t _{PZL}	Bus Enable Time AEN/BEN to A, B, C	$C_L = 50 \text{ pF}$ $R_L = 500\Omega$	1.5		6.5	ns
t _{PHZ} t _{PLZ}	$\frac{\text{Bus Disable Time}}{\text{AEN}/\text{BEN}}$ to A, B, C		1.5		5.5	

Notes:

1. See test circuit and waveforms.

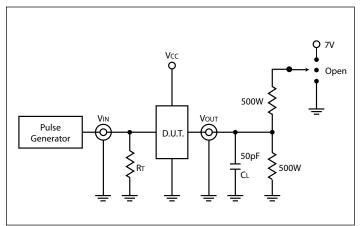
2. This parameter is guaranteed but not tested on Propagation Delays.

3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

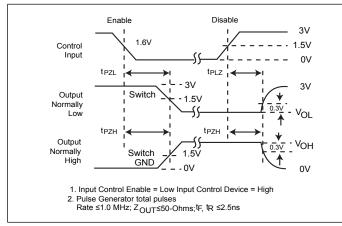




Test Circuit



Enable and Disable Timing Diagram



Switch Position

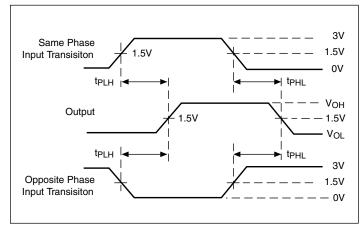
Test	Switch
Disable LOW	Closed
Enable LOW	Closed
t _{PD}	Open

Definitions:

C_L = Load capacitance (includes jig and probe capacitance)

 R_T = Termination resistance (should be equal to Z_{OUT} of the pulse generator)

Propagation Delay Diagram







Part Marking PI5C3309

Q Package



B: Fab Port Code/Cost Down Code YY: Year WW: Workweek 1st X: Assembly Code 2nd X: Fab Code

Q Package-2017



A: Fab Port Code / Cost Down Code YY: Year WW: Workweek 1st X: Assembly Code 2nd X: Fab Code

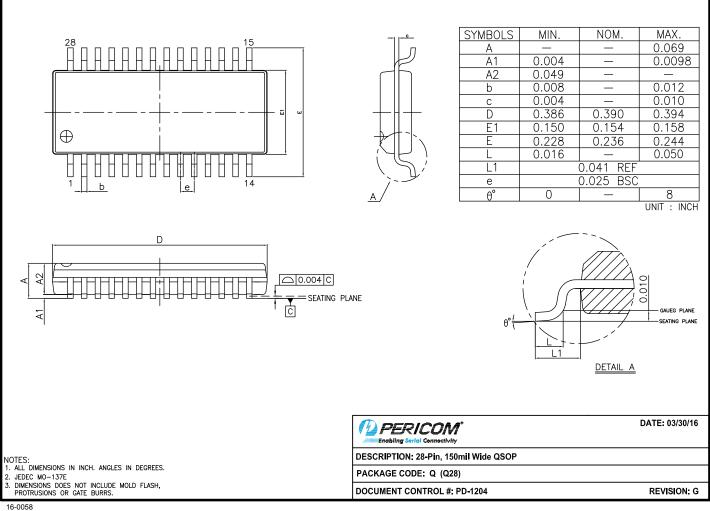
PI5C32309

Top mark not available at this time. To obtain advance information regarding the top mark, please contact your local sales representative.





Packaging Mechanical: 24-QSOP (Q)



For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Package Code	Package Description
PI5C3390QEX	Q	28-pin 150mil wide (QSOP)
PI5C32390QEX	Q	28-pin 150mil wide (QSOP)

Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. Thermal characteristics can be found on the company web site at www.diodes.com/design/support/packaging/

3. E = Pb-free and Green

4. X suffix = Tape/Reel





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