

SRA2219U

PNP Silicon Transistor

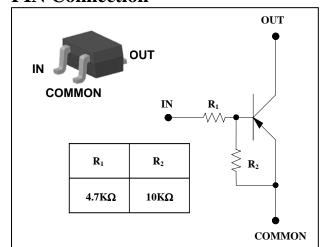
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

	Marking	Package Code	
SRA2201U	<u>CR</u> □ ① ②	SOT-323	

①Device Code ②Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V _I	-20, 7	V
Output current	Io	-100	mA
Power dissipation	P_{D}	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

 $(Ta=25^{\circ}C)$

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_0 = -5V$, $I_0 = -10$ mA	30	-	-	-
Output voltage	V _{O(ON)}	I _O =-10mA, I _I =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_0 = -0.2V$, $I_0 = -5mA$	-	-1.2	-1.6	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = -5V$, $I_0 = -0.1$ mA	-0.5	-0.82	-	V
Transition frequency	f _T *	$V_0 = -10V$, $I_0 = -5mA$, $f = 1MHz$	-	200	-	MHz
Input current	l ₁	$V_1 = -5V, I_0 = 0$	-	-	-1.8	mA
Input resistor (Input to base)	R_1	-	3.3	4.7	6.1	KΩ
Input resistor (Base to common)	R_2	-	7	10	13	KΩ

^{* :} Characteristic of transistor only

Electrical Characteristic Curves

Fig. 1 Pc - Ta

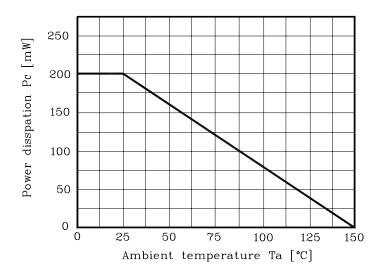


Fig. 2 I_O - $V_{I(ON)}$

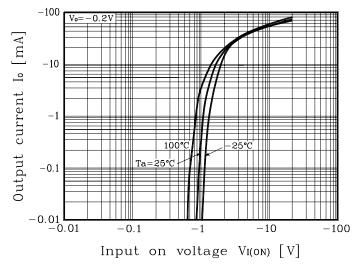


Fig. 3 $I_{\rm O}$ - $V_{\rm I(OFF)}$

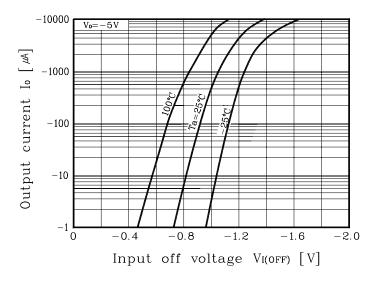
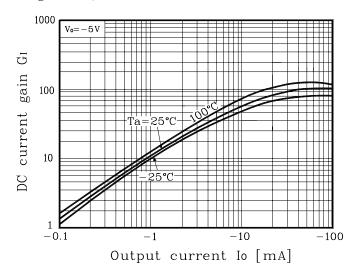
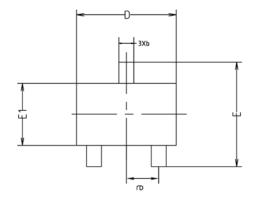


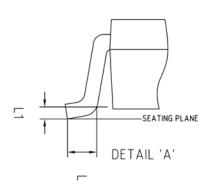
Fig. 4 G_I - I_O

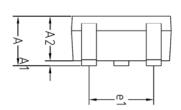


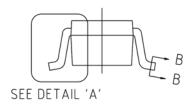
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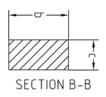
Outline Dimension





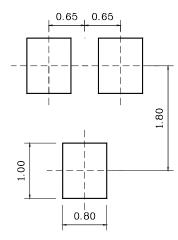






SYMBOL	1	NOTE			
STRIBUL	MINIMUM	NOMINAL	MAXIMUM	NUIE	
Α	0.90	-	1.25		
A1	0.00	-	0.10		
A2	0.85	0.90	0.95		
Ь	0.30	-	0.40		
С	0.10	-	0.25		
D	1.90	2.00	2.10		
E	1.95	2.10	2.25		
E1	1.15	1.25	1.35		
e	0.65BSC				
e1	1.20	-	1.40		
L	0.10	-	-		
11		0.12BS	۲		

*Recommend PCB solder land [Unit: mm]



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