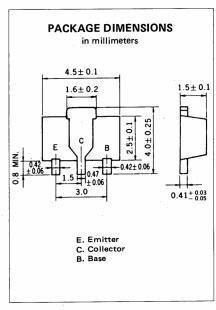


SILICON TRANSISTOR 2SA1463

HIGH SPEED SWITCHING PNP SILICON EPITAXIAL TRANSISTOR POWER MINI MOLD



DESCRIPTION

The 2SA1463 is designed for power amplifier and high speed

switching applications.

FEATURES

- High speed, high voltage switching.
- Low Collector Saturation Voltage.
- Complementary to the NEC 2SC3736 NPN transistor.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C)

Collector to Base Voltage	V _{CBO}	-60	V
Collector to Emitter Voltage	V_{CEO}	-45	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current (DC)	I _{C(DC)}	-1.0	Α
Collector Current (Pulse)*	I _C (Pulse)	-2.0	Α
Total Power Dissipation **	P_T	2.0	W
Junction Temperature	T _i	150	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

- * PW \leq 10 ms, Duty Cycle \leq 50 %
- ** When mounted on ceramic substrate of 16 cm² x 0.7 mm

ELECTRICAL CHARACTERISTICS (TA = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDICTIONS	
Collector Cutoff Current	ICES			-0.5	μА	V _{CE} = -45 V, R _{BE} = 0	
Emitter Cutoff Current	I _{EBO}			-0.5	μА	V _{EB} = -4.0 V, I _C = 0	
DC Current Gain	hFE1***	60		200		V _{CE} = -10 V, I _C = -50 mA	
DC Current Gain	h _{FE2} ***	60			-	$V_{CE} = -10 \text{ V, I}_{C} = -500 \text{ mA}$	
Collector Saturation Voltage	VCE(sat)***		-0.26	-0.6	V	IC = -500 mA, IB = -50 mA	
Base Saturation Voltage	V _{BE(sat)} ***		-0.98	-1.2	V	16 300 mA, 18 30 m/	
Gain Bandwidth Product	f _T	300	400		MHz	$V_{CE} = -10 \text{ V, I}_{E} = 100 \text{ mA}$	
Output Capacitance	C _{ob}		11	25	pF	V _{CB} = -10 V, I _E = 0, f = 1.0 MHz	
Turn-on Time	ton		25	40	ns	I _C = -500 mA	
Storage Time	t _{stg}		46	70	ns	I _{B1} = -I _{B2} = -50 mA	
Turn-off Time	toff		62	100	ns		

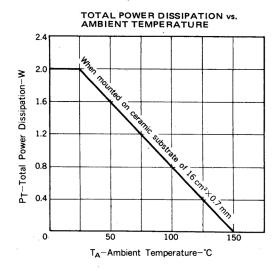
^{***}Pulsed: PW \leq 350 μs , Duty Cycle \leq 2 %

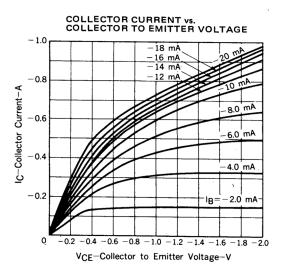
h_{FE} Classification

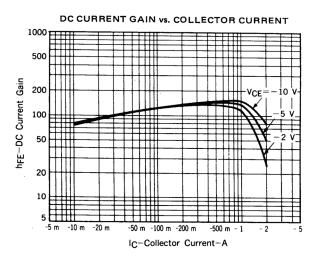
MARKING	TL.	IK		
hFE1	60 to 120	100 to 200		

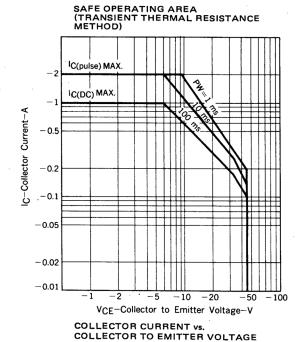
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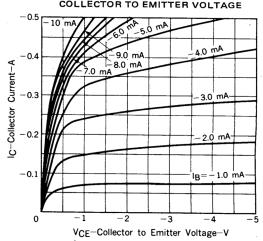
TYPICAL CHARACTERISTICS (TA = 25°C)

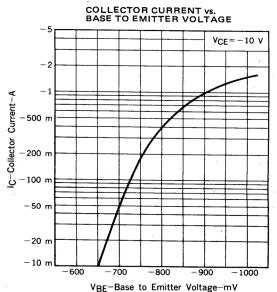


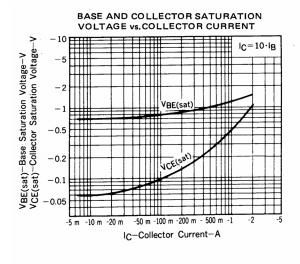


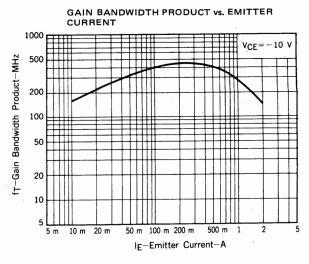


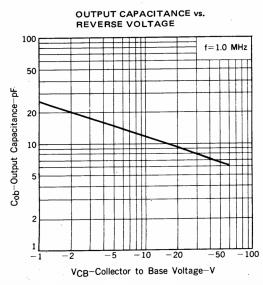


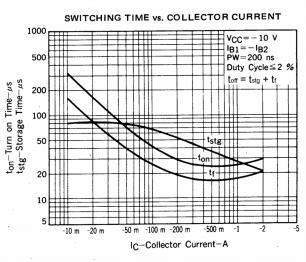




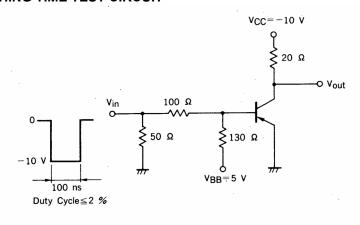


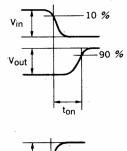


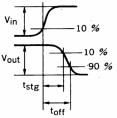




SWITCHING TIME TEST CIRCUIT







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