



P-Channel 1.8-V (G-S) MOSFET

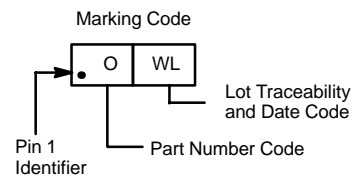
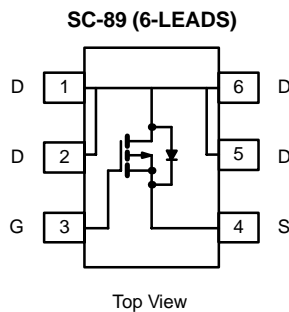
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-12	0.165 @ $V_{GS} = -4.5$ V	-0.95
	0.220 @ $V_{GS} = -2.5$ V	-0.82
	0.280 @ $V_{GS} = -1.8$ V	-0.67

FEATURES

- TrenchFET® Power MOSFET
- Low Threshold
- Smallest LITTLE FOOT® Package: 1.6 mm x 1.6 mm
- Low 0.6-mm Profile

APPLICATIONS

- Cell Phones and Pagers
 - Load Switch



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	5 secs	Steady State	Unit	
Drain-Source Voltage	V_{DS}	-12		V	
Gate-Source Voltage	V_{GS}	± 8			
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	$T_A = 25^\circ\text{C}$	-0.95	-0.87	A
		$T_A = 70^\circ\text{C}$	-0.76	-0.69	
Pulsed Drain Current	I_{DM}	-4			
Continuous Diode Current (Diode Conduction) ^a	I_S	-0.18	-0.14		
Maximum Power Dissipation ^a	P_D	$T_A = 25^\circ\text{C}$	0.21	0.17	W
		$T_A = 70^\circ\text{C}$	0.13	0.10	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R_{thJA}	$t \leq 5$ sec	500	600	$^\circ\text{C/W}$
		Steady State	600	720	

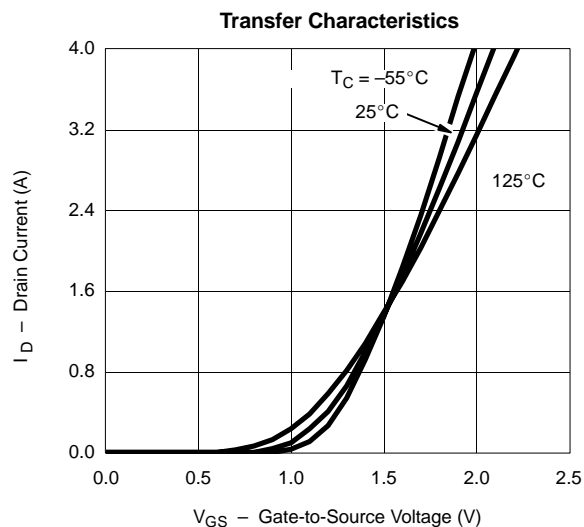
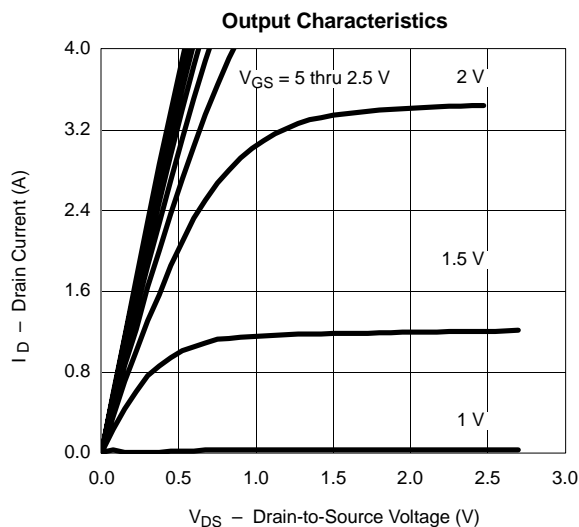
Notes
a. Surface Mounted on 1" x 1" FR4 Board with minimum copper.

SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-0.45			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±8 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -9.6 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -9.6 V, V _{GS} = 0 V, T _J = 70 °C			-5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -4.5 V	-4			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -4.5 V, I _D = -0.87 A		0.140	0.165	Ω
		V _{GS} = -2.5 V, I _D = -0.75 A		0.180	0.220	
		V _{GS} = -1.8 V, I _D = -0.2 A		0.230	0.280	
Forward Transconductance ^a	g _{fs}	V _{DS} = -10 V, I _D = -0.87 A		3.5		S
Diode Forward Voltage ^a	V _{SD}	I _S = -0.14 A, V _{GS} = 0 V		-0.78	-1.2	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -6 V, V _{GS} = -4.5 V, I _D = -0.87 A		3.8	6	nC
Gate-Source Charge	Q _{gs}			0.7		
Gate-Drain Charge	Q _{gd}			0.8		
Turn-On Delay Time	t _{d(on)}	V _{DD} = -6 V, R _L = 12 Ω I _D ≅ -0.5 A, V _{GEN} = -4.5 V, R _G = 6 Ω		15	30	ns
Rise Time	t _r			20	40	
Turn-Off Delay Time	t _{d(off)}			30	60	
Fall Time	t _f			16	30	
Source-Drain Reverse Recovery Time	t _{rr}		I _F = -0.14 A, di/dt = 100 A/μs		20	

Notes

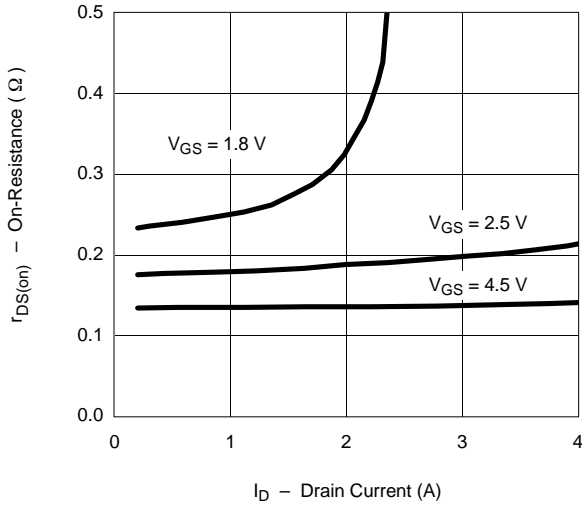
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

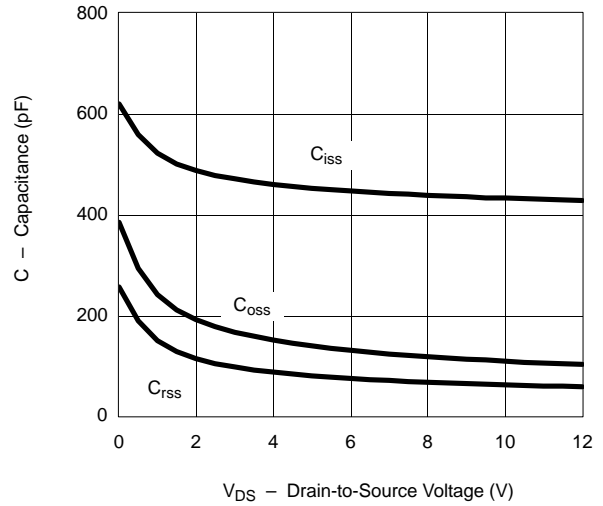


TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

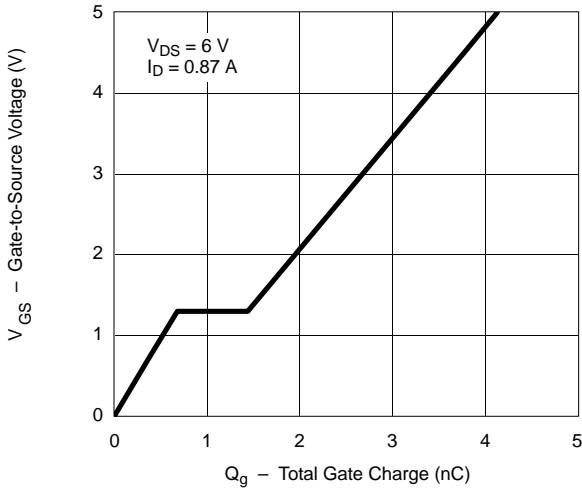
On-Resistance vs. Drain Current



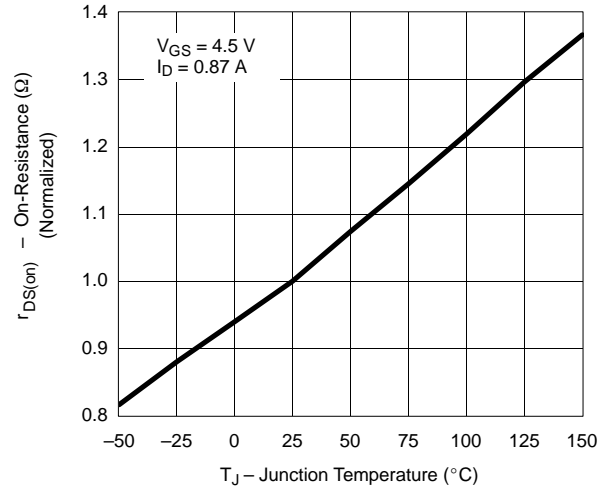
Capacitance



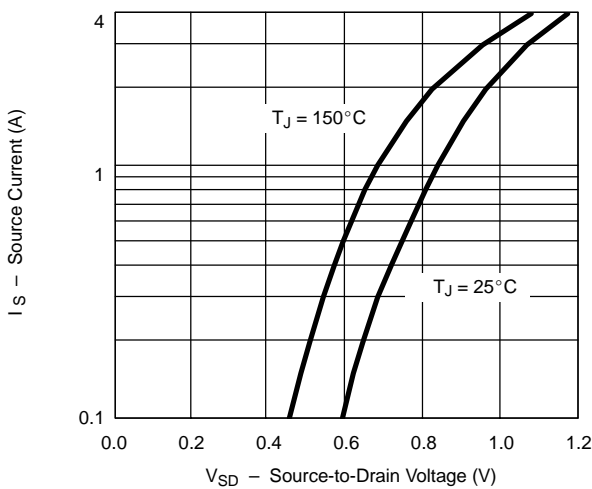
Gate Charge



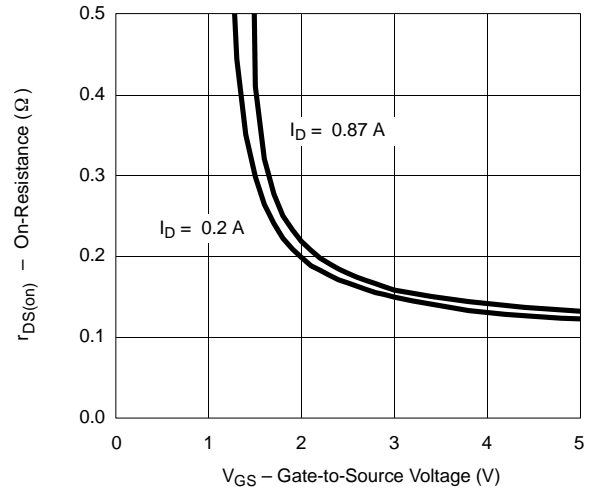
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

