



SIGNETICS
INTEGRATED CIRCUITS
 (Interface) 8T Series

The digital 8T Series is a standard TTL compatible family of interface circuits that is used in such applications as line driving and receiving, tri-state buses, "party line" bus systems, driving LEDs and other displays, MOS interface and a variety of timing circuits and special purpose buffer gates.

MAXIMUM RATINGS

Rating	Value	Unit
V_{CC}	$5.0 \pm 5.0\%$	V
Input Voltage - V_{in}	5.5	V
Output Voltage - V_{out}	7.0	V
Storage Temperature - T_{stg}	-65 to +175	°C
Operating Temperature - T_A	-55 to +125	°C

PACKAGING AVAILABLE

(See page 88):

- | | |
|-------------------------|-----------------------|
| C - 14-LEAD DUAL INLINE | D - 14-LEAD FLAT PACK |
| E - 16-LEAD DUAL INLINE | F - 16-LEAD FLAT PACK |
| J - 24-LEAD DUAL INLINE | K - 24-LEAD FLAT PACK |
| R - 20-LEAD DUAL INLINE | |

FUNCTIONS AND CHARACTERISTICS

FUNCTION	TYPE/PKG	POWER CONSUMPTION (mW)
7-Segment Decoder/Driver	8T04/E,F	394
7-Segment Decoder/Transistor Driver	8T05/E,F	394
7-Segment Decoder/Display Driver	8T06/E,F	394
Quad Bus Driver	8T09/C,D	340
Quad D Type Bus Flip-Flop	8T10/E,F	—
Dual Line Driver	8T13/E,F	315
Triple Line Receiver with Hysteresis	8T14/E,F	380
Dual Communications EIA/MIL Line Driver	8T15/C,D	275
MIL Line Receiver with Hysteresis	8T16/C,D	75
Dual 2-Input NAND Interface Gate	8T18/C,D	44
Bidirectional Monostable Multivibrator	8T20/E,F	289
Dual Line Driver	8T23/E,F	315
Triple Line Receiver with Hysteresis	8T24/E,F	380
Tri-State Quad Bus Transceiver	8T26/E,F	457
3-State Quad Bus Transceiver	8T26A/E,F	—

TTL FUNCTIONS AND CHARACTERISTICS (continued)

FUNCTION	TYPE/PKG	POWER CONSUMPTION (mW)
3-State Quad Bus Transceiver	8T28/E,F	—
8-Bit Latched Bidirectional I/O Port	8T31/J,K	150
8-Bit Latched Addressable Bidirectional I/O Port	8T32/J,K	—
8-Bit Latched Addressable Bidirectional I/O Port	8T33/J,K	—
Quad Bus Transceiver with 3-State Outputs	8T34/E,F	—
8-Bit Latched Addressable Bidirectional I/O Port	8T35/J,K	—
Tri-State, Asynchronous User Port	8T36/J,K	—
Hex Bus Receiver with Hysteresis-Schmitt Trigger	8T37/E,F	—
Quad Bus Transceiver (O.C.)	8T38/E,F	—
Quad 2-Input NAND Interface Gate	8T80/C,D	20
Hex Inverter Interface Element	8T90/C,D	—
High-Speed Hex 3-State Buffer	8T95/E,F	—
High-Speed Hex 3-State Buffer	8T96/E,F	295
High-Speed Hex 3-State Inverter	8T97/E,F	—
High-Speed Hex 3-State Inverter	8T98/E,F	—
Octal 3-State Transceiver	8T125/R	—
Quad 3-State Transceiver	8T126/E,F	—
Quad 3-State Transceiver	8T129/E,F	—
Octal Transceiver	8T245/R	—
Quad Bus Receiver	8T380/C,D	210
6-Bit Latch	8T3404/E,F	—