

Ultrafast Rectifier, 8 A FRED Pt[™]



- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" data sheet)
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

FRED Pt[™] series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

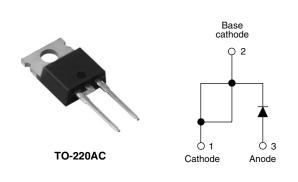
ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Repetitive peak reverse voltage	V _{RRM}		400	V	
Average rectified forward current	I _{F(AV)}	T _C = 155 °C	8		
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	100	А	
Repetitive peak forward current	I _{FRM}		16		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	400	-	-	
Forward voltage	V _F	I _F = 8 A	-	1.19	1.3	V
		I _F = 8 A, T _J = 150 °C	-	0.94	1.0	
Reverse leakage current	I _R	$V_{R} = V_{R}$ rated	-	0.2	10	
		$T_J = 150 \ ^{\circ}C, \ V_R = V_R \ rated$	-	20	500	μΑ
Junction capacitance	CT	V _R = 400 V	-	14	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply







PRODUCT SUMMARY			
t _{rr}	60 ns		
I _{F(AV)}	8 A		
V _R	400 V		

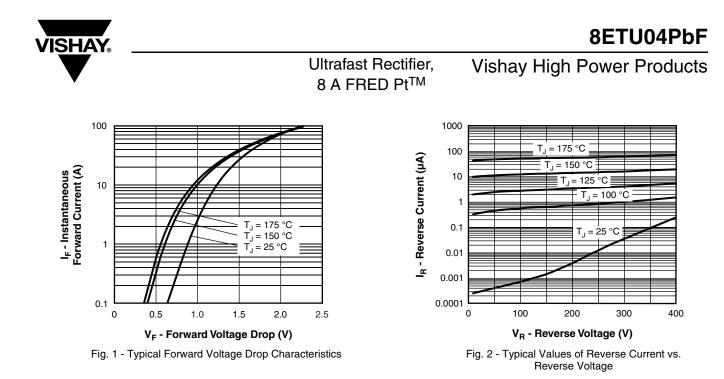
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{A}, \text{ V}_R = 30 \text{ V}$		-	35	60		
Reverse recovery time	ie t _{rr}	T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs	-	43	-	ns	
		T _J = 125 °C		-	67	-		
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2.8	-	А	
		IRRM	T _J = 125 °C	di _F /di = 200 A/μs V _R = 200 V	-	6.3	-	~
Reverse recovery charge	Q _{rr}	Q _{rr}	T _J = 25 °C		-	60	-	nC
			T _J = 125 °C		-	210	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R _{thJC}		-	1.8	2	
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
		-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-220AC		8ETU04		



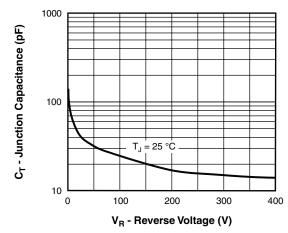


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

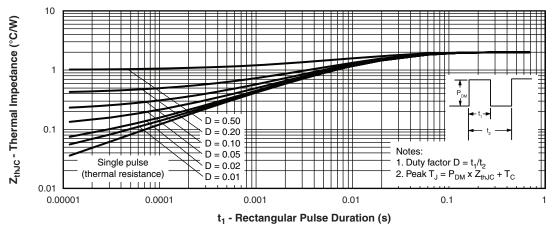


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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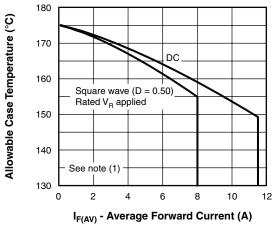
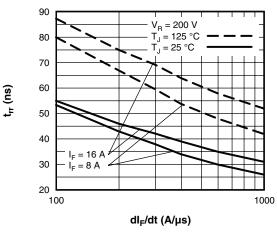
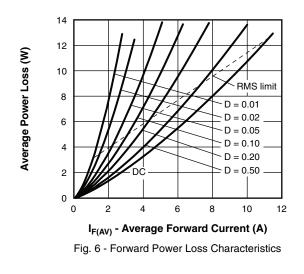


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt





- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

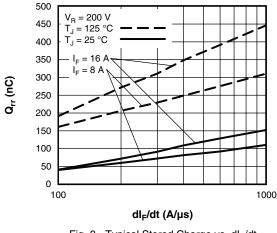


Fig. 8 - Typical Stored Charge vs. dl_F/dt



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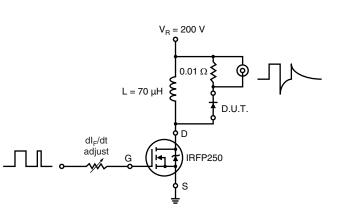


Fig. 9 - Reverse Recovery Parameter Test Circuit

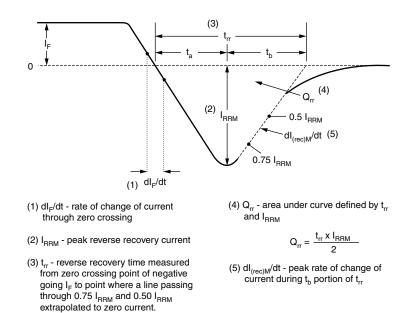


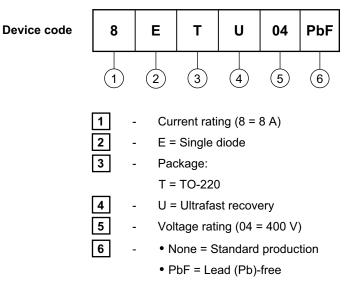
Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95221				
Part marking information	http://www.vishay.com/doc?95224			



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