

# GD54/74HC367, GD54/74HCT367 HEX 3-STATE NONINVERTING BUFFERS

## General Description

These devices are identical in pinout to the 54/74LS367. They have high drive current outputs which enable high speed operation even when driving large bus capacitances. The HC/HCT 365 and HC/HCT 367 have noninverting outputs, while the HC/HCT 366 and HC/HCT 368 have inverting outputs. The HC/HCT 365 and HC/HCT 366 have two 3-state control inputs which are NORed together to control all 6 gates. The HC/HCT 367 and HC/HCT 368 have two output enables, where one enable controls 4 gates and the other controls the remaining 2 gates. These devices are characterized for operation over wide temperature ranges to meet industry and military specifications.

## Features

- Low Power consumption characteristic of CMOS devices
- Output drive capability: 15 LS TTL Loads Min.
- Operating speed superior to LS TTL
- Wide operating voltage range: for HC 2 to 6 volts for HCT 4.5 to 5.5 volts
- Low input current: 1 $\mu$ A Max.
- Low quiescent current: 80 $\mu$ A Max. (74HC)
- High noise immunity characteristic of CMOS
- Diode protection on all inputs

## Logic Symbol

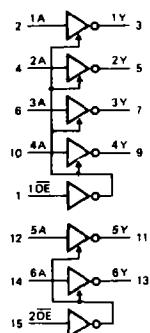
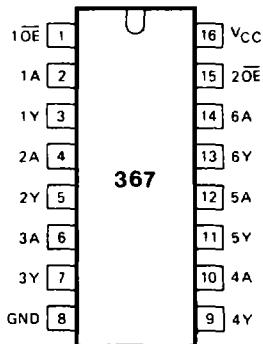


Fig. 1 Logic symbol

## Pin Configuration



suffix-blank : Plastic Dual In Line Package  
suffix-J : Ceramic Dual In Line Package  
suffix-D : Small Outline Package

## Function Table

INPUTS		OUTPUTS
n $\bar{O}E$	nA	nY
L	L	L
L	H	H
H	X	Z

H = HIGH voltage level

L = LOW voltage level

X = don't care

Z = high impedance OFF-state

## Absolute Maximum Ratings

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CC}$	DC Supply voltage		-0.5	+7	V
$I_{IK}, I_{OK}$	DC input or output diode current	for $V_i < -0.5$ or $V_i > V_{CC} + 0.5V$		20	mA
$I_O$	DC output source or sink current	for $-0.5V < V_O < V_{CC} + 0.5V$		35	mA
$I_{CC}$	DC $V_{CC}$ or GND current			70	mA
$T_{STJ}$	Storage temperature range		-65	150	°C
$P_D$	Power dissipation per package	above $+70^{\circ}\text{C}$ derate linearly with $8\text{mW/K}$		500	mW
$T_L$	Lead temperature	At distance $1.16 \pm 1.32$ in from case for 60 sec(CERAMIC) 10 sec(PLASTIC)		300 260	°C

## Recommended Operating Conditions

CHARACTERISTIC	LIMITS		UNITS
	MIN.	MAX	
Supply-Voltage Range $V_{CC}$ : GD54/74HC Types GD54/74HCT Types	2 4.5	6 5.5	V
DC Input or Output Voltage $V_i, V_O$	0	$V_{CC}$	V
Operating Temperature $T_A$ : GD74 Types GD54 Types	-40 -55	+85 +125	°C
Input Rise and Fall times $t_r, t_f$ : GD54/74HC Types at 2V at 4.5V at 6V GD54/74HCT Types at 4.5V		1000 500 400 500	ns

## Logic Diagram

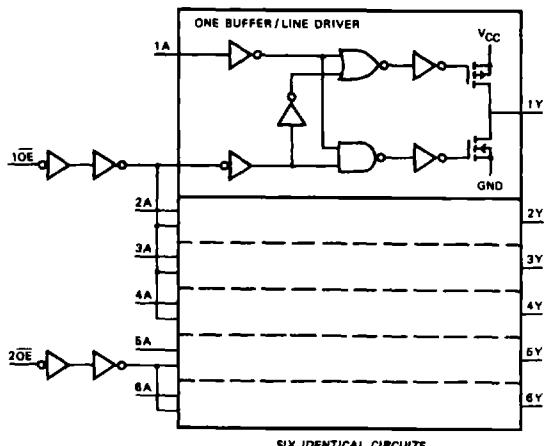


Fig. 2 Logic diagram.

## DC Electrical Characteristics for HC

SYMBOL	PARAMETER	TEST CONDITION	V <sub>CC</sub> (V)	T <sub>A</sub> =25°C			GD74HC367		GD54HC367		UNIT
				MIN	TYP	MAX	MIN.	MAX	MIN	MAX	
V <sub>IH</sub>	HIGH level input Voltage		2.0 4.5 6.0	1.5 3.15 4.2			1.5 3.15 4.2		1.5 3.15 4.2		V
V <sub>IL</sub>	LOW level input voltage		2.0 4.5 6.0		0.3 0.9 1.2		0.3 0.9 1.2		0.3 0.9 1.2		V
V <sub>OH</sub>	HIGH level output voltage	V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> =-20μA I <sub>OH</sub> =-6mA I <sub>OH</sub> =-7.8mA	2.0 4.5 6.0	1.9 4.4 5.9	2.0 4.5 6.0	1.9 4.4 5.9		1.9 4.4 5.9		V
V <sub>OL</sub>	LOW level output voltage	V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> =20μA I <sub>OL</sub> =6mA I <sub>OL</sub> =7.8mA	2.0 4.5 6.0		0.1 0.1 0.1	0.1 0.1 0.1	0.1 0.1 0.1	0.1 0.1 0.1		V
I <sub>IN</sub>	Input leakage Current	V <sub>IN</sub> =V <sub>CC</sub> or GND		6.0			0.1		1.0		1.0 μA
I <sub>OZ</sub>	Three-State leakage current	V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub>	V <sub>O</sub> =V <sub>CC</sub> or GND	6.0		0.01	0.5		5.0		10.0 μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> =V <sub>CC</sub> or GND I <sub>out</sub> =0μA		6.0			8		80		160 μA

## DC Electrical Characteristics for HCT

SYMBOL	PARAMETER	TEST CONDITION	V <sub>CC</sub> (V)	T <sub>A</sub> =25°C			GD74HCT367		GD54HCT367		UNIT
				MIN	TYP	MAX	MIN.	MAX	MIN	MAX	
V <sub>IH</sub>	HIGH level input Voltage		4.5 to 5.0	2.0			2.0		2.0		V
V <sub>IL</sub>	LOW level input voltage		4.5 to 5.5			0.8		0.8		0.8	V
V <sub>OH</sub>	HIGH level output voltage	V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> =-20μA I <sub>OH</sub> =-6mA	4.5	4.4	4.5		4.4		4.4	
V <sub>OL</sub>	LOW level output voltage	V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> =20μA I <sub>OL</sub> =6mA	4.5	3.98	4.3		3.84		3.7	
I <sub>IN</sub>	Input leakage Current	V <sub>IN</sub> =V <sub>CC</sub> or GND		5.5			0.1		0.1		0.1 μA
I <sub>OZ</sub>	Three-State leakage current	V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub>	V <sub>O</sub> =V <sub>CC</sub> or GND	5.5		0.01	0.5		5.0		10.0 μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> =V <sub>CC</sub> or GND I <sub>out</sub> =0μA		5.5			8		80		160 μA

# GD54/74HC367, GD54/74HCT367

## AC Characteristics for HC: $t_r=t_f=6\text{ns}$ $C_L=50\text{ pF}$

SYMBOL	PARAMETER	$V_{CC}$ (V)	$T_A=25^\circ\text{C}$			GD74HC367		GD54HC367		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN.	MAX.	
$t_{PLH}$	Propagation Delay Time nA to nY	2.0		30	90		120		140	ns
		4.5		10	19		24		29	
		6.0		9	16		20		25	
$t_{PZH}$	3-state Output Enable Time $n\overline{OE}$ to nY	2.0		38	135		175		205	ns
		4.5		12	24		32		40	
		6.0		11	22		30		38	
$t_{PLZ}$	3-state Output Disable Time $n\overline{OE}$ to nY	2.0		38	135		175		205	ns
		4.5		12	24		32		40	
		6.0		11	22		30		38	
$t_{TLH}$	Output Transition Time	2.0		15	60		75		90	ns
		4.5		6	12		15		18	
		6.0		5	10		13		15	

## AC Characteristics for HCT: $t_r=t_f=6\text{ns}$ $C_L=50\text{pF}$

SYMBOL	PARAMETER	$V_{CC}$ (V)	$T_A=25^\circ\text{C}$			GD74HCT367		GD54HCT367		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
$t_{PLH}$	Propagation Delay Time nA to nY	4.5		12	22		26		30	ns
$t_{PZH}$	3-state Output Enable Time $n\overline{OE}$ to nY	4.5		13	26		34		42	ns
$t_{PLZ}$	3-state Output Disable Time $n\overline{OE}$ to nY	4.5		13	26		34		42	ns
$t_{TLH}$	Output Transition Time	4.5		7	12		15		18	ns

## AC Waveforms

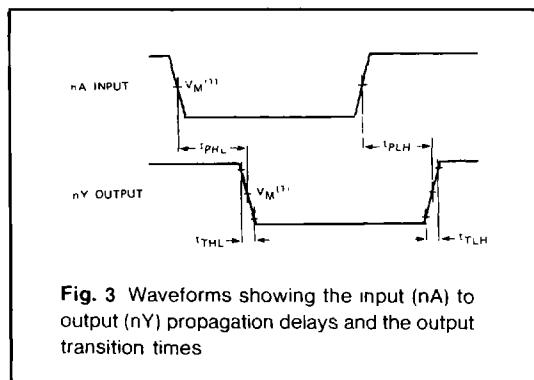


Fig. 3 Waveforms showing the input (nA) to output (nY) propagation delays and the output transition times

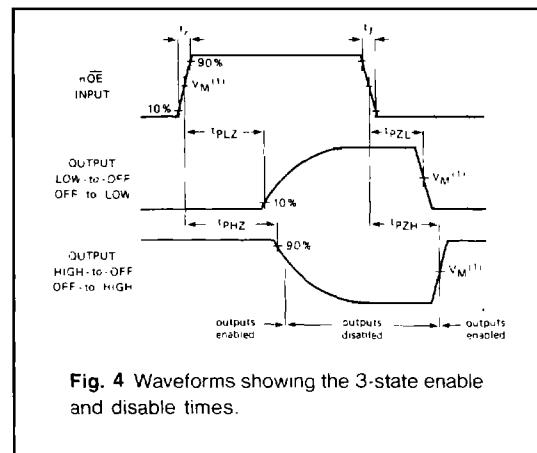


Fig. 4 Waveforms showing the 3-state enable and disable times.

### Note to AC waveforms

(1) HC       $V_M=50\%$ ,  $V_i=GND$  to  $V_{CC}$   
          HCT     $V_M=1.3V$   $V_i=GND$  to 3V