

**TC74HC151AP, TC74HC151AF, TC74HC151AFN****8 - CHANNEL MULTIPLEXER**

The TC74HC151A is a high speed CMOS 8 - CHANNEL MULTIPLEXER fabricated with silicon gate C<sup>2</sup>MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

One of eight date input signals (D0 - D7) is selected by decoding of the three - bit address input (A, B, C). The selected data appears on two outputs : non - inverting (Y) and inverting (W).

The strobe input provides two output conditions ; a low level on the strobe input transfers the selected data to the outputs. A high level on the strobe input sets the Y output low and the W output high without regard to the data or select input conditions.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

**FEATURES :**

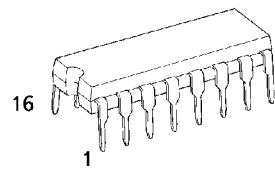
- High Speed..... $t_{pd} = 15\text{ns}(\text{typ.})$  at  $V_{CC} = 5\text{V}$
- Low Power Dissipation..... $I_{CC} = 4\mu\text{A}(\text{Max.})$  at  $T_a = 25^\circ\text{C}$
- High Noise Immunity..... $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (Min.)
- Output Drive Capability.....10 LSTTL Loads
- Symmetrical Output Impedance..... $|I_{OH}| = I_{OL} = 4\text{mA}(\text{Min.})$
- Balanced Propagation Delays..... $t_{PLH} \approx t_{PHL}$
- Wide Operating Voltage Range..... $V_{CC}$  (opr.) =  $2\text{V} \sim 6\text{V}$
- Pin and Function Compatible with 74LS151

**TRUTH TABLE**

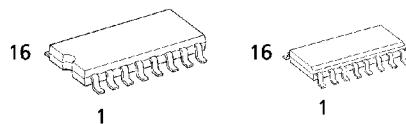
INPUTS				OUTPUTS	
SELECT			STROBE	Y	W
C	B	A	$\bar{ST}$		
X	X	X	H	L	H
L	L	L	L	D0	$\bar{D}_0$
L	L	H	L	D1	$\bar{D}_1$
L	H	L	L	D2	$\bar{D}_2$
L	H	H	L	D3	$\bar{D}_3$
H	L	L	L	D4	$\bar{D}_4$
H	L	H	L	D5	$\bar{D}_5$
H	H	L	L	D6	$\bar{D}_6$
H	H	H	L	D7	$\bar{D}_7$

X : Don't Care

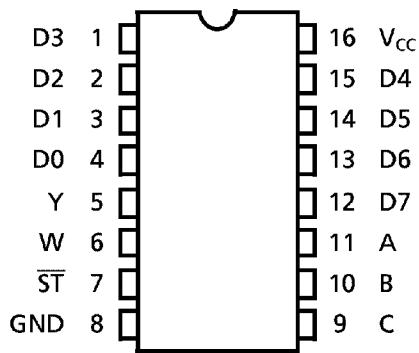
(Note) The JEDEC SOP (FN) is not available in Japan.



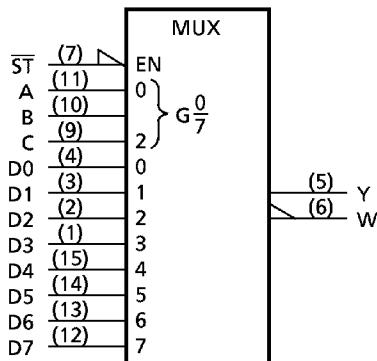
P (DIP16-P-300-2.54A)  
Weight : 1.00g (Typ.)



F (SOP16-P-300-1.27) FN (SOL16-P-150-1.27)  
Weight : 0.18g (Typ.) Weight : 0.13g (Typ.)

**PIN ASSIGNMENT**

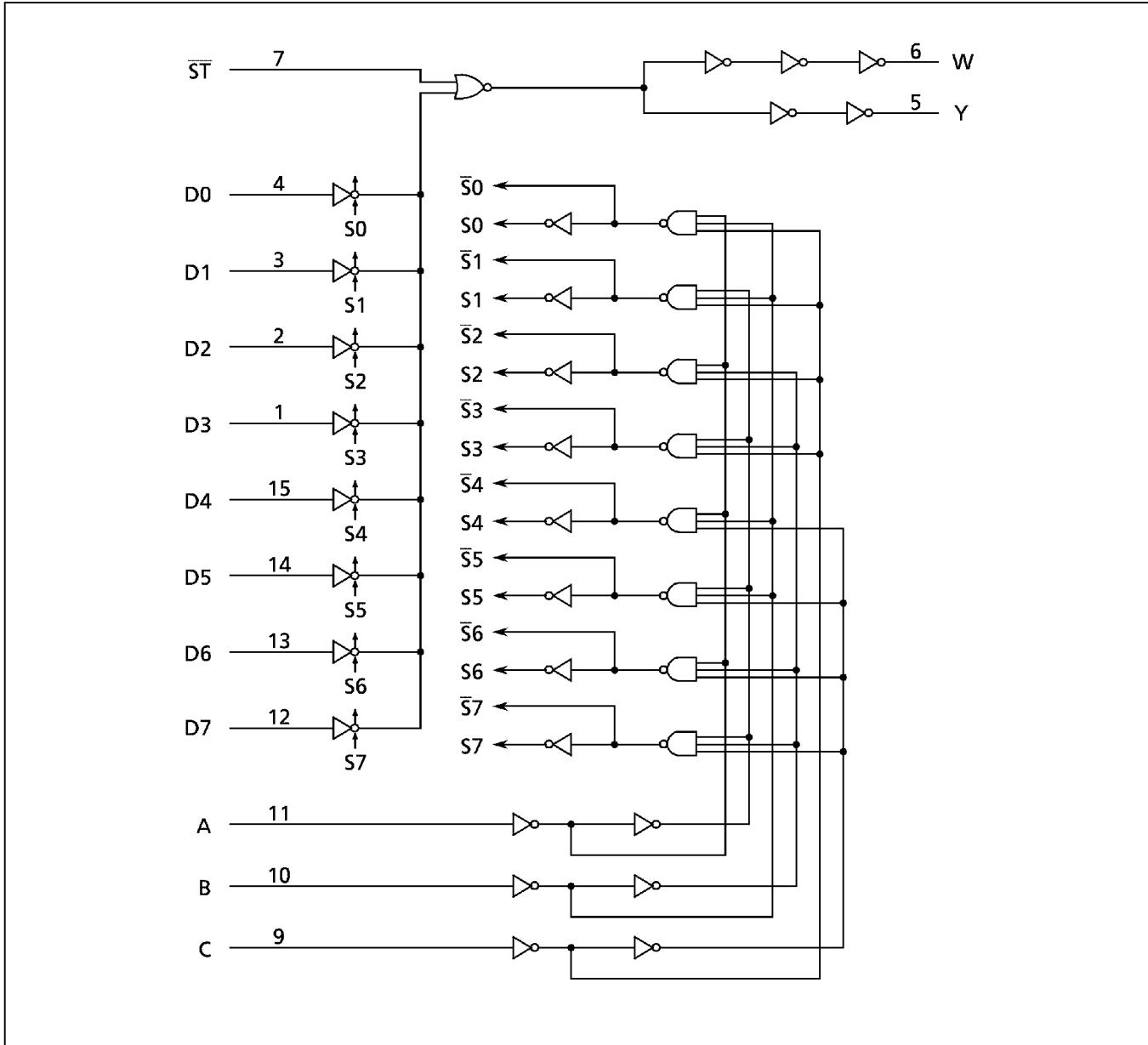
(TOP VIEW)

**IEC LOGIC SYMBOL**

980508EBA2

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## SYSTEM DIAGRAM



980508EBA2'

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## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	$V_{CC}$	-0.5~7	V
DC Input Voltage	$V_{IN}$	-0.5~ $V_{CC} + 0.5$	V
DC Output Voltage	$V_{OUT}$	-0.5~ $V_{CC} + 0.5$	V
Input Diode Current	$I_{IK}$	$\pm 20$	mA
Output Diode Current	$I_{OK}$	$\pm 20$	mA
DC Output Current	$I_{OUT}$	$\pm 25$	mA
DC $V_{CC}$ / Ground Current	$I_{CC}$	$\pm 50$	mA
Power Dissipation	$P_D$	500 (DIP)* / 180 (SOP)	mW
Storage Temperature	$T_{STG}$	-65~150	°C

\*500mW in the range of  $T_a = -40^{\circ}\text{C} \sim 65^{\circ}\text{C}$ . From  $T_a = 65^{\circ}\text{C}$  to  $85^{\circ}\text{C}$  a derating factor of  $-10\text{mW}/^{\circ}\text{C}$  shall be applied until 300mW.

## RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	$V_{CC}$	2~6	V
Input Voltage	$V_{IN}$	0~ $V_{CC}$	V
Output Voltage	$V_{OUT}$	0~ $V_{CC}$	V
Operating Temperature	$T_{opr}$	-40~85	°C
Input Rise and Fall Time	$t_r, t_f$	0~1000 ( $V_{CC} = 2.0\text{V}$ ) 0~500 ( $V_{CC} = 4.5\text{V}$ ) 0~400 ( $V_{CC} = 6.0\text{V}$ )	ns

## DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION	$V_{CC}$ (V)	Ta = 25°C			Ta = -40~85°C		UNIT
				MIN.	TYP.	MAX.	MIN.	MAX.	
High - Level Input Voltage	$V_{IH}$		2.0	1.50	—	—	1.50	—	V
			4.5	3.15	—	—	3.15	—	
			6.0	4.20	—	—	4.20	—	
Low - Level Input Voltage	$V_{IL}$		2.0	—	—	0.50	—	0.50	V
			4.5	—	—	1.35	—	1.35	
			6.0	—	—	1.80	—	1.80	
High - Level Output Voltage	$V_{OH}$	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OH} = -20\mu\text{A}$	2.0	1.9	2.0	—	1.9	V
			$I_{OH} = -4\text{ mA}$	4.5	4.4	4.5	—	4.4	
			$I_{OH} = -5.2\text{ mA}$	6.0	5.9	6.0	—	5.9	
				4.5	4.18	4.31	—	4.13	
Low - Level Output Voltage	$V_{OL}$	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 20\mu\text{A}$	6.0	5.68	5.80	—	5.63	V
			$I_{OL} = 4\text{ mA}$	2.0	—	0.0	0.1	—	
			$I_{OL} = 5.2\text{ mA}$	4.5	—	0.17	0.26	—	
				6.0	—	0.18	0.26	—	
Input Leakage Current	$I_{IN}$	$V_{IN} = V_{CC}$ or GND	6.0	—	—	$\pm 0.1$	—	$\pm 1.0$	$\mu\text{A}$
Quiescent Supply Current	$I_{CC}$	$V_{IN} = V_{CC}$ or GND	6.0	—	—	4.0	—	40.0	

AC ELECTRICAL CHARACTERISTICS ( $C_L = 15\text{pF}$ ,  $V_{CC} = 5\text{V}$ ,  $T_a = 25^\circ\text{C}$ , Input  $t_r = t_f = 6\text{ns}$ )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Transition Time	$t_{TLH}$ $t_{THL}$		—	4	8	ns
Propagation Delay Time (D-Y)	$t_{pLH}$ $t_{pHL}$		—	15	24	
Propagation Delay Time (D-W)	$t_{pLH}$ $t_{pHL}$		—	15	24	
Propagation Delay Time ( $\bar{S}\bar{T}-Y$ )	$t_{pLH}$ $t_{pHL}$		—	10	17	
Propagation Delay Time ( $\bar{S}\bar{T}-W$ )	$t_{pLH}$ $t_{pHL}$		—	10	17	
Propagation Delay Time (A, B, C-Y)	$t_{pLH}$ $t_{pHL}$		—	19	31	
Propagation Delay Time (A, B, C-W)	$t_{pLH}$ $t_{pHL}$		—	19	31	

AC ELECTRICAL CHARACTERISTICS ( $C_L = 50\text{pF}$ , Input  $t_r = t_f = 6\text{ns}$ )

PARAMETER	SYMBOL	TEST CONDITION	Ta = 25°C			Ta = -40~85°C		UNIT
			$V_{CC}$ (V)	MIN.	TYP.	MAX.	MIN.	
Output Transition Time	$t_{TLH}$ $t_{THL}$		2.0	—	30	75	—	95
			4.5	—	8	15	—	19
			6.0	—	7	13	—	16
Propagation Delay Time (D-Y)	$t_{pLH}$ $t_{pHL}$		2.0	—	65	140	—	175
			4.5	—	18	28	—	35
			6.0	—	15	24	—	30
Propagation Delay Time (D-W)	$t_{pLH}$ $t_{pHL}$		2.0	—	65	140	—	175
			4.5	—	18	28	—	35
			6.0	—	15	24	—	30
Propagation Delay Time ( $\bar{S}\bar{T}-Y$ )	$t_{pLH}$ $t_{pHL}$		2.0	—	36	100	—	125
			4.5	—	12	20	—	25
			6.0	—	10	17	—	21
Propagation Delay Time ( $\bar{S}\bar{T}-W$ )	$t_{pLH}$ $t_{pHL}$		2.0	—	36	100	—	125
			4.5	—	12	20	—	25
			6.0	—	10	17	—	21
Propagation Delay Time (A, B, C-Y)	$t_{pLH}$ $t_{pHL}$		2.0	—	80	180	—	225
			4.5	—	23	36	—	45
			6.0	—	19	31	—	38
Propagation Delay Time (A, B, C-W)	$t_{pLH}$ $t_{pHL}$		2.0	—	80	180	—	225
			4.5	—	23	36	—	45
			6.0	—	19	31	—	38
Input Capacitance	$C_{IN}$		—	5	10	—	10	pF
Power Dissipation Capacitance	$C_{PD}(1)$		—	69	—	—	—	

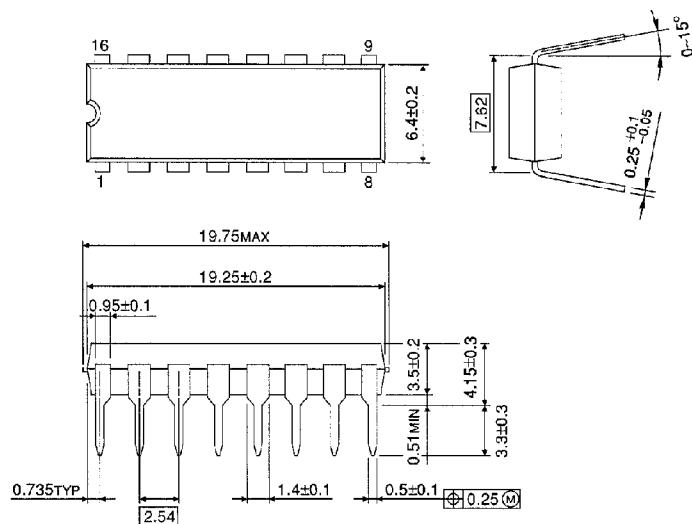
Note(1)  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation :

$$I_{CC(\text{opr})} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

## DIP 16PIN OUTLINE DRAWING (DIP16-P-300-2.54A )

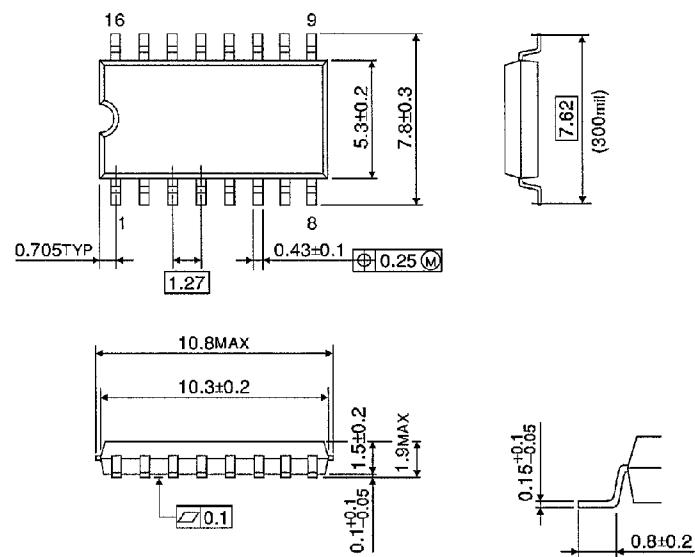
Unit in mm



Weight : 1.00g (Typ.)

## SOP 16PIN ( 200mil BODY ) OUTLINE DRAWING ( SOP16-P-300-1.27 )

Unit in mm

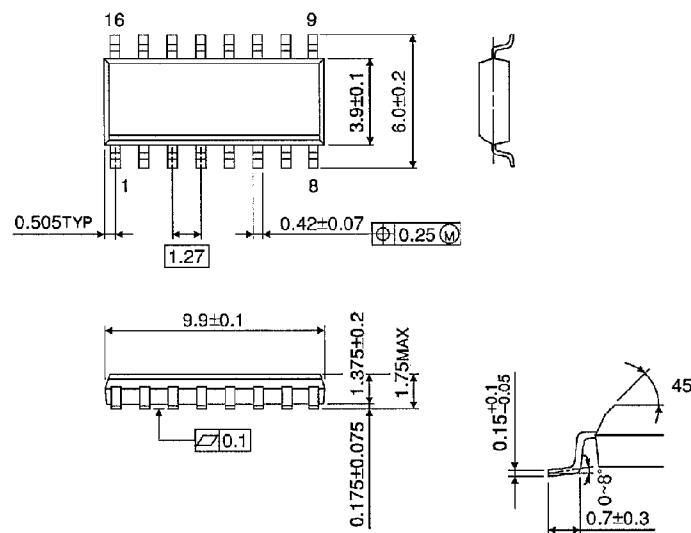


Weight : 0.18g (Typ.)

## SOP 16PIN (150mil BODY) OUTLINE DRAWING (SOL16-P-150-1.27)

Unit in mm

(Note) This package is not available in Japan.



Weight : 0.13g (Typ.)