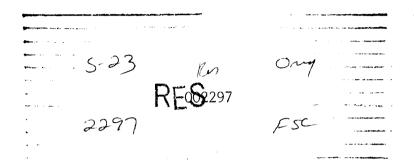


9534 QUAD LATCH WITH INPUT AND OUTPUT ENABLES

9538 OCTAL DECODE-3 LINE/8 LINE



Dearly 68 PKg

t-75 Kerfse

DESCRIPTION The 9534 quad D latch will store four bits of information simultaneously. Two Input enable inputs and a common output enable allow maximum logic flexibility. A common Select input selects 'D' type or 'Set' type of operation. A common reset clears the device so that the 1's catching feature may be used when desired.

This element is designed as a storage buffer for high speed registers in arithmetic logic units and parallel-serial conversion in communication systems.

- HIGH SPEED . . . 4.3 ns TYPICAL DATA DELAYS'S
- COMMON LATCH ENABLE
- COMMON MASTER RESET
- . COMMON SELECT FOR 'D' OR 'SET'
- COMPLEX MULTIGATE CHIP REDUCES PACKAGE COUNT
- EASILY EXPANDED TO LARGE HIGH SPEED MEMORY
- WIRED-OR CAPABILITY
- SEPARATE CURRENT SWITCH AND EMITTER FOLLOWER V_{CC} PINS — ELIMINATES NOISE COUPLING
- TEMPERATURE COMPENSATION
- INTERNAL PULLDOWN
- 50 Ω LINE DRIVE CAPABILITY
- SINGLE -5.2 VOLT POWER SUPPLY
- HERMETIC CERAMIC 16 PIN DIP

DESCRIPTION The 9538 decoder accepts three binary address inputs and under control of the enables activate one of the eight active low outputs. Both enables must be low for any output to go low.

The ECL 9538 may be used as a demultiplexer by connecting a data source to one of the enable inputs. The other enable input will function as a data enable line while inputs A_0 , A_1 , and A_2 select the desired data output line (0 through 7).

The 9538 is particularly useful in memory expansion and register or peripheral selection applications.

- HIGH SPEED . . . 4 ns FROM ADDRESS TO OUTPUT
- USEABLE FOR DEMULTIPLEXING 2 ENABLE INPUTS
- OUTPUTS ACTIVE LOW FOR ENABLING WITH OTHER MEMBERS OF THE 9500 FAMILY
- NO INVERSION FROM ENABLE TO SELECTED OUTPUT
- WIRED-OR CAPABILITY
- SEPARATE CURRENT SWITCH EMITTER FOLLOWER
 V_{CC} PINS ELIMINATE NOISE COUPLING
- TEMPERATURE COMPENSATION
- INTERNAL PULL DOWN
- 50 Ω LINE DRIVE CAPABILITY
- SINGLE -5.2 VOLT POWER SUPPLY
- HERMETIC CERAMIC 16 PIN DIP
- COMPLEX MULTI-GATE CHIP REDUCES PACKAGE COUNT & POWER

