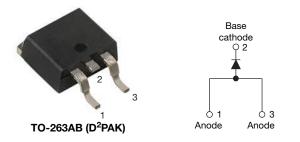
Vishay Semiconductors

High Voltage Surface Mount Input Rectifier Diode, 25 A



PRODUCT SUMMARY							
Package	TO-263AB (D ² PAK)						
I _{F(AV)}	25 A						
V _R	800 V, 1000 V, 1200 V						
V _F at I _F	1.14 V						
I _{FSM}	300 A						
T _j max.	150 °C						
Diode variation	Single die						

FEATURES

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Glass passivated pellet chip junction
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Input rectification
- Vishay switches and output rectifiers which are available in identical package outlines

DESCRIPTION

The VS-25ETS..SPbF rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 $^{\circ}$ C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS							
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS				
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	20	23	А				

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Sinusoidal waveform	25	А						
V _{RRM}		800 to 1200	V						
I _{FSM}		300	A						
V _F	10 A, T _J = 25 °C	1.0	V						
TJ		-40 to +150	°C						

VOLTAGE RATINGS										
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA							
VS-25ETS08SPbF	800	900								
VS-25ETS10SPbF	1000	1100	1							
VS-25ETS12SPbF	1200	1300								

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VS-25ETS..SPbF Series



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ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum average forward current	I _{F(AV)}	T_C = 106 °C, 180° conduction half sine wave	25				
Maximum peak one cycle	I _{FSM}	10 ms sine pulse, rated V_{RRM} applied	250	А			
non-repetitive surge current		10 ms sine pulse, no voltage reapplied	300				
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	316	A ² s			
Maximum I-t for fusing	1-1	10 ms sine pulse, no voltage reapplied 442		A-5			
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	4420	A²√s			

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CC	ONDITIONS	VALUES	UNITS			
Maximum forward voltage drop	V _{FM}	25 A, T _J = 25 °C	1.14	V				
Forward slope resistance	r _t	T _{.1} = 150 °C	9.62	mΩ				
Threshold voltage	V _{F(TO)}	1j=130 0	0.87	V				
Maximum reverse leakage current		T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm BBM}$	0.1	mA			
Maximum reverse leakage current	IRM	T _J = 150 °C	V _R = naleu V _{RRM}	1.0	ША			

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-40 to +150	°C			
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.9				
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5				
Approvimente weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf · cm			
Mounting torque maximum				12 (10)	(lb̃f ⋅ in)			
Marking device				25ET	S08S			
			Case style TO-263AB (D ² PAK)	25ET	S10S			
				25ET	25ETS12S			



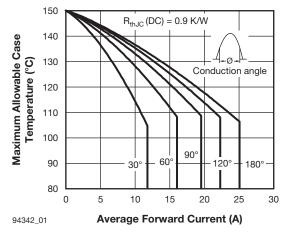


Fig. 1 - Current Rating Characteristics

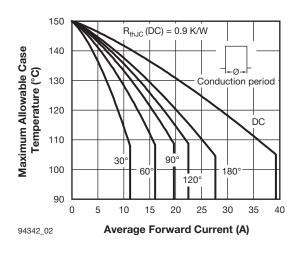


Fig. 2 - Current Rating Characteristics

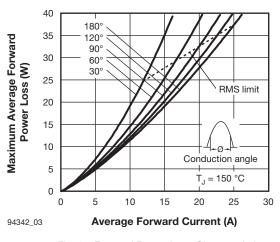


Fig. 3 - Forward Power Loss Characteristics

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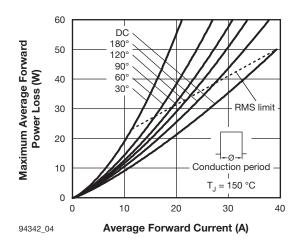
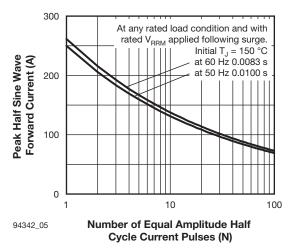


Fig. 4 - Forward Power Loss Characteristics





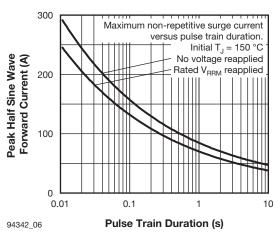


Fig. 6 - Maximum Non-Repetitive Surge Current

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VS-25ETS..SPbF Series

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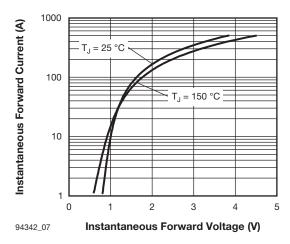


Fig. 7 - Forward Voltage Drop Characteristics

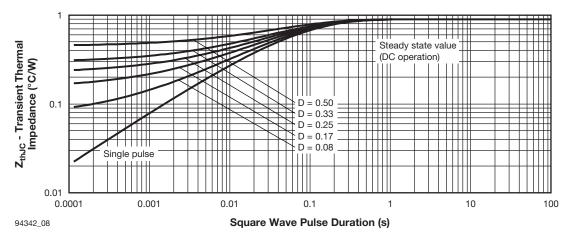


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

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ORDERING INFORMATION TABLE											
Device code	VS-	25	E	т	S	12	S	TRL	PbF		
		2	3	4	5	6	7	8	9		
	 Vishay Semiconductors product Current rating (25 = 25 A) Circuit configuration E = single diode Package: T = TO-220AC Type of silicon: 										
	6 - 7 - 8 - 9 -	Voli S = • No • TF • TF	tage coo TO-220 one = tu RL = tap RR = tap	lard reco de x 100) D ² PAK be be and re be and re (Pb)-fre	= V _{RRN} (SMD- eel (left eel (righ	1 220) ve oriented	1)	10 = 1	300 V 000 V 200 V		

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-25ETS08SPbF	50	1000	Antistatic plastic tube						
VS-25ETS08STRRPbF	800	800	13" diameter reel						
VS-25ETS08STRLPbF	800	800	13" diameter reel						
VS-25ETS10SPbF	50	1000	Antistatic plastic tube						
VS-25ETS10STRRPbF	800	800	13" diameter reel						
VS-25ETS10STRLPbF	800	800	13" diameter reel						
VS-25ETS12SPbF	50	1000	Antistatic plastic tube						
VS-25ETS12STRRPbF	800	800	13" diameter reel						
VS-25ETS12STRLPbF	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95046					
Part marking information	www.vishay.com/doc?95054					
Packaging information	www.vishay.com/doc?95032					

Semiconductors

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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