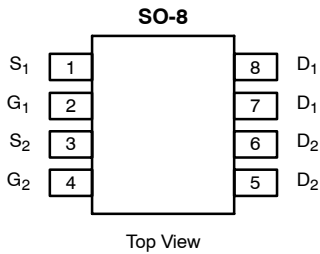




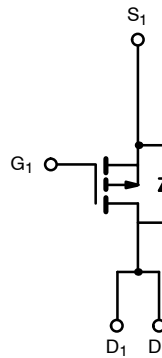
## Dual P-Channel 1.8-V (G-S) MOSFET

**TrenchFET<sup>®</sup>**  
Power MOSFETs  
1.8-V Rated

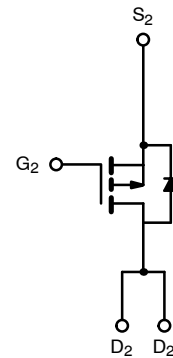
PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
-12	0.023 @ $V_{GS} = -4.5$ V	-7.5
	0.030 @ $V_{GS} = -2.5$ V	-6.7
	0.045 @ $V_{GS} = -1.8$ V	-5.4



Ordering Information: Si4967DY  
Si4967DY-T1 (with Tape and Reel)



P-Channel MOSFET



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-12	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a, b</sup>	$T_A = 25^\circ\text{C}$	-7.5	A
	$T_A = 70^\circ\text{C}$	-6.1	
Pulsed Drain Current	$I_{DM}$	-30	
Continuous Source Current (Diode Conduction) <sup>a, b</sup>	$I_S$	-1.7	
Maximum Power Dissipation <sup>a, b</sup>	$T_A = 25^\circ\text{C}$	2.0	W
	$T_A = 70^\circ\text{C}$	1.3	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ sec		62.5	$^\circ\text{C/W}$	
	Steady State	93			

Notes  
a. Surface Mounted on FR4 Board.  
b.  $t \leq 10$  sec.

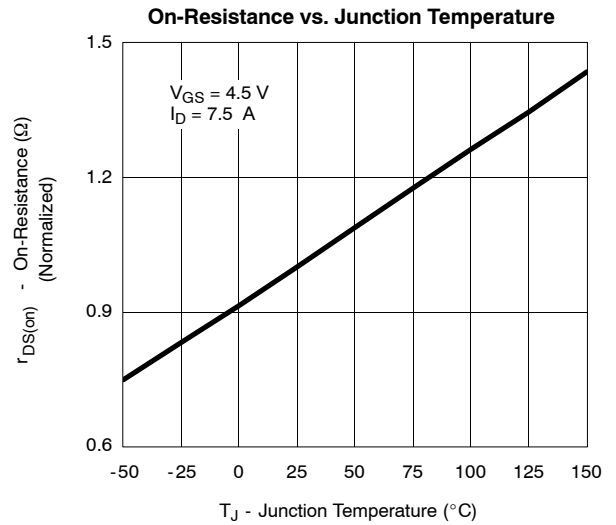
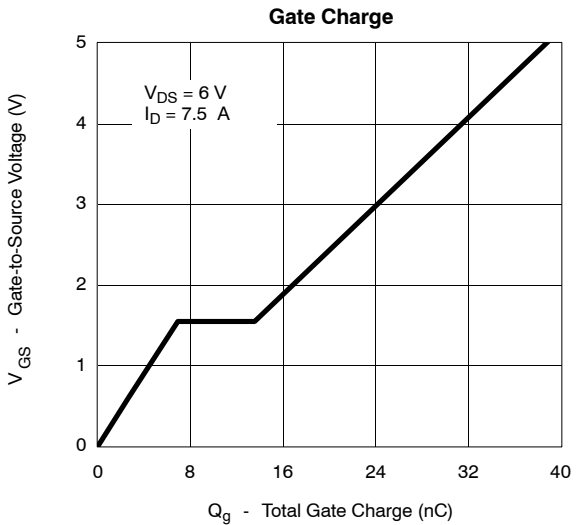
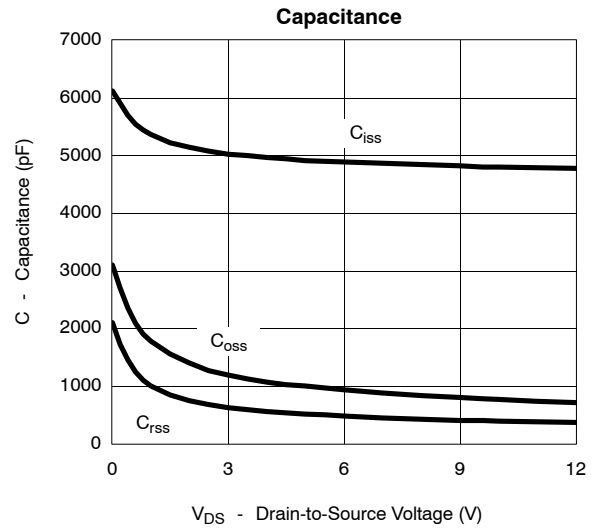
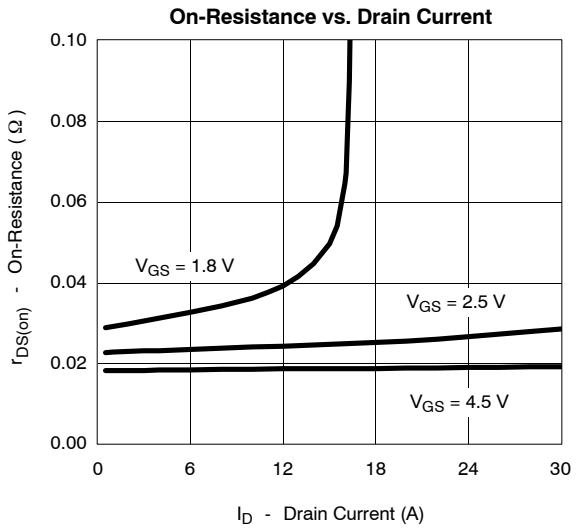
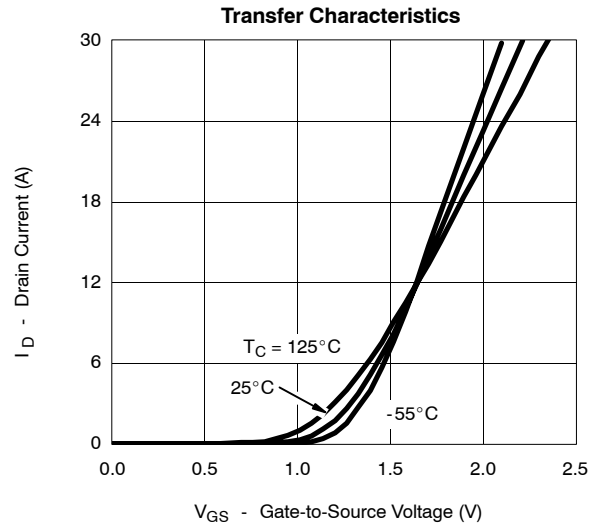
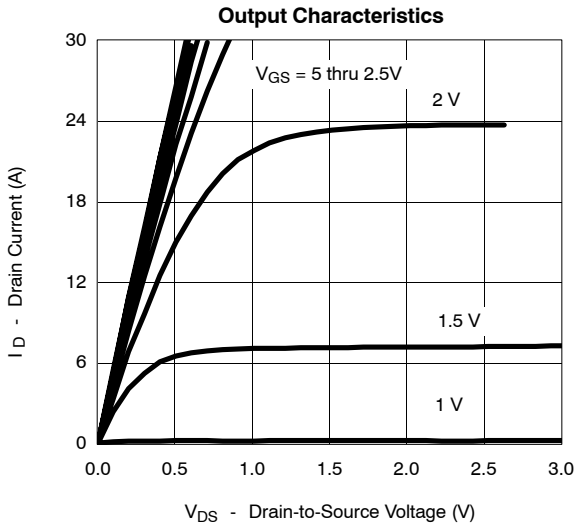
SPECIFICATIONS (T <sub>J</sub> = 25°C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-0.45			V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±8 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -12 V, V <sub>GS</sub> = 0 V			-1	μA
		V <sub>DS</sub> = -12 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70°C			-5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ -5 V, V <sub>GS</sub> = -4.5 V	-20			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -7.5 A		0.019	0.023	Ω
		V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -6.7 A		0.024	0.030	
		V <sub>GS</sub> = -1.8 V, I <sub>D</sub> = -5.4 A		0.033	0.045	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -7.5 A		27		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = -1.7 A, V <sub>GS</sub> = 0 V		0.7	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -6 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -7.5 A		35	55	nC
Gate-Source Charge	Q <sub>gs</sub>			7		
Gate-Drain Charge	Q <sub>gd</sub>			7		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -6 V, R <sub>L</sub> = 10 Ω I <sub>D</sub> ≅ -1 A, V <sub>GEN</sub> = -10 V, R <sub>G</sub> = 6 Ω		25	50	ns
Rise Time	t <sub>r</sub>			40	80	
Turn-Off Delay Time	t <sub>d(off)</sub>			210	350	
Fall Time	t <sub>f</sub>			95	150	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>		I <sub>F</sub> = -1.7 A, di/dt = 100 A/μs		50	

## Notes

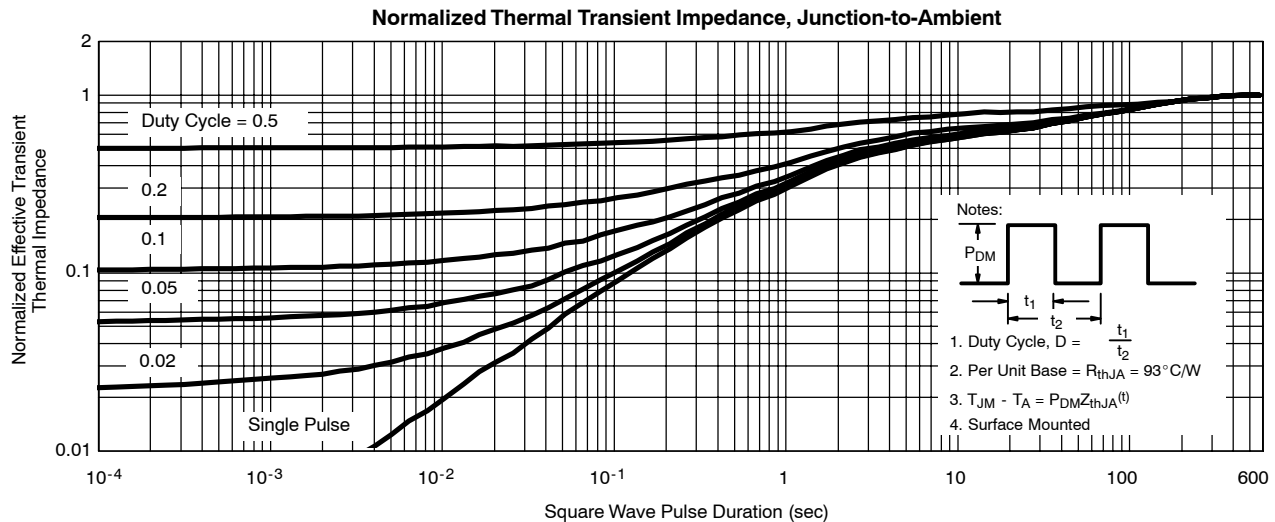
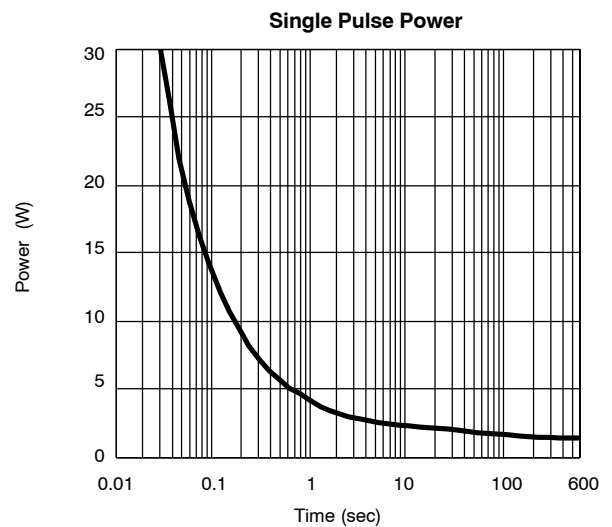
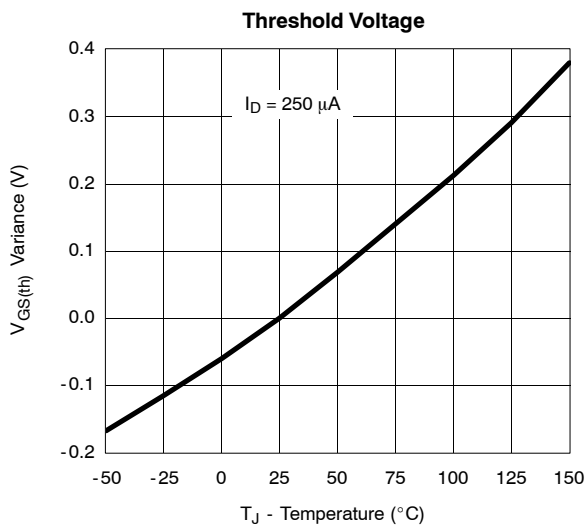
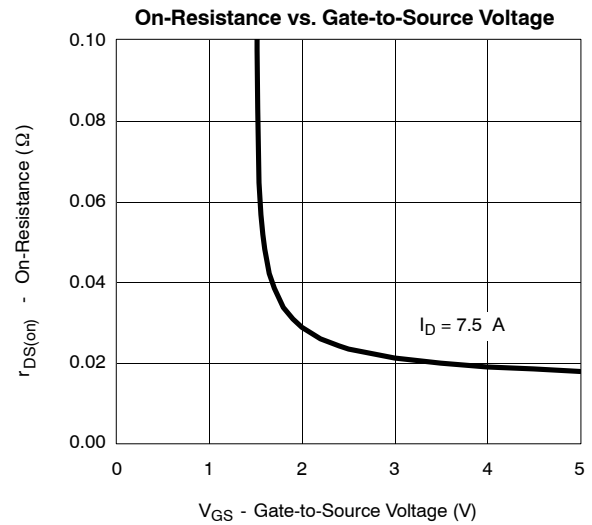
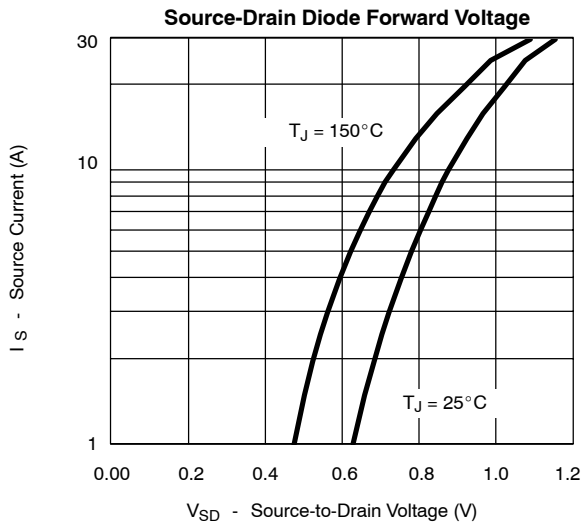
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.  
b. Guaranteed by design, not subject to production testing.



**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**



**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**





## Disclaimer

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